plerow™ ALN1810

Internally Matched LNA Module

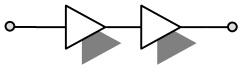
Features

- 30 dB Gain at 1810 MHz
- 19 dBm P1dB
- 37 dBm Output IP3
- 0.9 dB Noise Figure
- Operating at Single 5 V Supply
- 80 mA Current Consumption

Description

The plerow™ ALN-series is the compactly designed surface-mount module for the use of the LNA with or without the following gain blocks in the infrastructure equipment of the mobile wireless (CDMA, GSM, PCS, PHS, WCDMA, DMB, WLAN, WiBro, WiMAX), GPS, satellite communication terminals, CATV and so on. It has an exceptional performance of low noise figure, high gain, high OIP3, and low bias current. The stability factor is always kept more than unity over the application band in order to ensure its unconditionally stable implementation to the application system environment. The surface-mount module package including the completed matching circuit and other components necessary just in case allows very simple and convenient implementation onto the system board in mass production level.

RoHS-compliant



2-stage Single Type

Specifications (in Production)

Typ. @ T = 25°C, V_s = 5 V, Freq. =1810 MHz, $Z_{o.sys}$ = 50 ohm

Parameter	Unit	Specifications		
		Min	Typ	Max
Frequency Range	MHz	1750		1870
Gain	dB	29	30	
Gain Flatness	dB		± 0.5	± 0.7
Noise Figure	dB		0.9	0.95
Output IP3 (1)	dBm	36	37	
S11 / S22 (2)	dB			-14 / -14
Output P1dB	dBm	18	19	
Switching Time (3)	μsec		-	
Supply Current	mA		80	100
Supply Voltage	V		5	
Impedance	Ω		50	
Max. RF Input Power	dBm		C.W 29 ~ 31 (before fail)	
Package Type & Size	mm		Surface Mount Type, 13Wx13Lx3.8H	

Operating temperature is -40°C to +85°C.
1) OIP3 is measured with two tones at an output power of 10 dBm / tone separated by 1 MHz.
2) S11/S22 (max) is the worst value within the frequency band.
3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_S.

More Information

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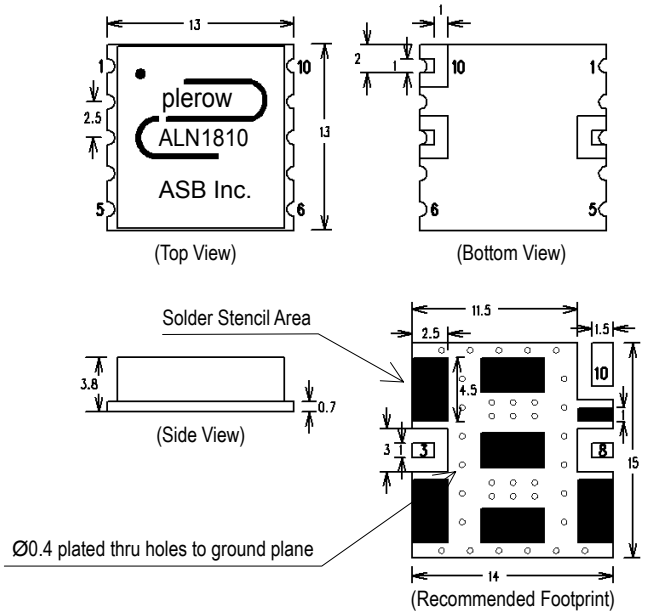
Outline Drawing (Unit: mm)



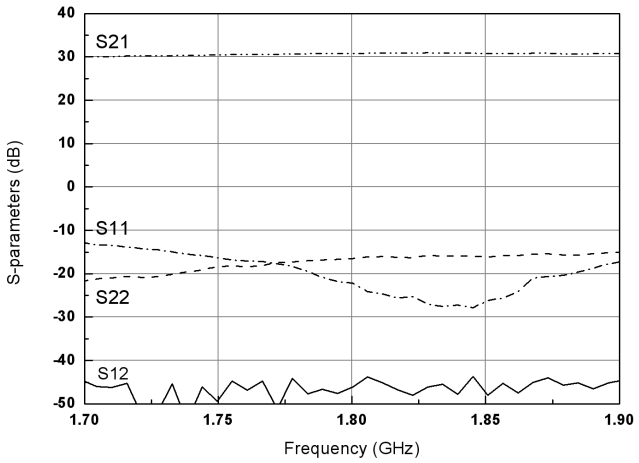
Pin Number	Function
3	RF In
8	RF Out
10	+Vcc
Others	Ground

Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.
2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

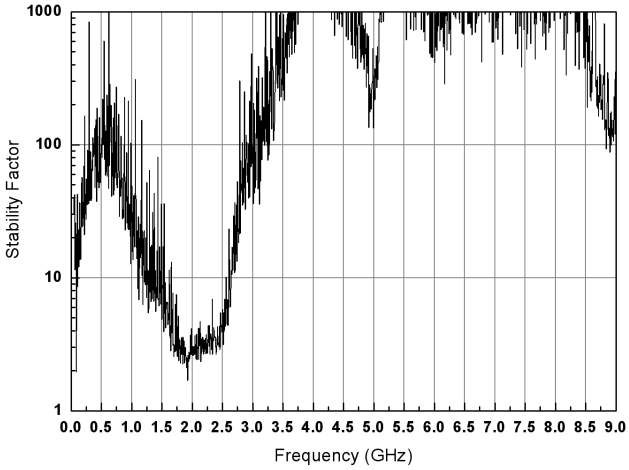
Typical Performance (Measured)

1750~1870
+5 V

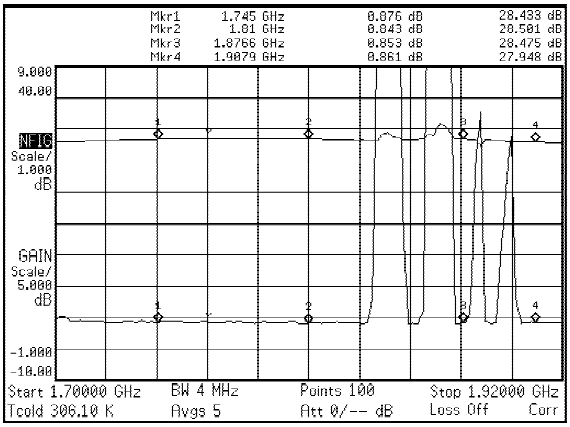
S-parameters



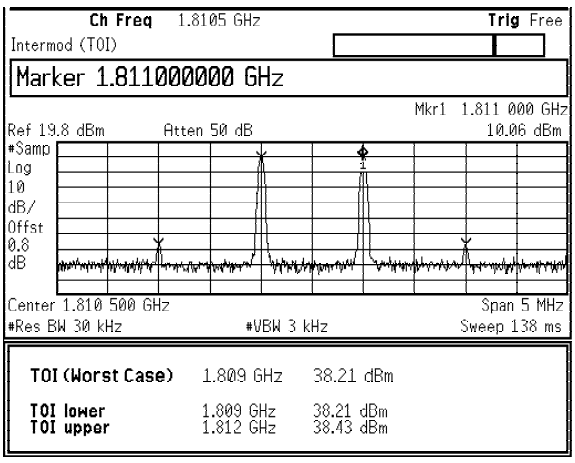
Stability Factor (K)



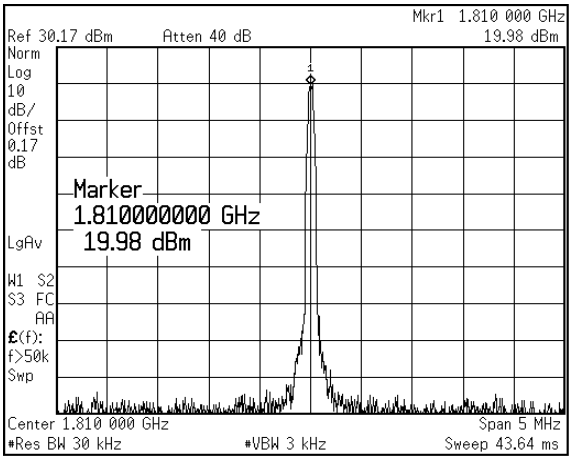
Noise Figure



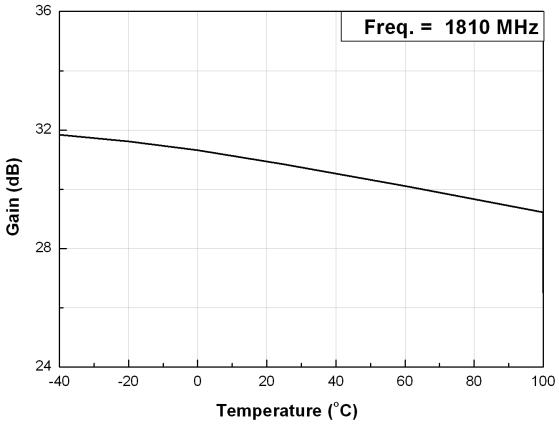
OIP3

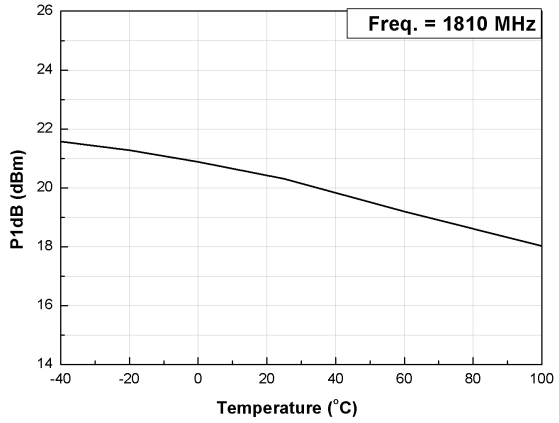


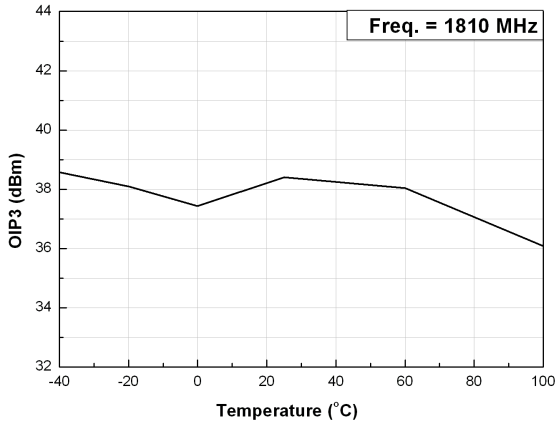
P1dB



Gain, P1dB, and OIP3 with Temperature (-40℃ ~ 100℃)

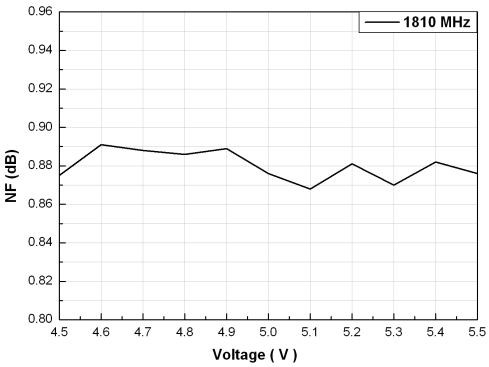


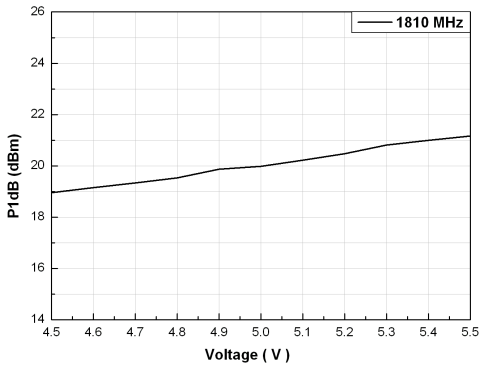


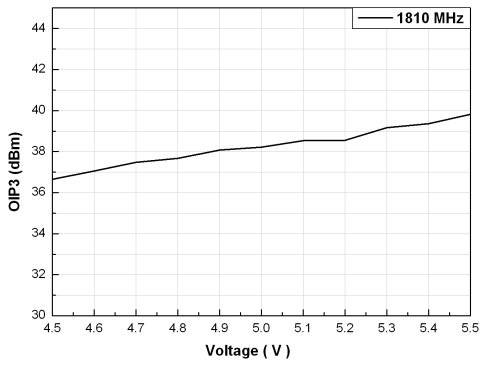


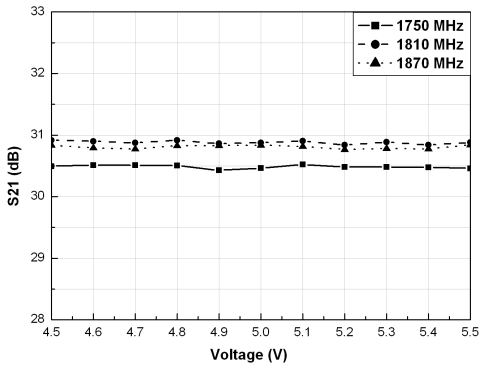
NF, P1dB, OIP3, and S-parameters with Voltage Change (4.5 V ~ 5.5 V)

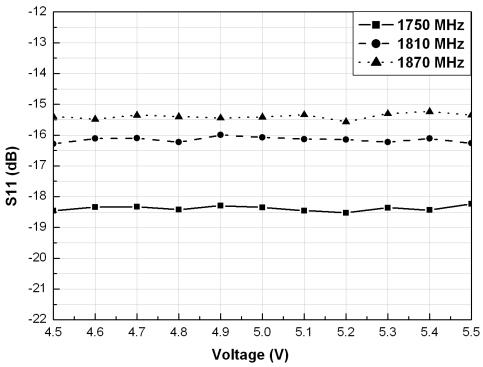
	Voltage (V)	Current (mA)	S21 (dB)			S11 (dB)			S22 (dB)			P1dB (dBm)	OIP3 (dBm)	NF (dB)
			1750MHz	1810 MHz	1870 MHz	1750MHz	1810 MHz	1870 MHz	1750MHz	1810 MHz	1870 MHz			
	4.5	70	30.495	30.918	30.834	-18.463	-16.282	-15.395	-16.328	-23.844	-21.553	18.95	36.65	0.875
	4.6	72	30.511	30.899	30.79	-18.34	-16.116	-15.485	-16.268	-24.171	-21.761	19.15	37.05	0.891
	4.7	74	30.508	30.874	30.775	-18.326	-16.101	-15.354	-16.218	-24.015	-21.814	19.33	37.48	0.888
	4.8	76	30.505	30.918	30.826	-18.424	-16.233	-15.402	-16.329	-23.958	-21.525	19.53	37.68	0.886
	4.9	78	30.428	30.861	30.83	-18.296	-15.997	-15.445	-16.253	-24.036	-21.08	19.87	38.07	0.889
	5	80	30.461	30.875	30.836	-18.352	-16.074	-15.414	-16.335	-24.556	-20.844	19.98	38.21	0.876
	5.1	81	30.524	30.906	30.815	-18.453	-16.131	-15.341	-16.264	-24.64	-21.521	20.22	38.54	0.868
	5.2	84	30.483	30.836	30.766	-18.524	-16.148	-15.568	-16.356	-24.094	-21.656	20.47	38.55	0.881
	5.3	86	30.48	30.886	30.782	-18.36	-16.228	-15.303	-16.157	-23.797	-21.236	20.82	39.17	0.87
	5.4	88	30.478	30.841	30.774	-18.439	-16.115	-15.238	-16.125	-24.306	-21.705	21	39.36	0.882
	5.5	90	30.46	30.88	30.841	-18.238	-16.268	-15.348	-16.119	-24.049	-21.442	21.17	39.81	0.876
Variation	1	20	0.035	0.038	0.007	0.225	0.014	0.047	0.209	0.205	0.111	2.22	3.16	0.001

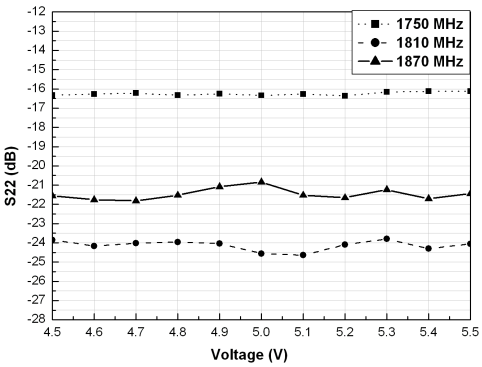




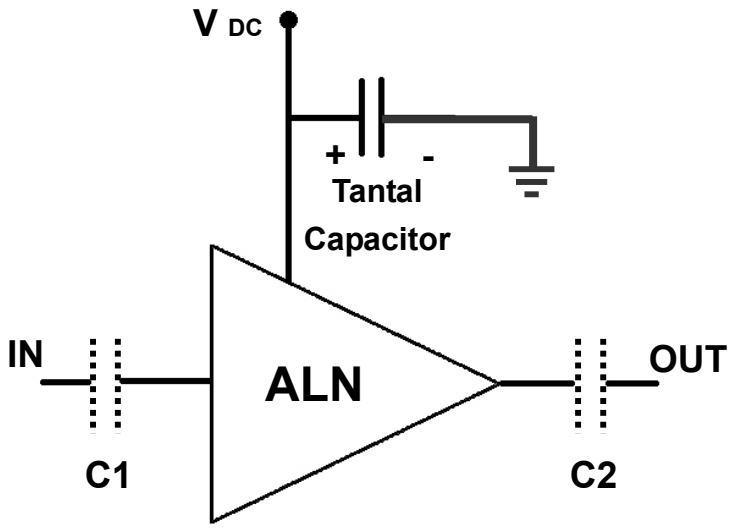






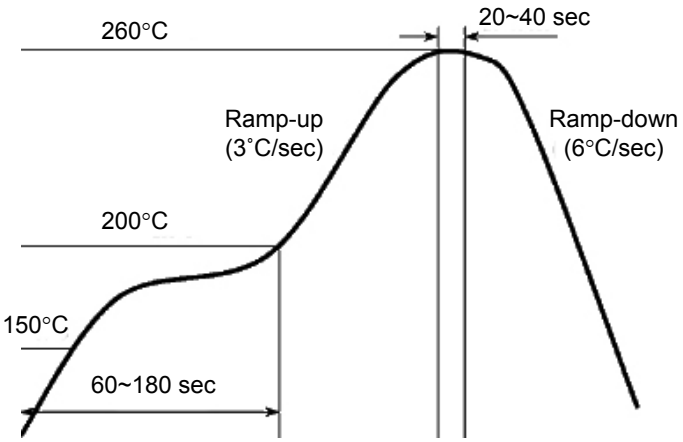


Application Circuit

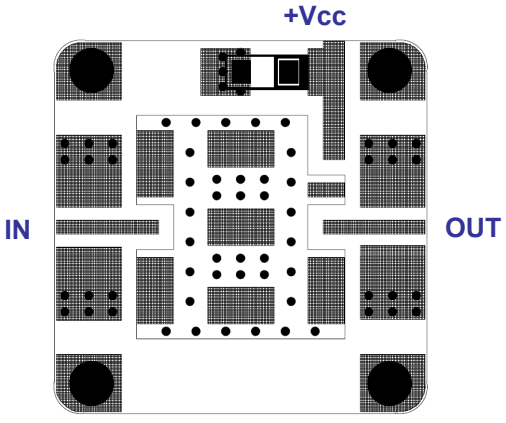


1) The tantal capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status.
2) So-called DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the LNA module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



Evaluation Board Layout



Size 25 x 25mm
(for ALN Series – 13x13mm)