

Internally Matched LNA Module

Features

- · S₂₁ = 36.6 dB @ 1710 MHz = 35.4 dB @ 1910 MHz
- · NF of 0.7 dB over Frequency
- · Unconditionally Stable
- · Single 5V Supply
- · High OIP3 @ Low Current

Description

The plerow™ ALN-series is the compactly designed surface-mount module for the use of the LNA with or without the following gain blocks in the infrastructure equipment of the mobile wireless (CDMA, GSM, PCS, PHS, WCDMA, DMB, WLAN, WiBro, WiMAX), GPS, satellite communication terminals, CATV and so on. It has an exceptional performance of low noise figure, high gain, high OIP3, and low bias current. The stability factor is always kept more than unity over the application band in order to ensure its unconditionally stable implementation to the application system environment. The surface-mount module package including the completed matching circuit and other components necessary just in case allows very simple and convenient implementation onto the system board in mass production level.







3-stage Single Type

Specifications (in Production)

Typ. @ T = 25°C, V_s = 5 V, Freq. = 1810 MHz, $Z_{o.sys}$ = 50 ohm

Parameter	Unit	Specifications			
Faiametei	Offic	Min	Тур	Max	
Frequency Range	MHz	1710		1910	
Gain	dB	35	36		
Gain Flatness	dB		± 0.6	± 0.8	
Noise Figure (NF)	dB		0.70	0.80	
Output IP3 (1)	dBm	41	42		
S11 / S22 ⁽²⁾	DB			-20 / -20	
Output P1dB	dBm	27	28		
Switching Time (3)	μsec		-		
Supply Current	mA		400	430	
Supply Voltage	V		5		
Impedance	Ω	50			
Max. RF Input Power	dBm	C.W 29 ~ 31 (before fail)			
Package Type & Size	mm	Surface Mount Type, 25Wx13Lx3.8H			

More Information

Website: www.asb.co.kr E-mail: sales@asb.co.kr

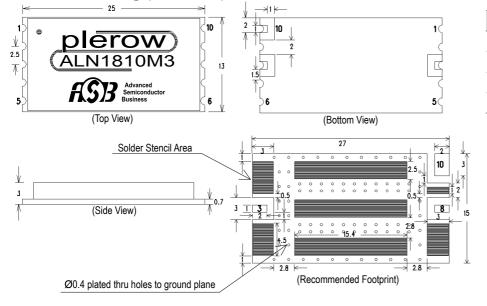
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Operating temperature is -40°C to +85°C.

- 1) OIP3 is measured with two tones at an output power of 10 dBm / tone separated by 1 MHz.
 2) S11/S22 (max) is the worst value within the frequency band.
 3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_S.

Outline Drawing (Unit: mm)



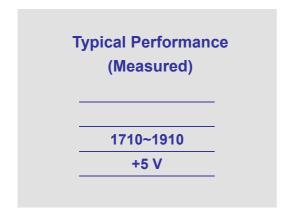
Pin Number	Function		
3	RF In		
8	RF Out		
10	+Vcc		
Others	Ground		

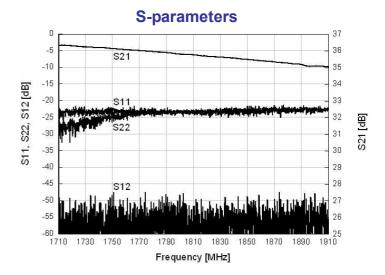
Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.

2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

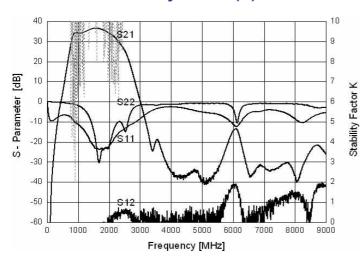


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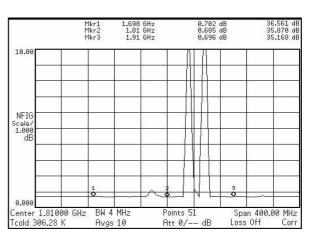




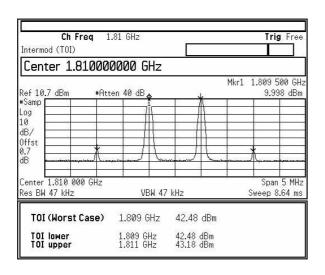
Stability Factor (K)



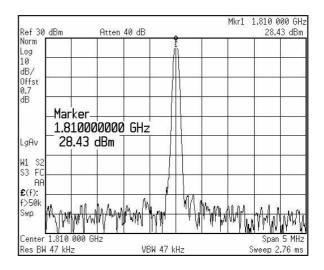
Noise Figure



OIP3



P1dB





RF Performance with Voltage Change

Item Voltage	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	NF (dB)	P1dB (dBm)	OIP3 (dBm)	Current (mA)
4.5	-20.4	-25.1	35.18	1.24	0.692	26.4	40.2	267
4.6	-21.1	-24.2	35.37	1.25	0.694	26.9	41.2	292
4.7	-21.4	-24.2	35.49	1.26	0.692	27.2	41.8	313
4.8	-22.1	-24.0	35.56	1.27	0.693	27.6	42.0	336
4.9	-22.3	-23.9	35.63	12.8	0.699	27.9	42.4	356
5.0	-22.7	-23.8	35.67	1.29	0.699	28.1	42.5	376
5.1	-22.9	-23.7	35.70	1.29	0.704	28.4	42.4	395
5.2	-23.2	-23.6	35.75	1.30	0.709	28.5	42.4	412
5.3	-23.5	-23.6	35.73	1.29	0.716	28.7	42.4	427
5.4	-23.6	-23.5	35.70	1.29	0.720	28.8	42.3	442
5.5	-23.7	-23.4	35.66	1.29	0.726	28.9	42.2	456

Note: tested at 25°C.

S11, S22, NF are the worst value over frequency, 1710~1910 MHz.

S21, P1dB, and OIP3 are measured at the center frequency, 1810 MHz.

RF Performance with Operating Temperature

Item Temp.	S11 (dB)	S22 (dB)	S21 (dB)	G/F (dB)	NF (dB)	P1dB (dBm)	OIP3 (dBm)	Current (mA)
- 40°C	-22.9	-18.9	37.34	1.14	0.437	29.1	45.6	384
- 20°C	-23.4	-19.3	37.09	1.13	0.525	29.0	44.7	387
0°C	-23.7	-20.0	36.70	1.12	0.596	28.8	43.6	389
25°C	-23.9	-20.8	36.33	1.12	0.700	28.6	42.2	390
40°C	-24.0	-20.8	36.10	1.11	0.755	28.3	41.5	390
60°C	-23.6	-21.4	35.74	1.10	0.835	28.0	40.8	387
80°C	-23.7	-21.9	35.32	1.09	0.899	27.5	40.0	382
100°C	-23.5	-22.6	34.96	1.09	1.010	27.1	39.2	373

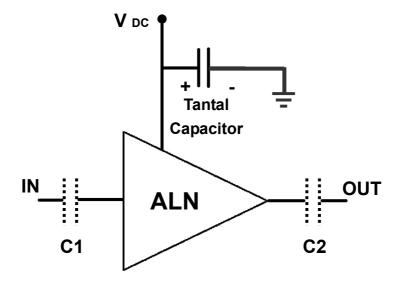
Note: tested at 5.0 V.

S11, S22, NF are the worst value over frequency, 1710~1910 MHz.

S21, P1dB, and OIP3 are measured at the center frequency, 1810 MHz.

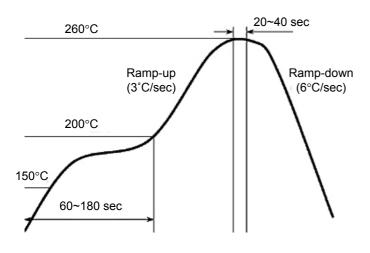


Application Circuit



- 1) The tantal capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status.
- 2) So-called DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the LNA module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



Evaluation Board Layout

