

## ● General Description

The AGM85P10A combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

## ● Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

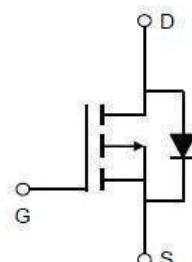
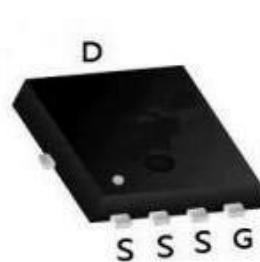
## ● Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

## Product Summary

BVDSS	RDS(on)	ID
-100V	86mΩ	-19A

## PDFN5\*6 Pin Configuration



## Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM85P10A	AGM85P10A	PDFN5*6	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	-19	A
	Drain Current-Continuous(Tc=100°C)	-13.5	A
IDM (pulse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	-76	A
PD	Maximum Power Dissipation(Tc=25°C)	66	w
	Maximum Power Dissipation(Tc=100°C)	26.3	w
EAS	Avalanche energy <b>(Note 3)</b>	156	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	20	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	1.9	°C/W

**Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)**

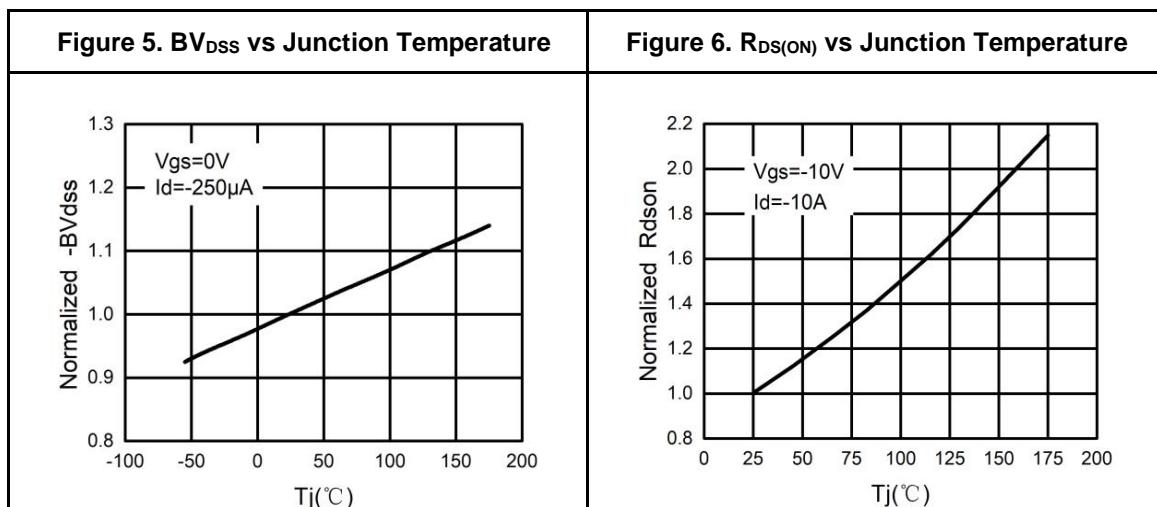
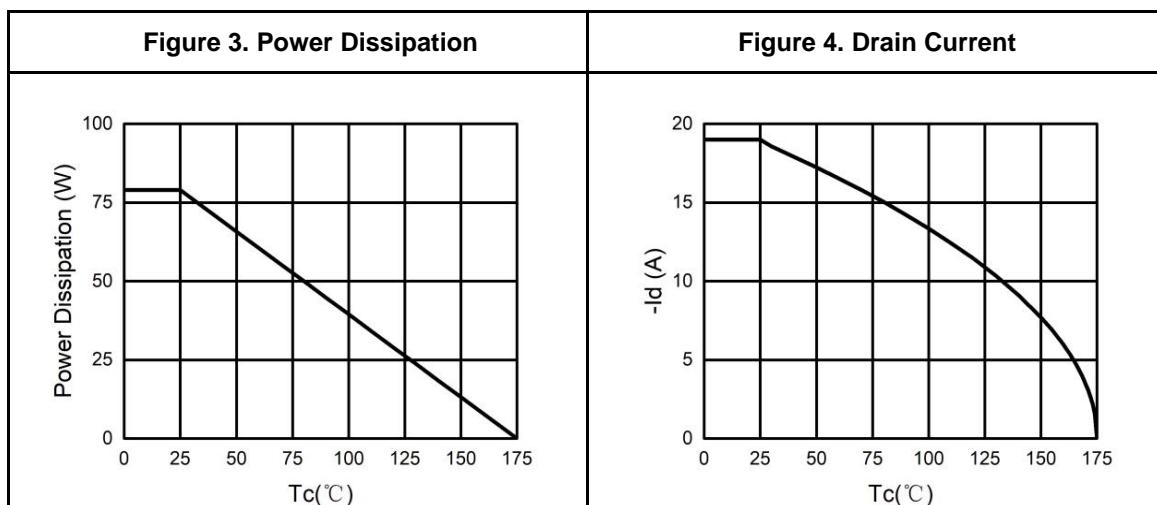
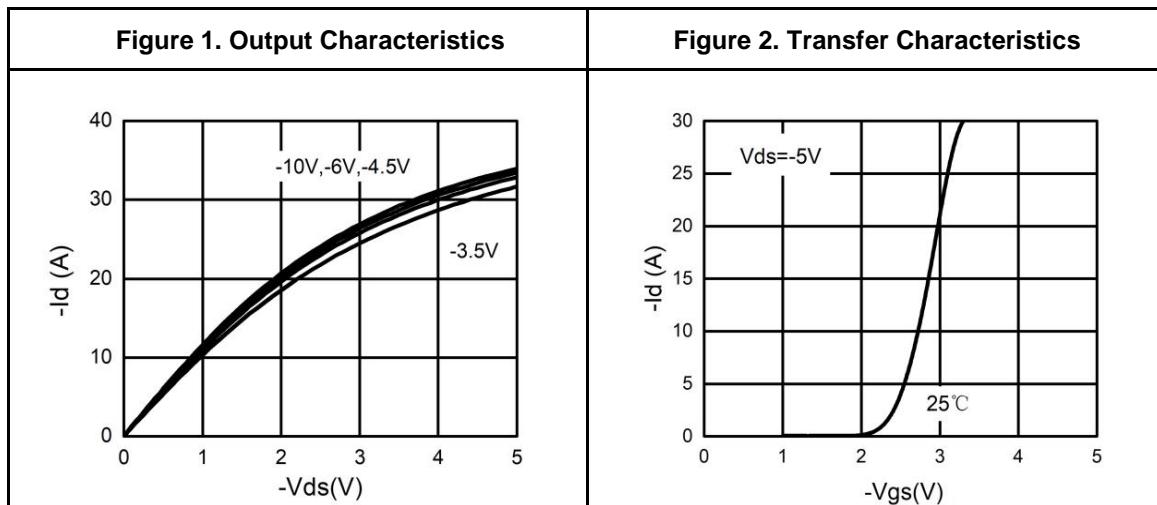
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-100	-121	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-100V, VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250μA	-1.0	-1.8	-2.5	V
gFS	Forward Transconductance	VDS=5V, ID=-8A	--	26	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-10A	--	86	103	mΩ
		VGS=-4.5V, ID=-8A	--	90	117	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=-25V, VGS=0V, F=1MHZ	--	3700	--	pF
Coss	Output Capacitance		--	90	--	pF
Crss	Reverse Transfer Capacitance		--	32	--	pF
Rg	Gate resistance	f=1.0MHz	--	--	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=-10V, VDS=-50V, RL=5Ω, RGEN=9.1Ω	--	6.0	--	ns
tr	Turn-on Rise Time		--	29	--	ns
td(off)	Turn-Off Delay Time		--	17	--	ns
tf	Turn-Off Fall Time		--	24	--	ns
Qg	Total Gate Charge	VGS=-10V, VDS=-50V, ID=-10A	--	72	--	nC
Qgs	Gate-Source Charge		--	8.4	--	nC
Qgd	Gate-Drain Charge		--	17.3	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	-19	A
VSD	Forward on Voltage	VGS=0V, IS=-10A	--	--	-1.2	V
trr	Reverse Recovery Time	Isd=-10A , di/dt=100A/μs , TJ=25°C	--	32	--	ns
Qrr	Reverse Recovery Charge		--	53	--	nc

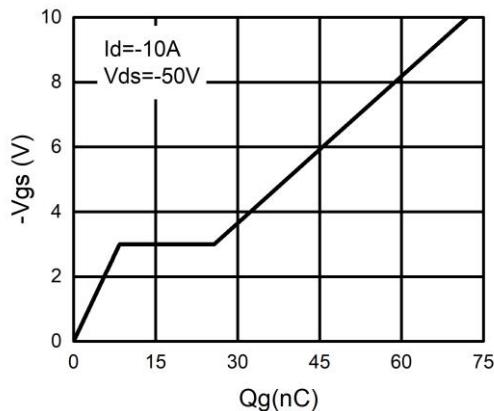
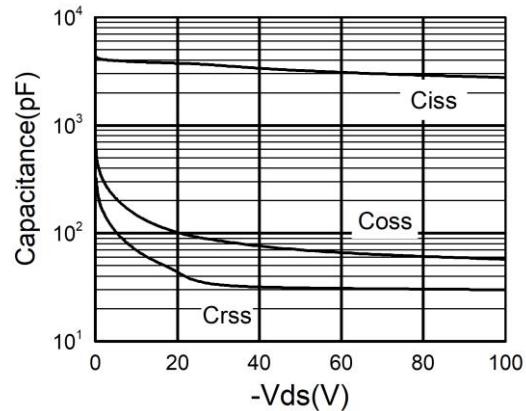
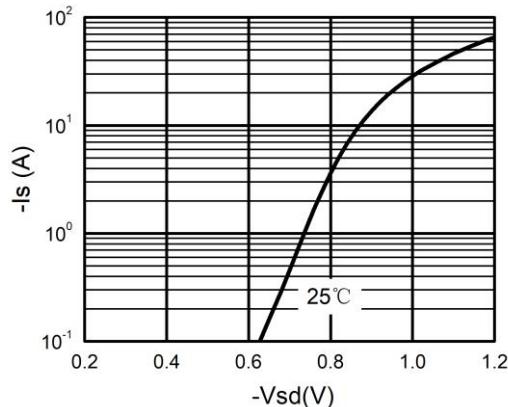
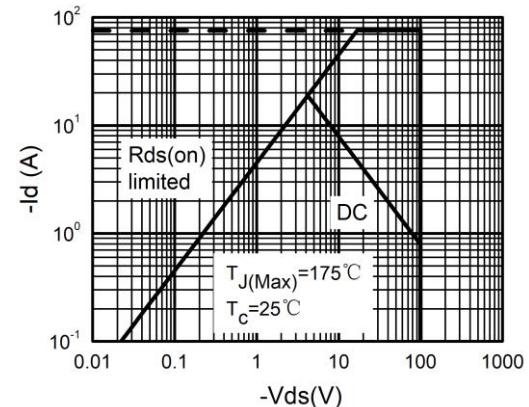
Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

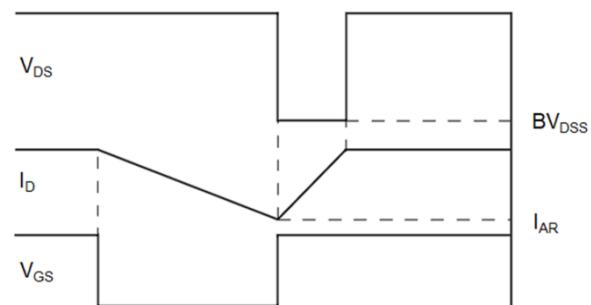
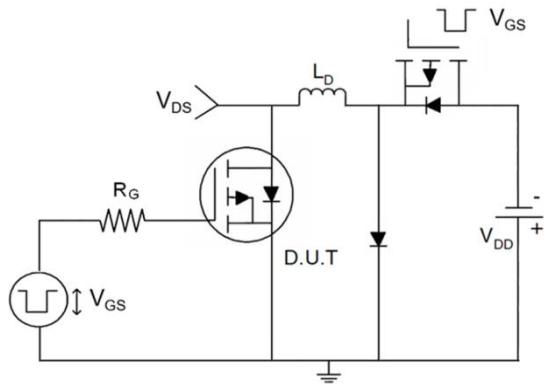
## Typical Electrical And Thermal Characteristics (Curves)



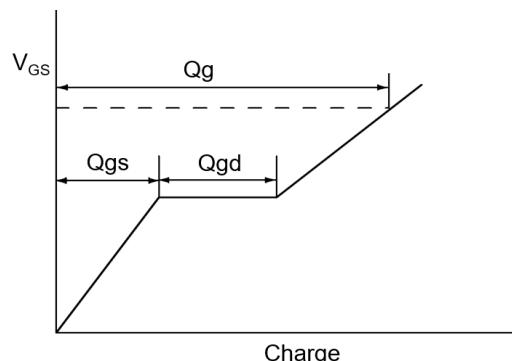
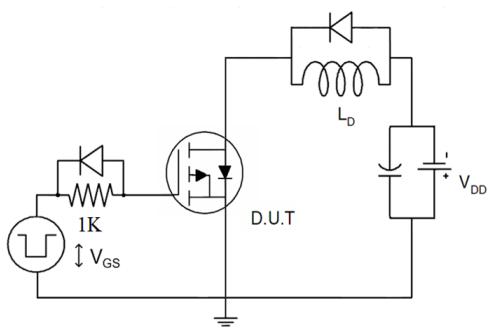
**Figure 7. Gate Charge Waveforms****Figure 8. Capacitance****Figure 9. Body-Diode Characteristics****Figure 10. Maximum Safe Operating Area**

## Test Circuit

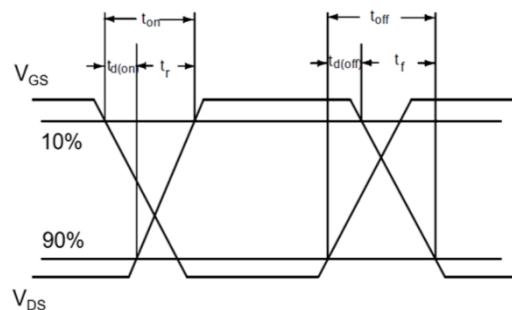
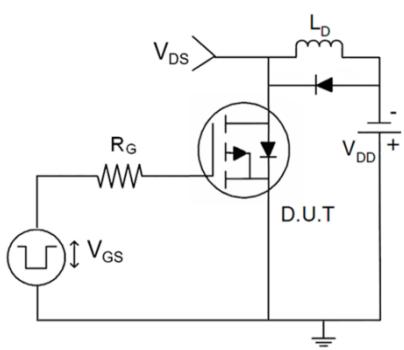
### 1) E<sub>AS</sub> Test Circuits



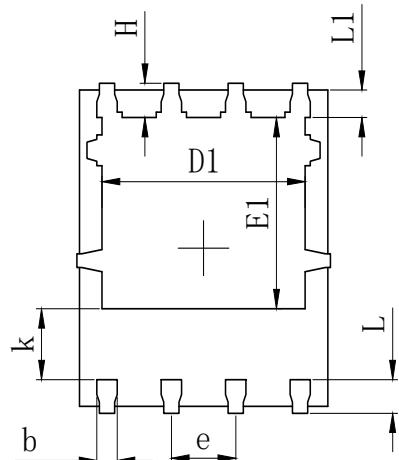
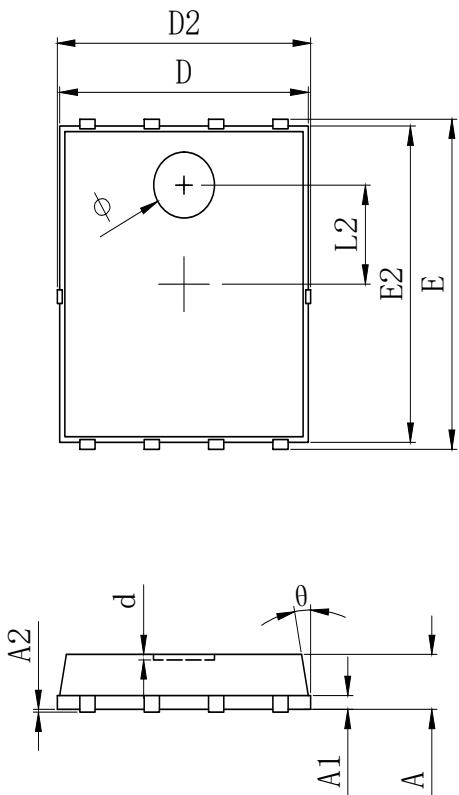
### 2) Gate Charge Test Circuit



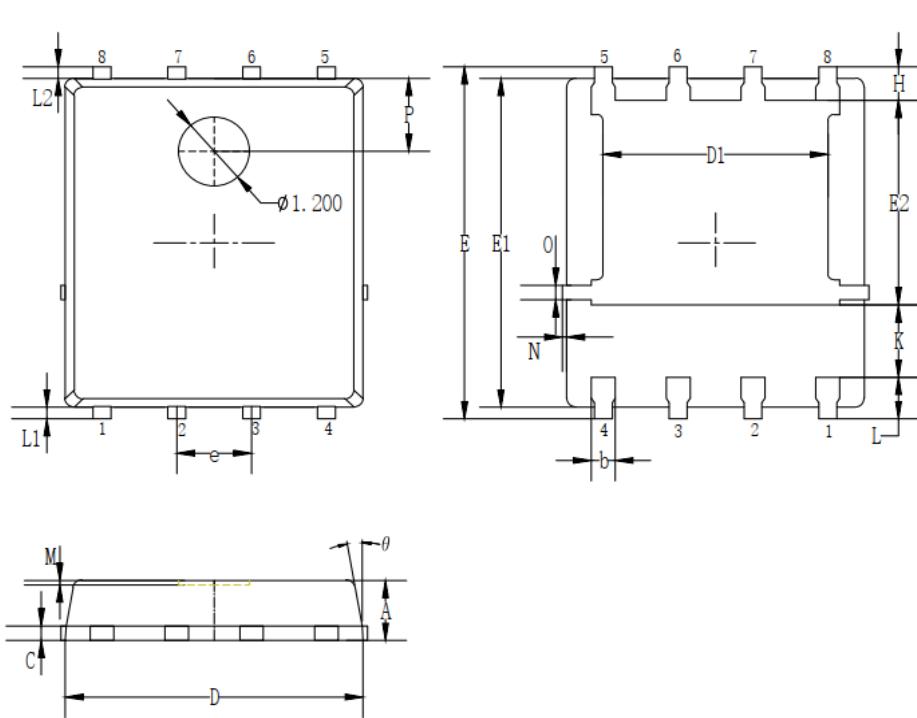
### 3) Switch Time Test Circuit



•Dimensions (PDFN5\*6)



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0~0.05		
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
$\theta$	8°	10°	12°
$\phi$	1.100	1.200	1.300
d			0.100



Symbols	Millimeters		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1	3.72	3.82	3.92
E	6.00	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
$\theta$	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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