ORIENT DISPLAY

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SPECIFICATION
FOR
LCD MODULE

MODULE NO: AFD800480A1L-7.0N6WTH

VERSION NO: 01

Customer's Approval:

	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)		
CHECKED BY		
APPROVED BY		

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REVISION STATUS

Version	Revised Date	Page	Content	Modified by
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1. GENERAL DESCRIPTION

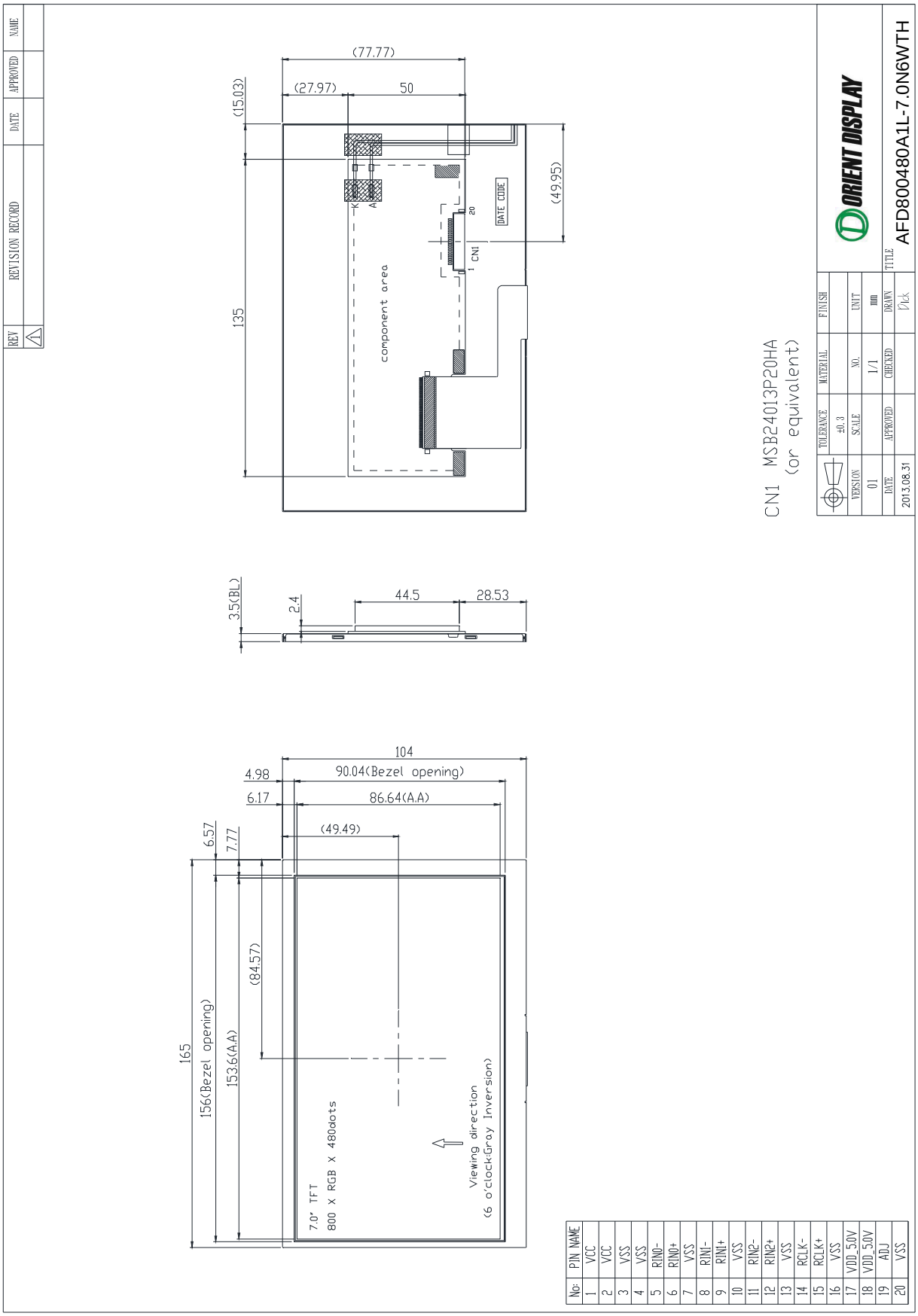
1.1 Description

The specifications is model AFD800480A1L-7.0N6WTH is a color active matrix thin film film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a back light system. This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with WVGA (800 horizontal by 480 vertical pixel) resolution.

1.2 Features:

No.	Item	Specification	Unit
1	Panel Size	7.0”	Inch
2	Number of Pixels	800 (W) x RGB x 480 (H)	Pixels
3	Active Area	153.6 (W) × 86.64 (H)	mm
4	Pixel Pitch	0.192 (W) x 0.1805 (H)	mm
5	Outline Dimension	165 (W) × 104 (H) × 3.5 (T)	mm
6	Number of Colors	262K	- -
7	Display Mode	TN / Normally White / Transmissive	- -
8	View Direction	6 o’clock(Gray Inversion)	
9	Display Format	RGB vertical stripe	- -
10	Surface Treatment	Anti-Glare	- -
11	Contrast Ratio	500 (Typ.)	- -
12	Luminance (cd/m^2)	1000 (Typ.)	cd/m2
13	Interface	LVDS 6 bit Interface	- -
14	Backlight	White LED	- -
15	Operation Temperature	-20 ~ 70	°C
16	Storage Temperature	-30 ~ 80	°C
17	Weight	(TBD)	g

2. MECHANICAL SPECIFICATION



No.	PIN NAME
1	VCC
2	VCC
3	VSS
4	VSS
5	RIN0-
6	RIN0+
7	VSS
8	RIN1-
9	RIN1+
10	VSS
11	RIN2-
12	RIN2+
13	VSS
14	RCLK-
15	RCLK+
16	VSS
17	VDD_5.0V
18	VDD_5.0V
19	ADJ
20	VSS

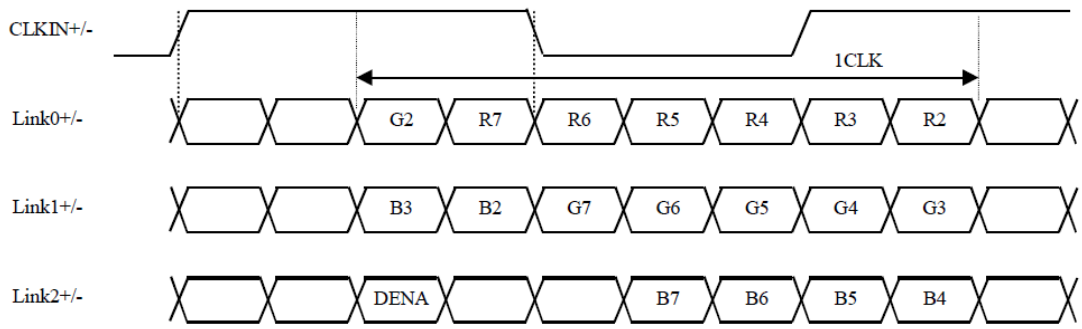
3. PIN DESCRIPTION

3.1 TFT LCD Module

Pin No.	Symbol	I/O	Function	Remark
1	VCC	P	Power Supply +3.3V	
2	VCC	P	Power Supply +3.3V	
3	GND	P	Ground	
4	GND	P	Ground	
5	RXIN0-	I	Negative LVDS differential data input	
6	RXIN0+	I	Positive LVDS differential data input	
7	GND	P	Ground	
8	RXIN1-	I	Negative LVDS differential data input	
9	RXIN1+	I	Positive LVDS differential data input	
10	GND	P	Ground	
11	RXIN2-	I	Negative LVDS differential data input	
12	RXIN2+	I	Positive LVDS differential data input	
13	GND	P	Ground	
14	CLK-	I	Negative LVDS differential clock input	
15	CLK+	I	Positive LVDS differential clock input	
16	GND	P	Ground	
17	VDD_5V	P	Power Supply LED voltage +5V	
18	VDD_5V	P	Power Supply LED voltage +5V	
19	ADJ	I	Back-light Dimming control	
20	GND	P	Ground	

NOTE:

1. NC Pin must be floating, VSS=GND
2. LVDS Mapping



4. ABSOLUTE MAXIMUM RATINGS

4.1 Electrical Absolute Rating

4.1.1 TFT LCD Module

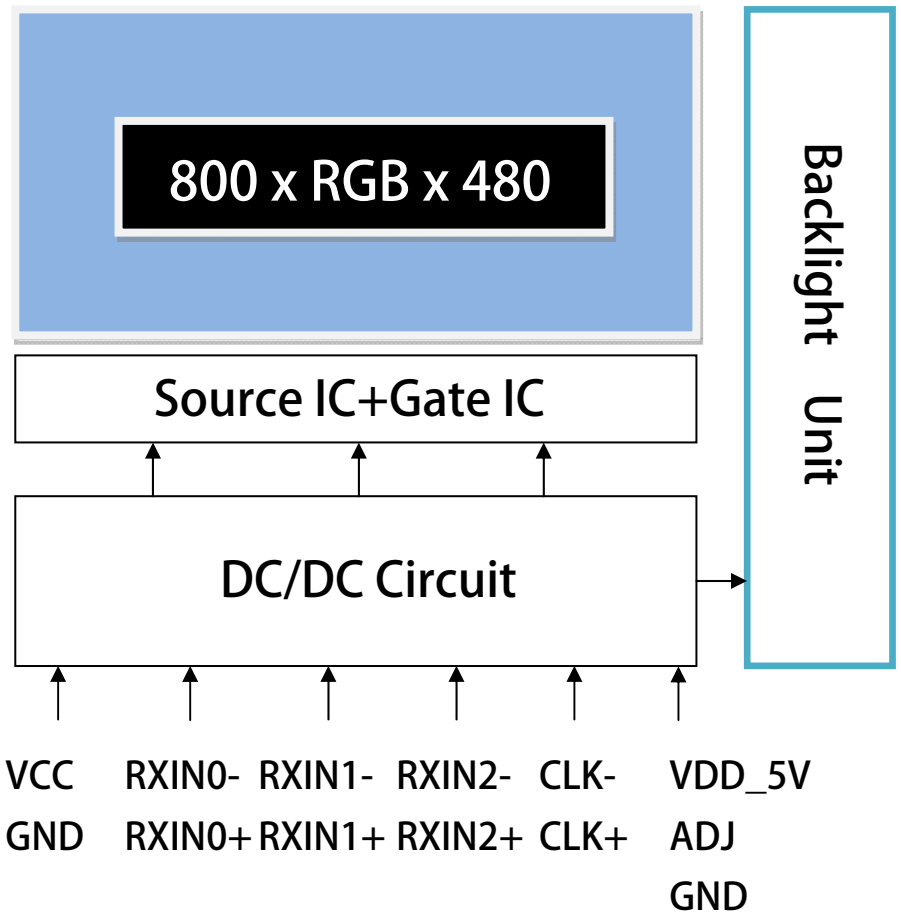
Item	Symbol	Values		Unit	Note
		Min	Max.		
Power supply voltage	VCC	-0.3	4.0	V	
	VDD_5V	0	6.0	V	

4.1.2 Environment Absolute Rating

Item	Symbol	Values			Unit	Note
		Min	Typ	Max.		
Operating Temperature	Topa	-20		70	°C	Ambient temperature
Storage Temperature	Tstg	-30		80	°C	

5. BLOCK DIAGRAM

5.1 TFT LCD Module



6. Relationship Between Displayed Color and Input

6.1 6 bit

	Color & Gray Scale	Data Signal																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage, 1 :High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. With the combination of total 18 bit data signals, the 262K-color display can be achieved on the screen.

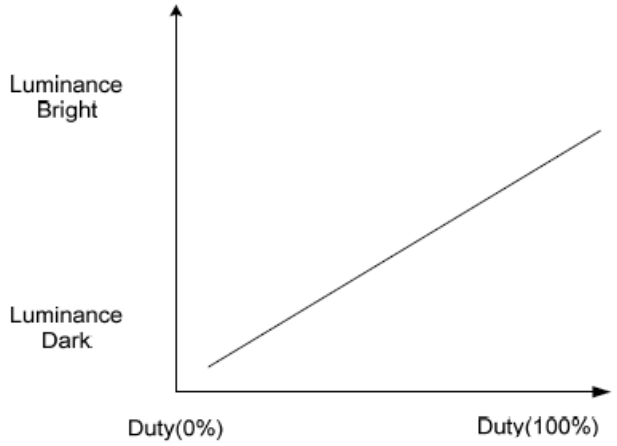
7. ELECTRICAL CHARACTERISTICS

7.1 TFT LCD Module

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		VCC	3.0	3.3	3.6	V	
		VDD_5V	4.5	5.0	5.5	V	
Input Voltage for logic	Differential Input High Threshold	VTH			+100	mV	
	Differential Input Low Threshold	VTL	-100			mV	
ADJ frequency		ADJ	19K	20K	21K	Hz	Note 2
Power Supply current		ICC	-	(150)	(200)	mA	Note 1
		IDD	-	(450)	(550)	mA	
LED Life Time (25℃)		-	(30000)	-	-	hr	Note 3

Note 1: frame =60Hz , Ta=25°C , Display pattern : Black pattern

Note 2: ADJ signal is 0~3.3V.Operation frequency is 20KHz

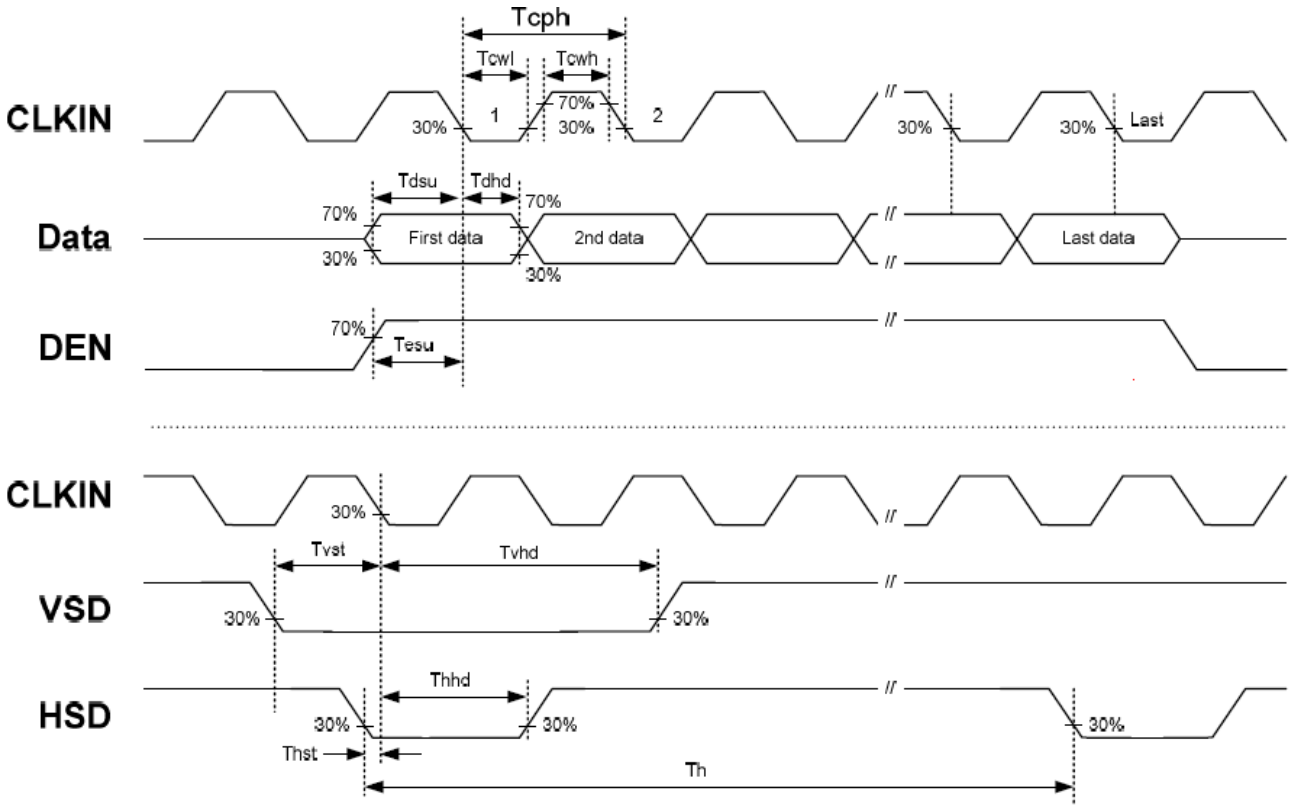


Note 3: The “LED life time” is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25℃ 60% RH.

7.2 INTERFACE SPECIFICATIONS

7.2.1 AC Timing characteristics

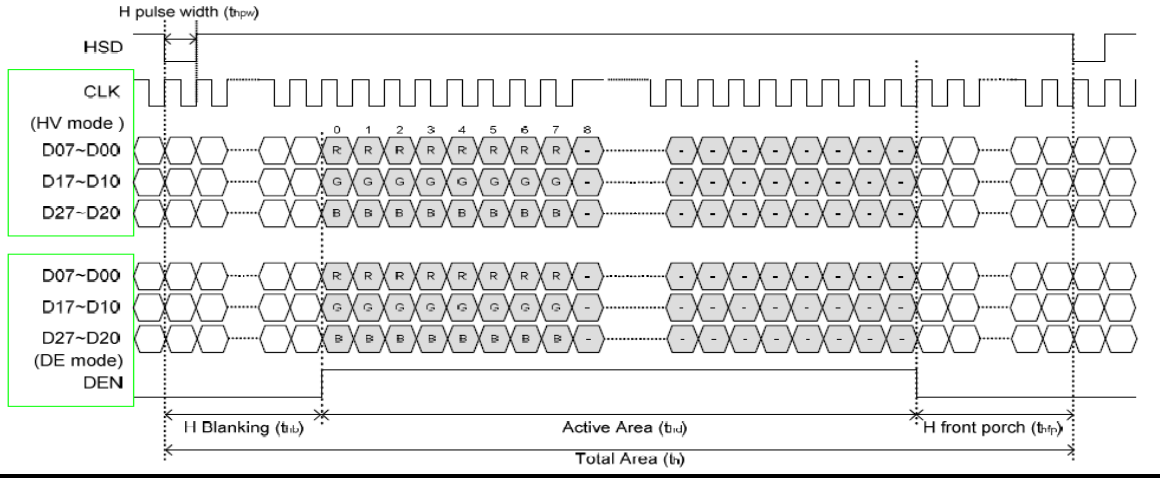
Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
HSYNC	HS setup time	Thst	8	-	-	ns	
	HS hold time	Thhd	8	-	-	ns	
VSYNC	VS setup time	Tvst	8	-	-	ns	
	VS hold time	Tvhd	8	-	-	ns	
Data	Data setup time	Tdsu	8	-	-	ns	
	Data hold time	Tdhd	8	-	-	ns	
DE	DEN setup time	Tvpw	8	-	-	ns	
	DEN hold time	Tvb	8	-	-	ns	



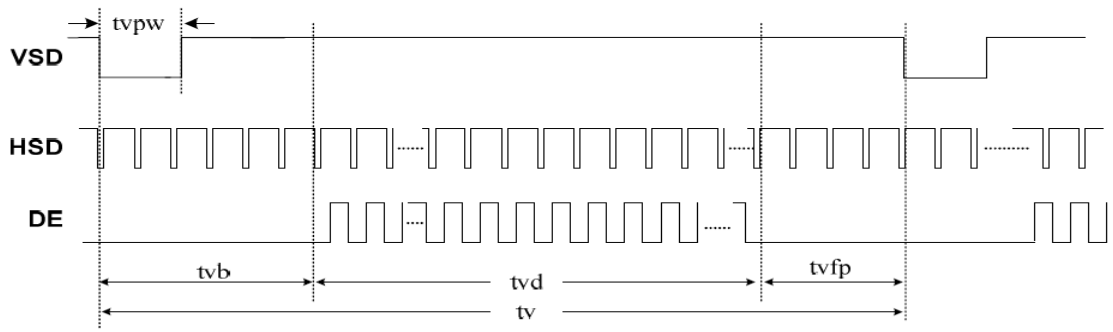
7.2.2 DE Mode Input Timing Table

Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
DCLK	CLK frequency	Fclk	-	30	50	MHz	
	CLK period	Tcph	20	-	-	ns	
	CLK pulse duty	Tcwh	40	50	60	%	
HSYNC	Horizontal Line	Th	862	1056	1200	CLK	
	HS Display Area	Thd	-	800	-	CLK	
	HS Pulse Width	Thpw	1	-	40	CLK	
	HS Back Porch	Thb	-	46	-	CLK	
	HS Front Porch	Thfp	16	210	354	CLK	
DE	DE Mode Blanking	Th-Thd	85	256	400	CLK	
VSYNC	VS Display Area	Tvd	-	480	-	th	
	VS Period Time	Tv	513	525	650	th	
	VS Pulse Width	Tvpw	3	-	20	th	
	VS Back Porch	Tvb	-	23	-	th	
	VS Front Porch	Tvfp	1	12	77	th	
DE	DE Mode Blanking	Tv-Tvd	30	45	170	th	

Horizontal input timing



Vertical input timing

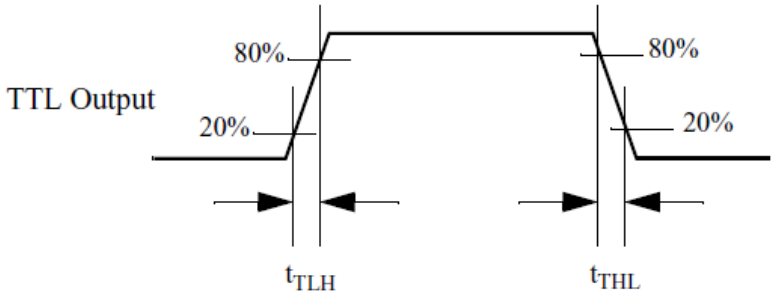


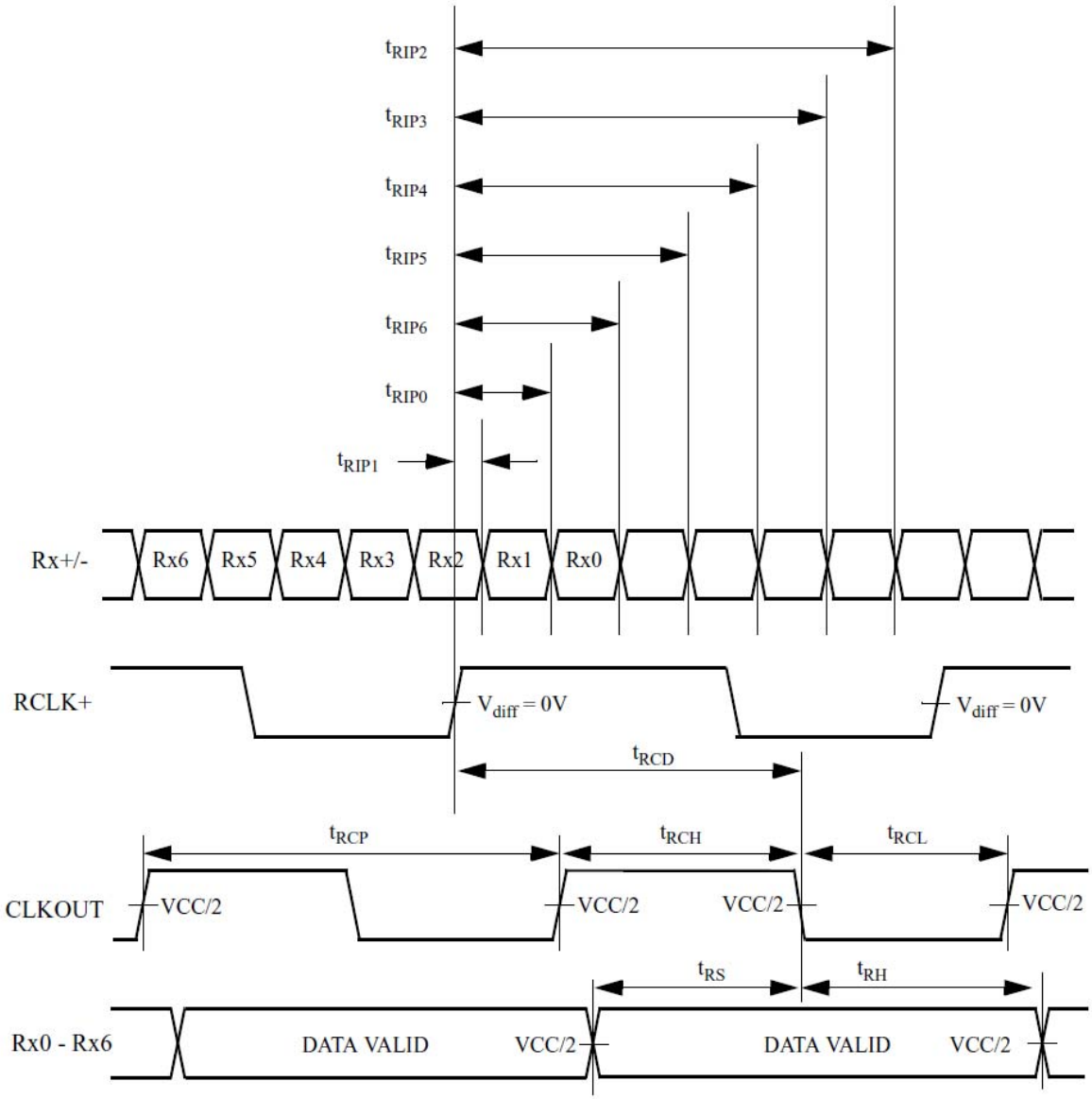
7.3 LVDS Switching Characteristics

7.3.1 LVDS Timing Condition

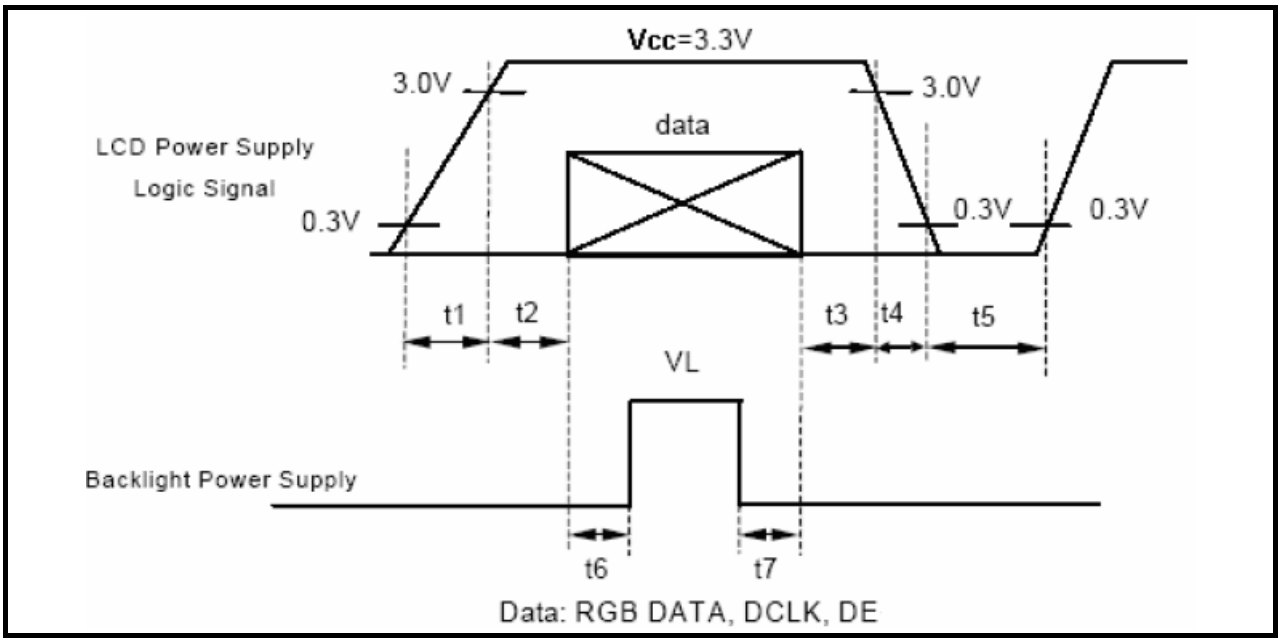
Symbol	Parameter		Min.	Typ.	Max.	Unit	Note
tRCP	CLK OUT Period	VCC = 3.0 - 3.6V	11.76	T	50.0	ns	
		VCC = 2.5 - 3.6V	14.28	T	50.0	ns	
tRCH	CLK OUT High Time		-	4T/7	-	ns	
tRCL	CLK OUT Low Time		-	3T/7	-	ns	
tRCD	RCLK +/- to CLK OUT Delay		-	5T/7	-	ns	
tRS	TTL Data Setup to CLK OUT		0.35T-0.3	-	-	ns	
tRH	TTL Data Hold from CKL OUT		0.45T-1.6	-	-	ns	
tTLH	TTL Low to High Transition Time		-	2.0	3.0	ns	
tTHL	TTL High to Low Transition Time		-	1.8	3.0	ns	
tRIP1	Input Data Position0 (T = 11.76ns)		-0.4	0.0	0.4	ns	
tRIP0	Input Data Position1 (T = 11.76ns)		T/7-0.4	T/7	T/7+0.4	ns	
tRIP6	Input Data Position2 (T = 11.76ns)		2T/7-0.4	2T/7	2T/7+0.4	ns	
tRIP5	Input Data Position3 (T = 11.76ns)		3T/7-0.4	3T/7	3T/7+0.4	ns	
tRIP4	Input Data Position4 (T = 11.76ns)		4T/7-0.4	4T/7	4T/7+0.4	ns	
tRIP3	Input Data Position5 (T = 11.76ns)		5T/7-0.4	5T/7	5T/7+0.4	ns	
tRIP2	Input Data Position6 (T = 11.76ns)		6T/7-0.4	6T/7	6T/7+0.4	ns	
tRPLL	Phase Lock Loop Set				10.0	ms	

7.3.2 LVDS AC Timing





7.4 Power On / Off Sequence



t1 ≤10ms : 1 sec≤ t5

50ms≤ t2 : 200ms ≤t6

0<t3 ≤50ms: 200ms≤ t7

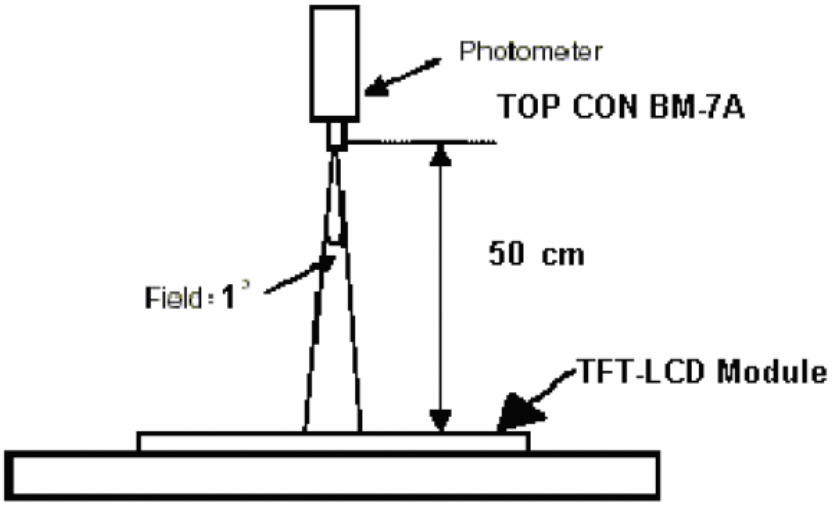
0<t4 ≤10ms

8. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Brightness		--	Note1, Note 3, ($\theta = 0°$, Normal Viewing Angle)	800	1000	--	cd/m2
Uniformity		B-uni		70	75	--	%
Contrast Ratio		CR		400	500	--	--
Response Time		Tr		--	10	--	ms
		Tf		--	15	--	ms
Color Chromaticity	White	Wx		0.260	0.310	0.360	--
		Wy		0.280	0.330	0.380	--
View angle	Horizontal	θ x+	Center CR≥10	50	60	--	
		θ x-		50	60	--	
	Vertical	θ Y+		40	50	--	
		θ Y-		50	60	--	
Image sticking		tis	2 hours	--	--	2	Sec

Note：The following optical specifications shall be measured in a darkroom or equivalent state(ambient luminance ≤1 lux, and at room temperature). The operation temperature is 25°C±2°C. The measurement method is shown in Note1.

Note1: The method of optical measurement:

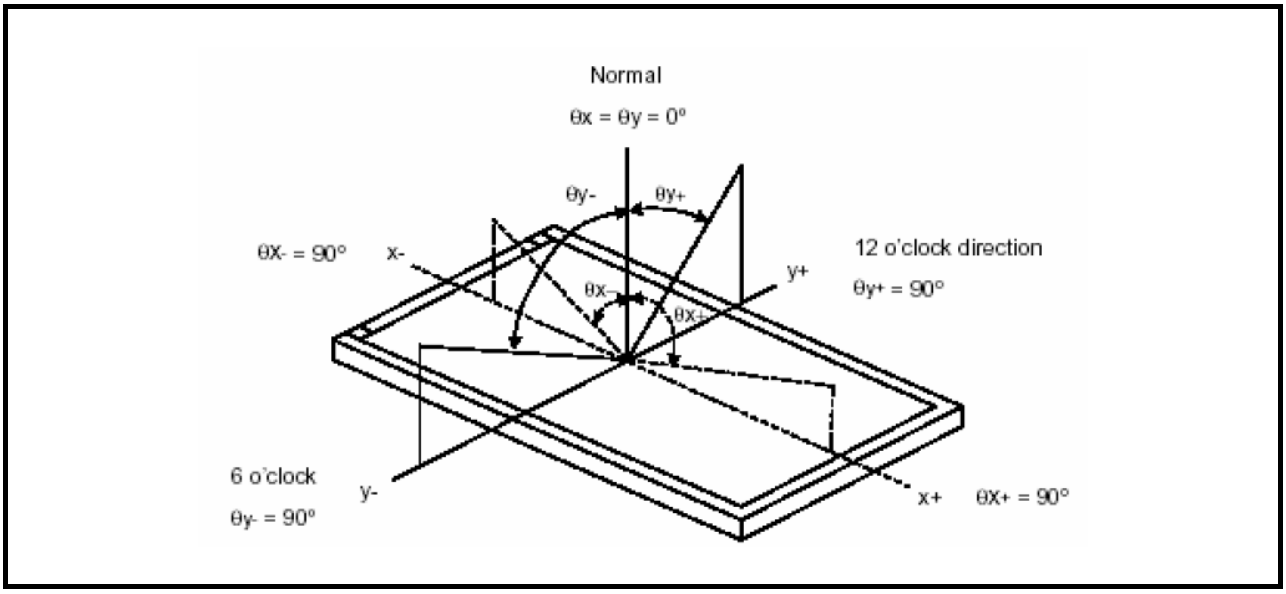


Note2: Measured at the center area of the panel and at the viewing angle of the θ x= θ y =0°

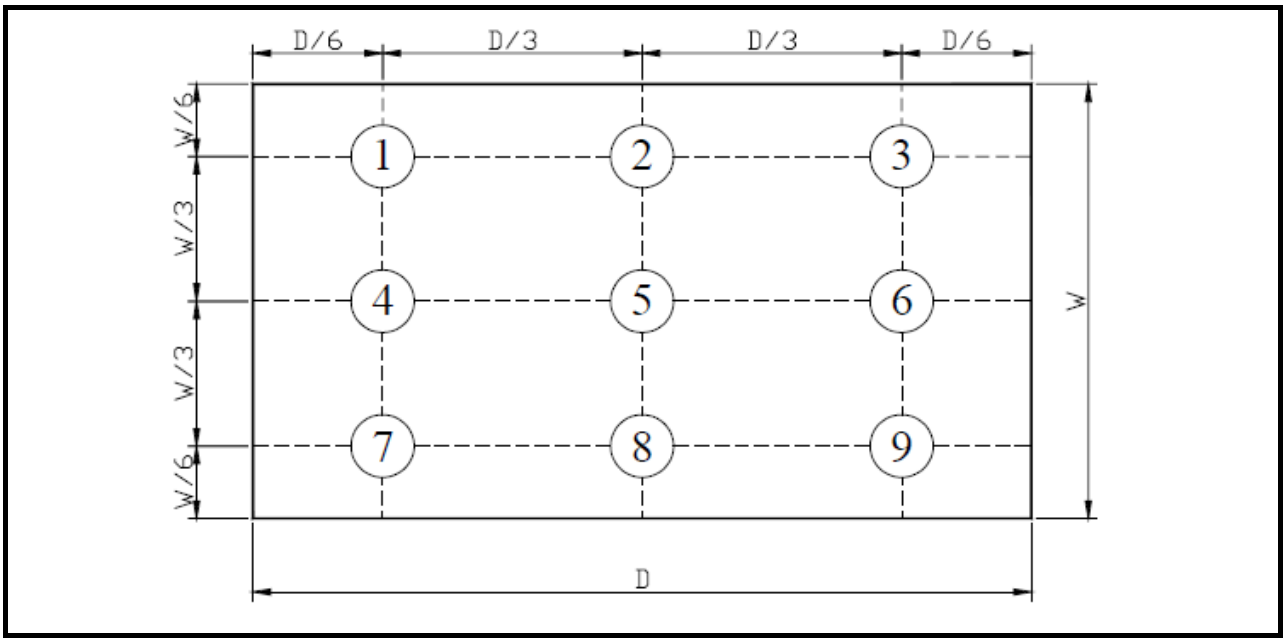
Note3: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state ÷ Luminance with all pixels in Black state

Note4: Definition of Viewing Angle:

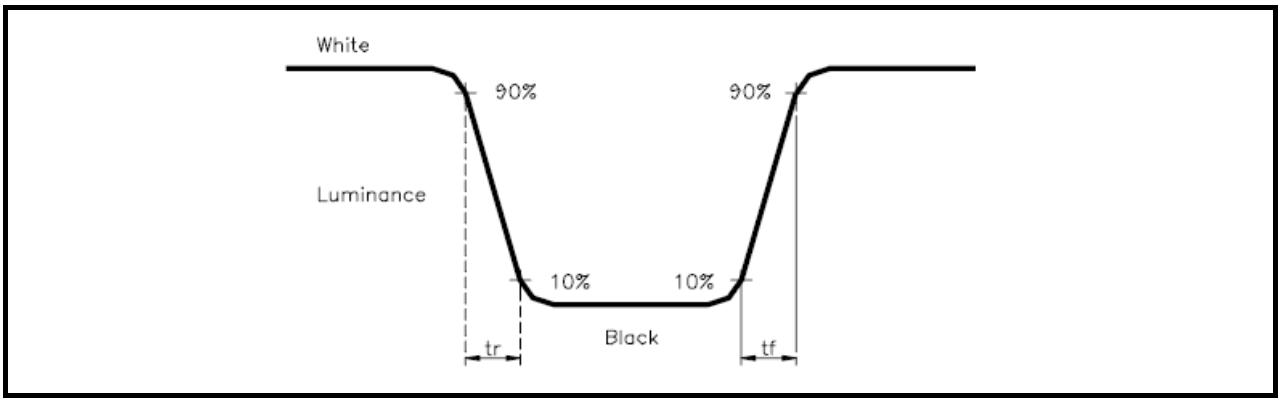


Note 5: Definition of Brightness Uniformity (B-uni):



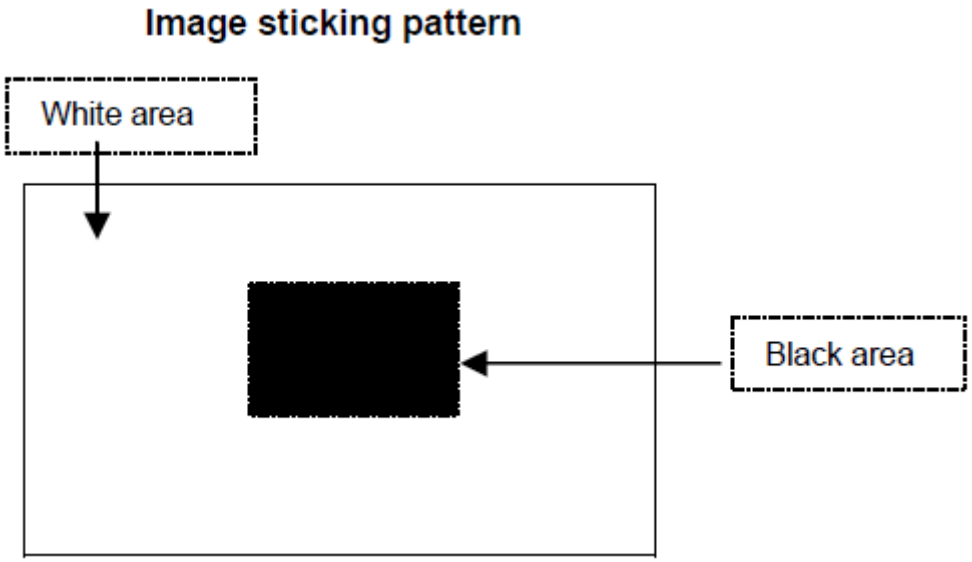
B-uni ＝(Minimum luminance of 9 points÷Maximum luminance of 9points)X100%

Note6: Definition of Response Time:
The Response Time is set initially by defining the “Rising Time (Tr)” and the “Falling Time (Tf)” respectively. Tr and Tf are defined as following figure



Note 7: Definition of Chromaticity:
The color coordinates (Wx,Wy),(Rx,Ry),(Gx,Gy),and (Bx,By) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.

Note 8: Definition of Image sticking (tis):
Continuously display the test pattern shown in the figure below for 2 hours. Then display a completely white screen. The previous image shall not persist more than 2 sec at 25 ℃



9. RELIABILITY

9.1 Test Condition

9.1.1 Temperature and Humidity(Ambient Temperature)

Temperature : 25 ± 5°C

Humidity : 65 ± 5%

9.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

9.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

9.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

9.2 TESTS

No.	ITEM	CONDITION CRITERION
1	High Temperature Storage	80°C, 120 hrs
2	Low Temperature Storage	-30°C, 120 hrs
3	High Temperature Operating	70°C, 120 hrs
4	Low Temperature Operating	-20°C, 120 hrs
5	High Temperature/Humidity Non-Operating	60°C, 90%RH, 120 hrs
6	Temperature Shock Non-Operating	-30°C ←→ 80°C (0.5hr each), 25 cycles
7	Vibration Test Non-Operating	Frequency:0 ~ 55 Hz Amplitude:1.5 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z
8	Electro-static Discharge Non-Operating	150pF,330Ω Air:± 8KV;Contact: ±4KV 10 times/point;4 points/panel face

9.3 JUDGEMENT STANDARD

The judgement of the above test should be made as follows:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

9.4 INCOMING INSPECTION STANDARDS

<table>
<tr><th>No.</th><th>Parameter</th><th>Criteria</th></tr>
<tr><td rowspan="2">1</td><td rowspan="2">Operating</td><td>Display function: No Display malfunction (Major)</td></tr>
<tr><td>Contrast ratio (Black, White):
Does not meet specified range in the spec. (Major) (Note:3)</td></tr>
<tr><td rowspan="4"></td><td rowspan="4"></td><td>Line Defect: No obvious Vertical and Horizontal line defect in bright, dark and colored. (Major) (Note:1)</td></tr>
<tr><td>Point Defect (Red, green, blue, dark): Active area ≤ 5 dots (Minor) (Note:1)
<table>
<tr><th rowspan="2">Item</th><th colspan="2">Acceptable number</th><th rowspan="2">Total</th></tr>
<tr><th>A</th><th>B</th></tr>
<tr><td>Bright</td><td>0</td><td>2</td><td rowspan="3">5</td></tr>
<tr><td>Dark</td><td>2</td><td>4</td></tr>
<tr><td>Total</td><td>2</td><td>4</td></tr>
</table></td></tr>
<tr><td>Non-uniformity: Visible through 6%ND filter. (Minor)</td></tr>
<tr><td>Foreign material in Black or White spots shape (W>1/4L)
<table>
<tr><th>Zone / Dimension</th><th>Acceptable number</th><th>Class Of Defects</th><th>AQL Level</th></tr>
<tr><td>D> 0.5</td><td>0</td><td rowspan="3">Minor</td><td rowspan="3">1.5</td></tr>
<tr><td>0.1 ≤ D ≤ 0.5</td><td>4</td></tr>
<tr><td>D ≤ 0.1</td><td>*</td></tr>
</table>
D = (Long + Short) / 2 ＊ : Disregard</td></tr>
<tr><td>Foreign Material in Line or spiral shape (W≤1/4L) (Note: 4)
<table>
<tr><th>L (mm) / Zone W(mm)</th><th>Acceptable number</th><th>Class Of Defects</th><th>AQL Level</th></tr>
<tr><td>L >2</td><td>W>0.1</td><td>0</td><td rowspan="3">Minor</td><td rowspan="3">1.5</td></tr>
<tr><td>0.5 < L ≤ 2</td><td>0.03 < W≤0.1</td><td>1</td></tr>
<tr><td>L ≤0.5</td><td>W≤0.03</td><td>*</td></tr>
</table>
L : Length W : Width ＊ : Disregard</td></tr>
<tr><td rowspan="4">2</td><td rowspan="4">External Inspection (non-operating)</td><td>Dimension: Outline (Major)</td></tr>
<tr><td>Bezel appearance: uneven (Minor)</td></tr>
<tr><td>Scratch on the polarize: (Note:2)
<table>
<tr><th>L (mm) / Zone W(mm)</th><th>Acceptable number</th><th>Class Of Defects</th><th>AQL Level</th></tr>
<tr><td>--</td><td>W>0.1</td><td>0</td><td rowspan="2">Minor</td><td rowspan="2">1.5</td></tr>
<tr><td>L ≤ 2</td><td>W≤0.1</td><td>3</td></tr>
</table>
L : Length W : Width ＊ : Disregard</td></tr>
<tr><td>Dent or bubble on the polarize (Note:2)
<table>
<tr><th>Zone / Dimension</th><th>Acceptable number</th><th>Class Of Defects</th><th>AQL Level</th></tr>
<tr><td>D≤0.05</td><td>*</td><td rowspan="2">Minor</td><td rowspan="2">1.5</td></tr>
<tr><td>D≤0.3</td><td>3</td></tr>
</table>
D = (Long + Short) / 2 ＊ : Disregard</td></tr>
</table>

Class of defects	**Major**	AQL 0.65%	**Definition** It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.
	Minor	AQL 1.5%	It is a defect that will not result in functioning problem with deviation classified.

Note1:

(a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively

(b)Dark point defect is defined as visible in full white pattern.

(c)Definition of distribution of point defect is as follows:

-minimum separation between dark point defects should be larger than 5mm.

-minimum separation between bright point defects should be larger than 5mm.

(d)Definition of joined bright point defect and joined dark point defect are as follows:

-Two or more joined bright point defects must be nil.

-Three joined dark point defects must be nil.

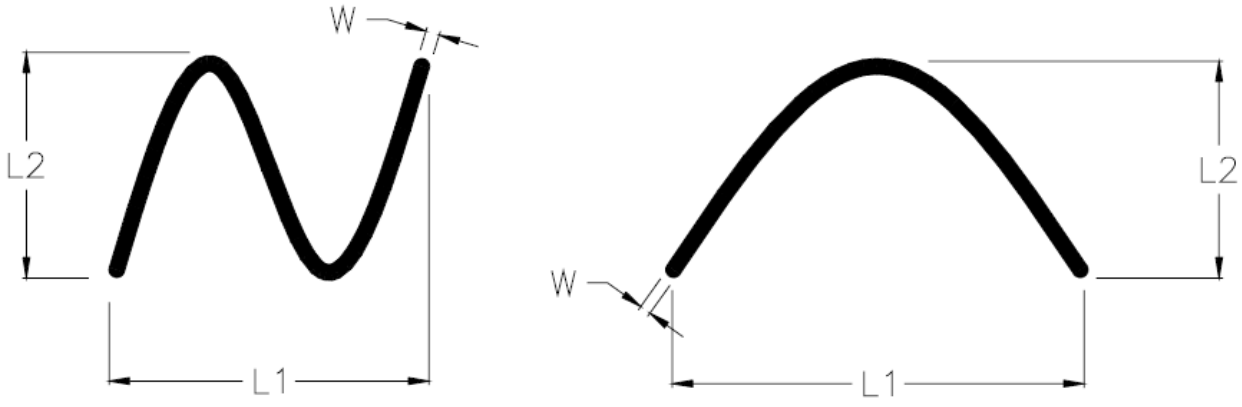
-Coupling of one dark and one bright point in junction is counted as one dark and bright spot with 1 pair maximum.

-Two Joined dark point is counted as two dark points with 2 pair maximum.

Note2: The external inspection should be conducted at the distance 30± 5cm between the eyes of inspector and the panel.

Note3: Luminance measurement for contrast ratio is at the distance 50± 5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note4: W-Width in mm , L-length of Max.(L1,L2) in mm.



9.5 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling

Sampling table: MIL-STD-105E

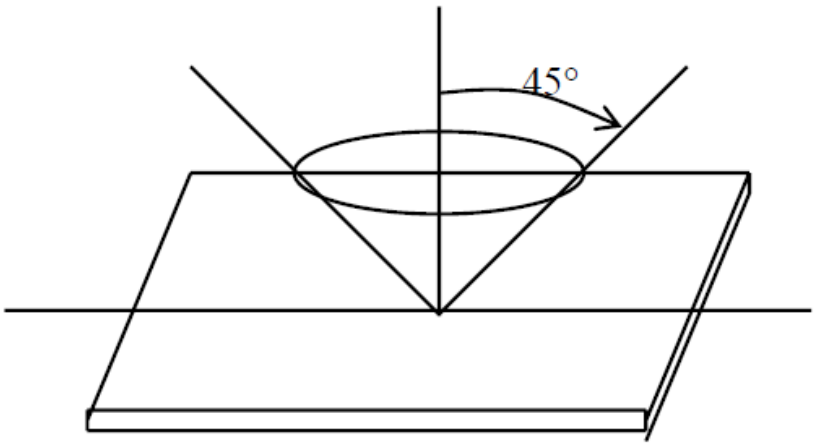
Inspection level: Level II

9.6 Inspection conditions

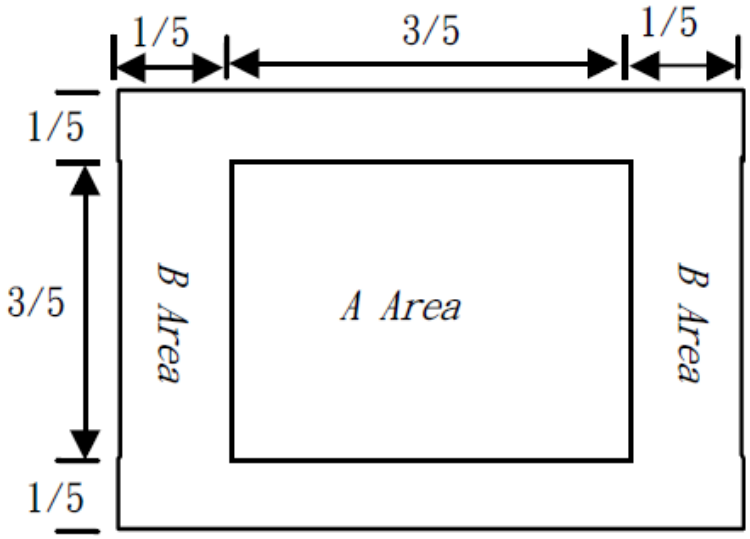
The LCD shall be inspected under 40W white fluorescent light.

$\theta \leq 45^{\circ}$ inspection under non-operating condition.

$\theta \leq 5^{\circ}$ inspection under operating condition



Definition of applicable Zones



10. PRECAUTION RELATING PRODUCT HANDLING

10.1 SAFETY

10.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

10.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

10.2 HANDLING

10.2.1 Avoid any strong mechanical shock which can break the glass.

10.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.

10.2.3 Do not remove the panel or frame from the module.

10.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, Do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

10.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

10.2.6 Do not touch the display area with bare hands , this will stain the display area.

10.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

10.2.8 To control temperature and time of soldering is 280 ± 10°C and 3-5 sec.

10.2.9 To avoid liquid (include organic solvent) stained on LCM.

10.3 STORAGE

10.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

10.3.2 Do not place the module near organics solvents or corrosive gases.

10.3.3 Do not crush, shake, or jolt the module.