National Semiconductor

VF16C

PRELIMINARY

AF160 Universal Wideband Active Filter

General Description

The AF160 state variable active filter is a general second order lumped RC network. Only four external resistors program the AF160 for specific second order functions. Lowpass, highpass, and bandpass functions are available simultaneously at separate outputs. Notch and allpass functions are available by summing the outputs in the uncommitted output summing amplifier. Higher order systems are realized by cascading AF160 active filters with appropriate programming resistors.

Any of the classical filter configurations, such as Butterworth, Bessel, Cauer, and Chebyshev can be formed. The AF160 is pin compatible with the AF100J.

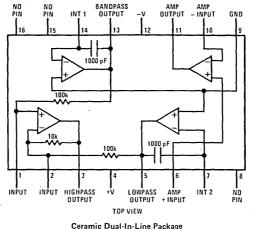
Features

- Military or commercial specification
- Independent Q, frequency, gain adjustments
- Low sensitivity to external component variation
- Separate lowpass, highpass, bandpass outputs
- Inputs may be differential, inverting, or non-inverting
- Allpass and notch outputs may be formed using uncommitted amplifier
- Operates to 40 kHz
- Q range to 500
- Power supply range
 Frequency accuracy

 ± 5.0 V to ± 18 V

- ±1% unadjusted
- Q frequency product ≤ 200,000
- BIFET™ input amplifier, pin compatible with AF100J

Connection Diagram



Ceramic Dual-In-Line Package Order Numbers AF160-1CJ, AF160-2CJ NS Package Number HY13A

Absolute Maximum Ratings

Supply Voltage		±18V
Power Dissipation	900 mW/Package (500) mW/Amp)
Differential Input Vo	±36 V	
Output Short Circuit	Infinite	
Lead Temperature (S	Soldering, 10 seconds)	300°C

Operating Temperature	
AF160-1CJ, AF160-2CJ, AF	160-1CG,
AF160-2CG	-25°C to +85°C
AF160-1G, AF160-2G	–55°C to +125°C
Storage Temperature	
AF160-1G, AF160-2G	–65°C to +125°C
AF160-1CG, AF160-2CG, A	
AF160-2CJ	–25°C to +100°C
AF160-2CJ	–25°C to +100°C

Electrical Characteristics (Complete Active Filter) (Note 2)

PARAMETER	CONDITIONS MIN		ТҮР	МАХ	UNITS
Frequency Range	f _C x Q ≤ 200,000			100k	Hz
Q Range	f _C x Q ≤ 200,000			500	Hz/Hz
f _O Accuracy AF160-1, AF160-1C AF160-2, AF160-2C	f _C x Q ≤ 40,000, T _A = 25°C f _C x Q ≤ 40,000, T _A = 25°C			±2.5 ±1.0	% · %
f _O Temperature Coefficient			±50	±150	ppm/°C
Q Accuracy	$f_{C} x Q \le 40,000, T_{A} = 25^{\circ}C$:	±7.5	%
Q Temperature Coefficient			±300	±750	ppm/°C
Power Supply Current	V _S = ±15V		7.2	11	mA

Electrical Characteristics (Internal Op Amp) (Note 3)

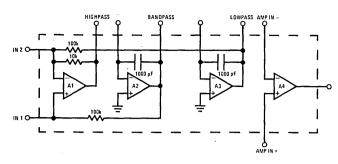
PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNITS
Input Offset Voltage	R _S ≤10 kΩ, T _A = 25°C		5	10	mV
Input Offset Current	T _J = 25°C		25	100	pА
Input Bias Current	- -		50	200	рА
Large Signal Voltage Gain	$R_L \ge 2k$, $V_{OUT} = \pm 10V$	25	100		V/mV
Output Voltage Swing	$R_L = 10 k\Omega$	±12	±13.5		v
Input Voltage Range		±11	+15/-12		v
Common Mode Rejection Ratio	$R_S \le 10 k\Omega$	70	100		dB
Supply Voltage Rejection Ratio	$R_S \le 10 k\Omega$	70	100		dB
Slew Rate (Unity Gain)			13		V/µs
Small Signal Bandwidth			4		MHz

Note 1: Any of the amplifiers can be shorted to ground indefinitely; however, more than one should not be simultaneously shorted, as the maximum junction temperature will be exceeded.

Note 2: Specifications apply for $V_S = \pm 15V$, over $-25^{\circ}C$ to $+85^{\circ}C$ for the AF160-1C and AF160-2C and over $-55^{\circ}C$ to $+125^{\circ}C$ for the AF160-1 and AF160-2, unless otherwise specified.

Note 3: Specifications apply for $V_S = \pm 15V$, $T_A = 25^{\circ}C$.

Applications Information





CIRCUIT DESCRIPTION AND OPERATION

A schematic of the AF160 is shown in Figure 1. Amplifier A1 is a summing amplifier with inputs from integrator A2 to the non-inverting input and integrator A3 to the inverting input. Amplifier A4 is an uncommitted amplifier.

By adding external resistors the circuit can be used to generate the second order system

$$T(s) = \frac{a_3s^2 + a_2s + a_1}{s^2 + b_2s + b_1}$$

The denominator coefficients determine the complex pole pair location and the quality of the poles where

 $\omega_0 = \sqrt{b_1}$ = the radian center frequency $Q = \frac{\omega_0}{b_2}$ = the quality of the complex pole pair

If the output is taken from the output of A1, numerator coefficients a1 and a2 equal zero, and the transfer function becomes:

$$\Gamma(s) = \frac{a_3 s^2}{s^2 + \frac{\omega_0}{\Omega} s + {\omega_0}^2}$$
 (highpass)

If the output is taken from the output of A2, numerator coefficients a1 and a3 equal zero and the transfer function becomes:

$$T(s) = \frac{a_2 s}{s^2 + \frac{\omega_0}{\Omega} s + \omega_0^2}$$
 (bandpass)

If the output is taken from the output of A3, numerator coefficients a3 and a2 equal zero and the transfer function becomes:

$$T(s) = \frac{a_1}{s^2 + \frac{\omega_0}{Q}s + \omega_0^2}$$
 (lowpass)

In the allpass transfer function $a_1 = 1$, $a_2 = -\omega_0/Q$ and $a_3 = \omega_0^2$. The transfer function becomes:

 $\hat{T}(s) = \frac{s^2 + \omega_z^2}{s^2 + \frac{\omega_0}{\Omega}s + \omega_0^2}$

notch and allpass filter.

function becomes:

$$T(s) = \frac{s^2 - \frac{\omega_0}{Q}s + {\omega_0}^2}{s^2 + \frac{\omega_0}{Q}s + {\omega_0}^2}$$
(all pass)

Using proper input and output connections the circuit

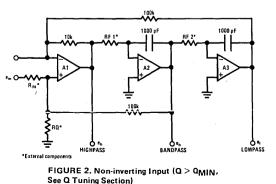
can also be used to generate the transfer functions for a

In the transfer function for a notch function a₂ becomes

zero, a_1 equals 1, and a_3 equals ω_z^2 . The transfer

COMMON CONFIGURATIONS

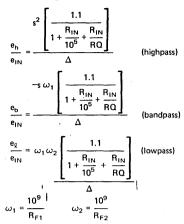
The specific transfer functions for some of the most useful circuit configurations using the AF160 are illustrated in Figures 2 through 8. Also included are the gain equations for each transfer function in the frequency band of interest, the Q equation, center frequency equation and the Q determining resistor equation.



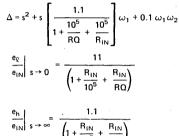
(notch)



a) Non-inverting input (Figure 2) transfer equations are:

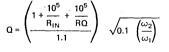


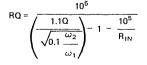
where

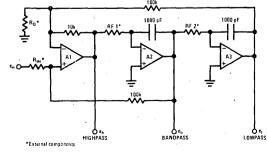


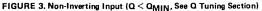
$$\frac{e_{\rm b}}{|{\rm e}_{\rm IN}|} = \frac{\left(1 + \frac{10^5}{\rm RQ} + \frac{10^5}{\rm R_{\rm IN}}\right)}{\left(1 + \frac{\rm R_{\rm IN}}{\rm 10^5} + \frac{\rm R_{\rm IN}}{\rm RQ}\right)}$$

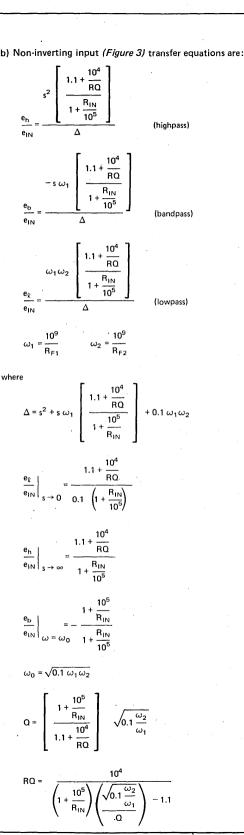
 $\omega_0 = \sqrt{0.1 \, \omega_1 \omega_2}$











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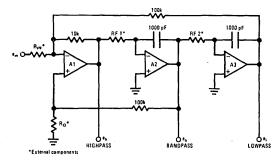
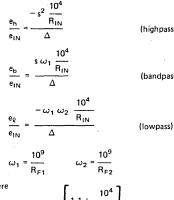


FIGURE 4. Inverting Input

c) Inverting input (Figure 4) transfer function equations are:

(highpass)

(bandpass)



where

$$\Delta = s^2 + s \omega_1 \left[\frac{1.1 + \frac{10^4}{R_{1N}}}{1 + \frac{10^5}{R_{\Omega}}} \right] + 0.1 \omega_1 \omega_2$$
$$\frac{e_{\varrho}}{e_{1N}} = -\frac{10^5}{R_{1N}} \qquad (lowpass)$$

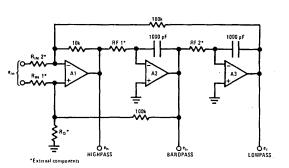
$$\frac{e_{iN}}{s \to 0} \bigg|_{s \to \infty} = -\frac{R_{iN}}{R_{iN}} \qquad (lowpass)$$

$$\frac{e_{h}}{e_{iN}} \bigg|_{s \to \infty} = -\frac{10^{4}}{R_{iN}} \qquad (highpass)$$

$$\frac{e_{b}}{e_{IN}}\Big|_{\omega = \omega_{0}} = \frac{\frac{10^{4}}{R_{IN}}\left(1 + \frac{10^{5}}{R_{0}}\right)}{1.1 + \frac{10^{4}}{R_{IN}}}$$
 (bandpass)

$$\omega_0 = \sqrt{0.1 \, \omega_1 \omega_2}$$

$$Q = \left[\frac{1 + \frac{10^5}{RQ}}{1.1 + \frac{10^4}{R_{IN}}} \right] \sqrt{0.1 \frac{\omega_2}{\omega_1}}$$
$$RQ = \frac{10^5}{\frac{Q}{\sqrt{0.1 \frac{\omega_2}{\omega_1}}}} \left(\frac{1.1 + \frac{10^4}{R_{IN}}}{1.1 + \frac{10^4}{R_{IN}}} \right) - 1$$



AF160

FIGURE 5. Differential Input

d) Differential input (Figure 5) transfer function equations are:

(highpass)

(lowpass)

$$\frac{e_{h}}{e_{IN}} = \frac{s^{2} \frac{10^{4}}{R_{IN2}}}{\Delta}$$

$$\frac{e_{b}}{e_{IN}} = \frac{-s \omega_{1} \frac{10^{4}}{R_{IN2}}}{\Delta}$$
 (bandpass)

$$\frac{e_{\varrho}}{e_{\rm INI}} = \frac{\omega_1 \omega_2 \frac{10^4}{R_{\rm IN2}}}{\Delta}$$

$$\omega_1 = \frac{10^9}{R_{F1}}$$
 $\omega_2 = \frac{10^9}{R_{F2}}$

where

$$\Delta = s^{2} + s \omega_{1} \left[\frac{1.1 + \frac{10^{4}}{R_{1N2}}}{1 + \frac{10^{5}}{RQ} + \frac{10^{5}}{R_{1N1}}} \right] + 0.1 \omega_{1} \omega_{2}$$

$$\omega_0 = \sqrt{0.1} \omega_1 \omega_2$$

$$Q = \left[\frac{1 + \frac{10^5}{RQ} + \frac{10^5}{R_{IN1}}}{1.1 + \frac{10^4}{R_{IN2}}} \right] \sqrt{0.1 \frac{\omega_2}{\omega_1}}$$



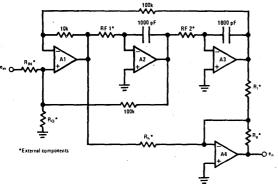
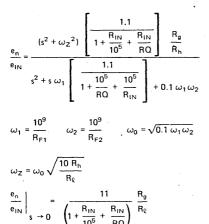
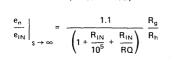


FIGURE 6. Output Notch Using All Four Amplifiers

e) Output notch (Figure 6) transfer function equations are:







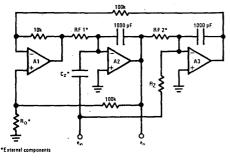
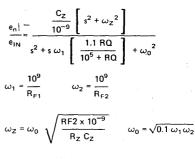


FIGURE 7. Input Notch Using Three Amplifiers

j) Input notch (Figure 7) transfer function equations are:





$$\frac{n}{|N|} = -\frac{C_z}{10^{-9}}$$

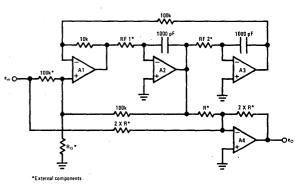
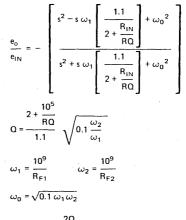


FIGURE 8. Allpass

g) Allpass (Figure 8) transfer function equations are:



Time delay at $\omega_0 = \frac{2\Omega}{\omega_0}$ seconds

FREQUENCY TUNING

To tune the AF160 two resistors are required for frequencies between 200 Hz and 10 kHz. For lower frequencies "T" tuning or addition of external capacitors

is required. Using external capacitors allows the user to go as low in frequency as he desires. "T" tuning and external capacitors can be used together.

Two resistor tuning for 200 Hz to 10 kHz

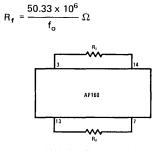
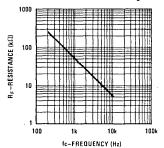
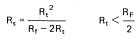


FIGURE 9. Resistive Tuning





"T" resistive tuning for $f_{\rm O}$ < 200 Hz



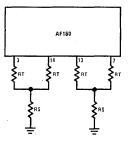
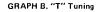
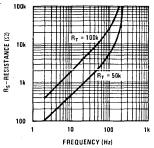


FIGURE 10. T Tuning





RC tuning for $f_0 < 200 \text{ Hz}$

$$R_{f} = \frac{0.05033}{f_{o} (C + 1 \times 10^{-9})}$$

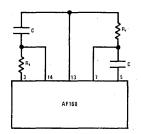
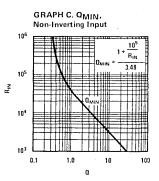


FIGURE 11. Low Frequency RC Tuning

Q TUNING

To tune the Q of an AF160 requires one resistor from pins 1 or 2 to ground. The value of the Q tuning resistor depends on the input connection and input resistance as well as the value of the Q. The Q of the unit is inversely proportional to resistance to ground at pin 1 and directly proportional to resistance to ground from pin 2.



For $Q > Q_{MIN}$ in non-inverting mode:

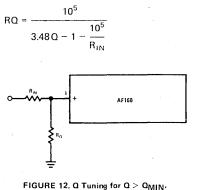
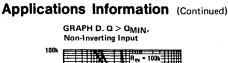
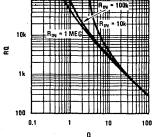


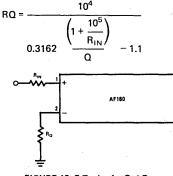
FIGURE 12. Q Tuning for Q > Q_{MIN} Non-Inverting Input AF160

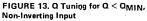
AF160



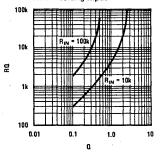


For $Q < Q_{MIN}$ in non-inverting mode:

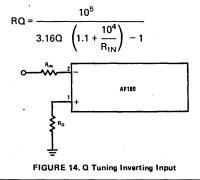


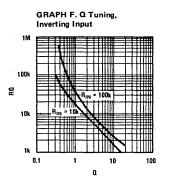


GRAPH E. Q < Q_{MIN}, Non-Inverting Input



For any Q in inverting mode:





NOTCH TUNING

Two methods to generate notches are the RC input and lowpass/highpass summing. The RC input method requires adding a capacitor and resistor connected to the two integrator inputs. The capacitor connects to "Int 1" and the resistor connects to "Int 2." The output summing requires two resistors connected to the lowpass and highpass output.

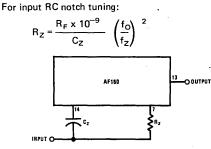
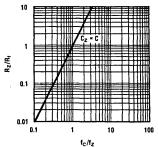
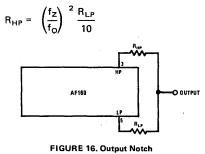


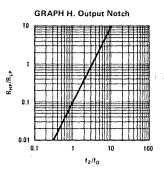
FIGURE 15, Input RC Notch

GRAPH G. Input RC Notch



For output notch tuning:





TUNING TIPS

In applications where 2 to 3% accuracy is not sufficient to provide the required filter response, the AF160 stages can be tuned by adding trim pots or trim resistors in series or parallel with one of the frequency determining resistors and the Q determining resistor.

When tuning a filter section, no matter what output configuration is to be used in the circuit, measurements are made between the input and the bandpass (pin 13) output.

Before any tuning is attempted the lowpass (pin 7) output should be checked to see that the output is not clipping. At the center frequency of the section the lowpass output is 10 dB higher than the bandpass output and 20 dB higher than the highpass. This should be kept in mind because if clipping occurs the results obtained when tuning will be incorrect.

Frequency Tuning

By adjusting the resistance between pins 7 and 13 the center frequency of a section can be adjusted. If the input is through pin 1 the phase shift at center frequency will be 180° and if the input is through pin 2 the phase shift at center frequency will be 0°. Adjusting center frequency by phase is the most accurate but tuning for maximum gain is also correct.

"Q" Tuning

The "Q" is tuned by adjusting the resistance between pin 1 or 2 and ground. Low Q tuning resistors will be from pin 2 to ground (Q < 0.6). High Q tuning resistors will be from pin 1 to ground. To tune the Q correctly the signal source must have an output impedance very much lower than the input resistance of the filter since the input resistance affects the Q. The input must be driven through the same resistance the circuit will see to obtain precise adjustment.

The lower 3 dB (45°) frequency, f_L , and the upper 3 dB (45°) frequency, f_H , can be calculated by the following equations:

$$f_{H} = \left(\frac{1}{2Q} + \sqrt{\left(\frac{1}{2Q}\right)^{2} + 1}\right) \times (f_{O})$$

where f_{O} = center frequency

$$f_{L} = \left(\sqrt{\left(\frac{1}{2Q}\right)^{2} + 1} - \frac{1}{2Q} \right) \times (f_{Q})$$

When adjusting the Q, set the signal source to either f_H or f_L and adjust for 45° phase change or a 3 dB gain change.

Notch Tuning

If a circuit has a jw axis zero pair the notch can be tuned by adjusting the ratio of the summing resistors (lowpass/highpass summing) or the input resistance (input RC).

In either case the signal is connected to the input and the proper resistor is adjusted for a null at the output.

Special Cases

When using the input RC notch the unit cannot be tuned through the normal input so an additional 100k resistor can be added at pin 1 and the unit can be tuned normally. Then the 100k input resistor should be grounded and the notch tuned through the normal RC input.

An alternative way of tuning is to tune using the Q resistor as the input. This requires the Q resistor be lifted from ground and connecting the signal source to the normally grounded end of the Q resistor. This has the problem that when the Q resistor is grounded after tuning, its value is decreased by the output impedance of the source. This technique has the advantage of not requiring an additional resistor.

TUNING PROCEDURE (See Figure 17)

Center Frequency Tuning

Set oscillator to center frequency desired for the filter . section, adjust amplitude and check that clipping does not occur at the lowpass output pin 5.

Adjust the resistance between pins 13 and 7 until the phase shift between input and bandpass output is 180° .

Q Tuning

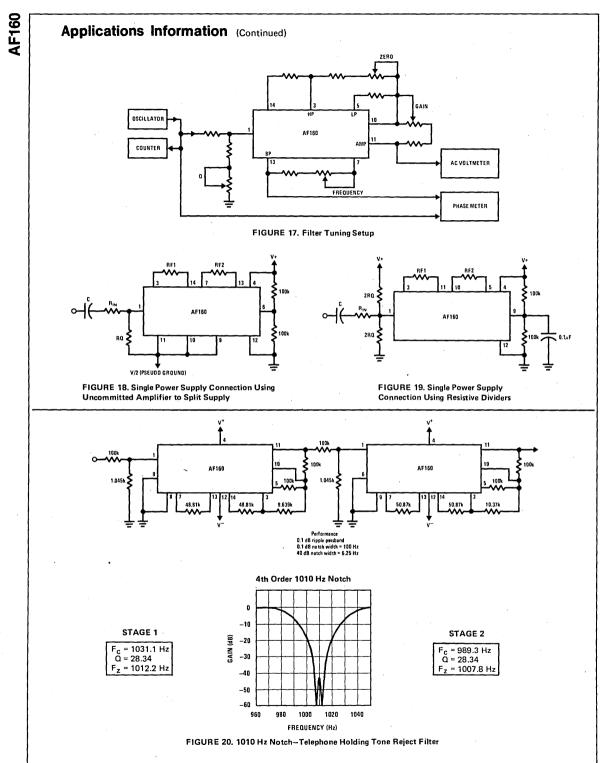
Set oscillator to upper or lower 45° frequency (see tuning tips) and tune the Q resistor until the phase shift is 135° (upper 45° frequency) or 225° (lower 45° frequency).

Zero Tuning

Set the oscillator output to the zero frequency and tune the zero resistor for a null at the output of the summing amplifier.

Gain Adjust

Set the oscillator to any desired frequency and the gain can be adjusted by measuring the output of the summing amplifier and adjusting the feedback resistance.



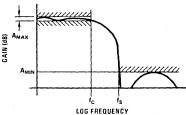
FILTER DESIGN

Since most filter tables are in terms of a normalized lowpass prototype, the filter to be designed is usually reduced to a lowpass prototype. After the lowpass transfer function is found, it is transformed to obtain the transfer function for the actual filter desired. *Graph I* shows the lowpass amplitude response which can be defined by four quantities.

AF160

Applications Information (Continued)





A_{MAX} = the maximum peak to peak ripple in the passband.

 A_{MIN} = the minimum attenuation in the stopband.

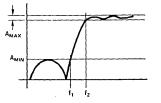
f_C = the passband cutoff frequency.

f_S = the stopband start frequency.

By defining these four quantities for the lowpass prototype the normalized pole and zero locations and the Q (quality) of the poles can be determined from tables or by computer programs.

To obtain the lowpass prototype for the highpass filter (*Graph J*) A_{MAX} and A_{MIN} are the same as for the lowpass case but $f_C = 1/f_2$ and $f_S = 1/f_1$.

GRAPH J. Highpass Response



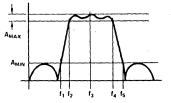
To obtain the lowpass prototype for a bandpass filter (Graph K) A_{MAX} and A_{MIN} are the same as for the lowpass case but

$$f_{\rm C} = 1$$
 $f_{\rm S} = \frac{f_5 - f_1}{f_4 - f_2}$

where $f_3 = \sqrt{f_1 f_5} = \sqrt{f_2 f_4}$ i.e. geometric symmetry

 $f_5 - f_1 = A_{MIN}$ bandwidth $f_4 - f_2 = Ripple bandwidth$

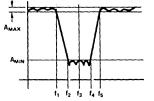
GRAPH K. Bandpass Response



To obtain the lowpass prototype for the notch filter (Graph L) A_{MAX} and A_{MIN} are the same as for the lowpass case and

 $f_{C} = 1 \qquad f_{S} = \frac{f_{5} - f_{1}}{f_{4} - f_{2}}$ where $f_{3} = \sqrt{f_{1} f_{5}} = \sqrt{f_{2} f_{4}}$

GRAPH L. Notch Response



Normalized Lowpass Transformed To Un-Normalized Lowpass

The normalized lowpass filter has the passband edge normalized to unity. The un-normalized lowpass filter instead has the passband edge at f_c . The normalized and un-normalized lowpass filters are related by the transformation $s = s\omega_c$. This transforms the normalized passband edge s = j to the un-normalized passband edge $s = j\omega_c$.

Normalized Lowpass Transformed To Un-Normalized Highpass

The transformation that can be used for lowpass to highpass is $S = \omega_C/s$. Since S is inversely proportional to s, the low frequency and high frequency responses are interchanged. The normalized lowpass $1/(S^2 + S/Q+1)$ transforms to the un-normalized highpass

$$\frac{s^2}{s^2 + \frac{\omega_c}{\Omega}s + \omega_c^2}$$

Normalized Lowpass Transformed To Un-Normalized Bandpass

The transformation that can be used for lowpass to bandpass is S = $(s^2 + \omega_0^2)$ BWs where ω_0^2 is the center frequency of the desired bandpass filter and BW is the ripple bandwidth.

Normalized Lowpass Transformed To Un-Normalized Bandstop (Or Notch)

The bandstop filter has a reciprocal response to a bandpass filter. Therefore a bandstop filter can be obtained by first transforming the lowpass prototype to a highpass and then performing the bandpass transformation.

SELECTION OF TRANSFER FUNCTION

The selection of a function which approximates the shape of the response desired is a complicated process. Except in the simplest cases it requires the use of tables or computer programs. The form of the transfer function desired is in terms of the pole and zero locations. The most common approximations found in tables are Butterworth, Tschebycheff, Elliptic, and Bessel. The decision as to which approximation to use is usually a function of the requirements and system objectives. Butterworth filters are the simplest but have the disadvantage of requiring high order transfer functions to obtain sharp roll-offs.

AF160

Applications Information (Continued)

The Tschebycheff function is a min/max approximation in the passband. This approximation has the property that it is equiripple which means that the error oscillates between maximums and minimums of equal amplitude in the passband. The Tschebycheff approximation, because of its equiripple nature, has a much steeper transition region than the Butterworth approximation.

The elliptic filter, also known as Cauer or Zolotarev filters, are equiripple in the passband and stopband and have a steeper transition region than the Butterworth or the Tschebycheff.

For a specific lowpass filter three quantities can be used to determine the degree of the transfer function: the maximum passband ripple, the minimum stopband attenuation, and the transition ratio (tr = ω_S/ω_C). Decreasing A_{MAX} , increasing A_{MIN} , or decreasing tr will increase the degree of the transfer function. But for the same requirements the elliptic filter will require the lowest order transfer function. Tables and graphs are available in reference books such as "Reference Data for Radio Engineers," Howard W. Sams & Co., Inc., 5th Edition, 1970 and Erich Christian and Egon Eisenmann, "Filter Design Tables and Graphs," John Wiley, and Sons, 1966.

For specific transfer functions and their pole locations such text as Louis Weinberg, "Network Analysis and Synthesis," McGraw Hill Book Company, 1962 and Richard W. Daniels, "Approximation Methods for Electronic Filter Design," McGraw-Hill Book Company, 1974, are available.

DESIGN OF CASCADED MULTISECTION FILTERS

The first step in designing is to define the response required and define the performance specifications:

1. Type of filter:

Lowpass, highpass, bandpass, notch, allpass

- 2. Attenuation and frequency response
- Performance Center frequency/corner frequency plus tolerance and stability
- Insertion loss/gain plus tolerance and stability
- Source impedance

Load impedance

Maximum output noise

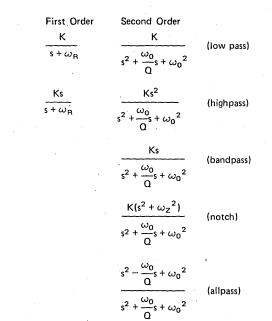
Power consumption

Power supply voltage

Dynamic range

Maximum output level

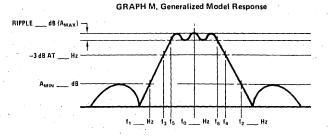
Second step is to find the pole and zero location for the transfer function which meet the above requirements. This can be done by using tables and graphs or network synthesis. The form of the transfer function which is easiest to convert to a cascaded filter is a product of first and second order terms in these forms:



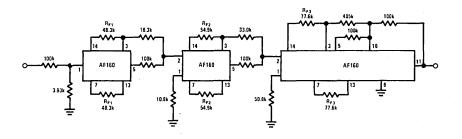
Each of the second order functions is realizable by tuning an AF160 stage. By cascading these stages the desired transfer function is realized.

CASCADING SECOND ORDER STAGES

The primary concern in cascading second order stages is to minimize the maximum difference in amplitude from input to output over the frequencies of interest. A computer program is probably required in very complicated cases but some general rules that can be used that will usually give satisfactory results are:



- 1. The highest "Q" pole pair should be paired with the zero pair closest in frequency.
- 2. If highpass and lowpass stages are cascaded the lowpass sections should be the higher frequency and highpass sections the lower frequency.
- 3. In cascaded filters of more than two sections the first section should be the section with "Q" closest to 0.707 and then additional stages should be added in order of least difference between first stage Q and their Q.



Lowpass Elliptic Filter

$F_{C} = 1$					
F _S = 1.3 A _{MAX} = 0.1 dB					
A _{MIN} = 40 dB					
N = 6				$(f_{-})^2$	
f ₀₁ = 1.0415	Q ₁ = 7.88	f _{Z1} = 1.329	f _Z /f _O = 1.28	$\left(\frac{f_2}{f_0}\right)$	= 1.63
f _{O2} = 0.9165	Q ₂ = 1.79	f _{Z2} = 1.664	$f_Z/f_O \approx 1.82$	$\left(\frac{f_Z}{f_O}\right)^2$	= 3.30
f _{O3} = 0.649	Q ₃ = 0.625	f _{Z3} = 4.1285	f _Z /f _O = 6.36	$\left(\frac{f_{Z}}{f_{O}}\right)^{2}$	= 40.5
$R_{F1} = \frac{(503.3)}{f_{01} \times f_C}$	x 10 ⁵ R _{F2}	$=\frac{(503.3)}{f_{02} \times f_{C}} \times \frac{1}{2}$	10^5 R _{F3} = $\frac{(50)}{f_{03}}$	$\frac{(3.3)}{3 \times f_C}$	
at 1000 Hz = f _c	:				
R _{F1} = 48.3k	R _{F2}	₂ = 54.9k	R _{F3} = 77	7.6k	

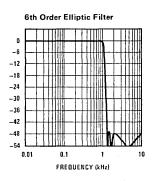
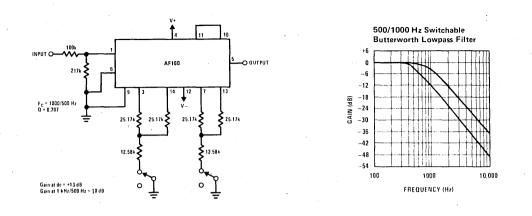
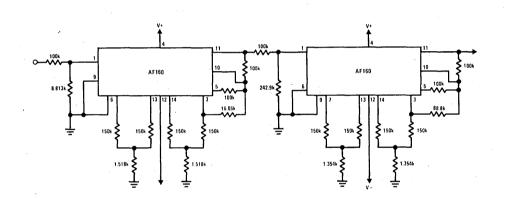


FIGURE 21. Lowpass Elliptic Filter Example

AF160







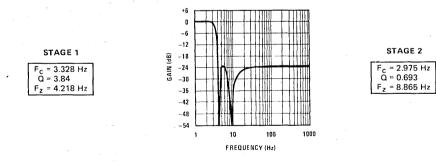
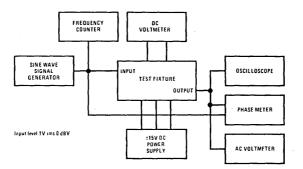


FIGURE 23. EEG Delta Filter-3 Hz Lowpass





COMPUTER AIDED DESIGN EXAMPLE*

This design is an example of a 60 Hz notch filter. The response is to have the following specifications:

Maximum passband ripple 0.1 dB

Minimum rejection 35 dB

0.1 dB bandwidth 15 Hz max

-35 dB bandwidth 1.5 Hz min

The steps in the design of this filter are:

- 1. Design a lowpass "prototype" for the filter.
- 2. Transformation of the lowpass prototype into a notch filter design.
- 3. Using the pole and zero locations found in step two calculate the value of the resistors required to build the filter.
- 4. Draw a schematic of filter using values obtained in step three.

*Computer programs shown are user interactive. Underlined copy is user input, non-underlined copy is computer response, and line indications in parenthesis are included for easy identification of data common to several programs.

PROGRAM NO. 1

RUN

THIS PROGRAM DESIGNS BUTTERWORTH CHEBYCHEFF OR ELLIPTIC NORMALIZED LOWPASS FILTERS WHAT TYPE OF FILTER ? B-C-E

ELLIPTIC DO YOU KNOW THE ORDER OF THE FILTER ? Y/N ? NO INPUT FC,FS,AMAX,AMIN ? 1, 10, .1, 35 FC 1.000 10.000 FS .100 AMAX AMIN 35.000 Ν 2.000 (ATTENUATION IN dB) ATT AT FS -35.671IS THIS SATISFACTORY ? Y/N ? YES F 0 1,823 (Line 1.1) .775 (Line 1.2) Ζ

14.124 (Line 1.3)

14

PROGRAM NO. 2 (DETERMINES UN-NORMALIZED POLE + ZERO LOCATIONS OF FIRST SECTION) (DATA ENTERED FROM PROGRAM NO. 1)

RUN WHAT TYPE FILTER BANDPASS OR NOTCH ? NOTCH ENTER # OF POLE PAIRS? 1

ENTER # OF JW AXIS ZEROS? 1

ENTER # OF REAL POLES? 0

ENTER # OF ZEROS AT ZERO? 0

ENTER # OF COMPLEX ZEROS? 0

ENTER # OF REAL ZEROS? 0

ENTER F & Q OF EACH POLE PAIR ? 1.823, .775 (FROM LINE 1.1 AND LINE 1.2)

ENTER VALUES OF JW AXIS ZEROS ? <u>14.124</u> (FROM LINE 1.3) ENTER FREQUENCY SCALING FACTOR

? 1 ENTER THE # OF FILTERS TO BE DESIGNED

? 1 ENTER THE C.F. AND BW OF EACH FILTER ? 60, 15

OUTPUT OF PROGRAM NO. 2 TRANSFORMED POLE/ZERO LOCATIONS FIRST SECTION

POLE LOCATIONS CENTER FREQ. Q

56.93601 (From Line 2.3) 11.31813 (From Line 2.4) 63.228877 (From Line 2.5) 11.31813 (From Line 2.6) JW AXIS ZEROS

59.471339 (From Line 2.1) 60.533361 (From Line 22

PROGRAM NO. 3 (CHECK OF FILTER RESPONSE USING PROGRAM NO. 2 DATA BASE)

RUN

12

NUMERATOR (ZEROS) A(I)S^2+R(I)S+Z(I)^2 1 0 59.471339 (From Line 2.1) 1 0 60.533361 (From Line 2.2)

REAL POLE

COMPLEX POLE PAIRS

	F	Q	
	56.93601	11.31813	(From Lines 2.3 and 2.4)
!	63.228877	11.31813	(From Lines 2.5 and 2.6)

<u>RUN</u>	,								`
FREQUENCY	NOR. GAIN (DB)	PHASE	DELAY	NOR. DELAY	FREQUENCY	NOR. GAIN (DB)	PHASE	DELAY	NOR. DELAY
40.000	.032	347.69	.002275	5.847169	60.600	-47.102	169.17	.050801	108.232021
45.000	.060	342.20	.004107	8.749738	60.800	-33.650	165.48	.051677	110.096278
50.000	.100	330.70	.009983	21.268142	61.000	-27.577	161.72	.052809	112.508334
55.000	795	290.54	.046620	99.324027	61.200	-23.418	157.87	.054167	115.403169
56.000	-2.298	270.61	.063945	136.234562	61.400	-20.198	153.92	.055712	118.694436
57.000	-5.813	245.51	.072894	155.299278	61.600	-17.554	149.85	.057391	122.270086
58.000	-12.748	220.19	.065758	140.096912	61.800	-15.308	145.65	.059136	125.989157
58.200	-14.740	215.54	.063369	135.006390	62.000	-13.362	141.33	.060869	129.681062
58.400	-17.032	211.06	.060979	129.914831	63.000	-6.557	118.23	.065975	140.559984
58.600	-19.722	206.76	.058692	125.043324	64.000	-2.936	95.30	.059402	126.556312
58.800	-22.983	202.61	.056588	120.561087	65.000	-1.215	76.38	.045424	96.774832
59.000	-27.172	198.60	.054724	116.589928	66.000	463	62.43	.032614	69.484716
59.200	-33.235	194.72	.053139	113.212012	67.000	138	52.44	.023498	50.062947
59.400	-46.300	190.94	.051856	110.478482	70.000	.091	35.43	.010452	22.267368
59.600	-42.909	7.24	.050888	108.417405	75.000	.085	23.44	.004250	9.054574
59.800	-36.897	3.60	.050242	107.040235	80.000	.060	17.80	.002310	4.921727
60.00	-35.567	360.00	.049916	106.346516	85.000	.043	14.50	.001460	3.110493
60.200	-36.887	356.41	.049907	106.326777	90.000	.032	12.31	.001011	2.154297
60.400	-42.757	352.81	.050206	106.963750	1				

PROGRAM NO. 4

^{F160}

DESIGN OF FIRST SECTION

RUN

WHICH FILTER AF160 - J OR G ? ? J

WHAT TYPE OF FILTER SECTION? HIGHPASS-BANDPASS-LOWPASS-NOTCH-ALLPASS ? NOTCH

INPUT FC AND Q VALUES ? 56.93601, 11.31813 (FROM LINES 2.3 AND 2.4) INPUT REAL POLE AND CAPACITOR VALUES IF NONE ENTER 0 ? 0 INPUT ZERO LOCATION ? 59.471339 (FROM LINE 2.1) ARE TUNING INSTRUCTIONS REQUIRED ?

? YES

TUNING INSTRUCTION

PHASE SHIFT FROM INPUT TO PIN 13 SHOULD BE 180 DEG. AT 56.93601 HZ. IF TUNING IS REQUIRED, RF2 FROM PINS 7 TO 13 SHOULD BE ADJUSTED. PHASE SHIFT FROM INPUT TO PIN 13 SHOULD BE 135 DEG. AT 59.506798HZ. OR 225 DEG. AT 54.476284 HZ.

IF TUNING IS REQUIRED RQ FROM 1 OR 2 TO GROUND SHOULD BE ADJUSTED GAIN AT PIN 11 AT 59.471339 SHOULD BE 0 IF NOT ADJUST RHP FROM PIN 3 TO 10 FOR NULL

GAIN AT F>> FC= .00DB

FUNCTION	CONNEC FROM	TION	VALUE OF EXTERNAL RESISTORS IN OHMS
RIN	INPUT	1	100000.000
RQ	1	GND	2675.931
RF1	3	14	883960.996
RF2	7	13	883960.996
RLP	5	10	100000.000
RHP	3	10	10910.418
RG	10	11	357910.697
+V		4	
-V		12	
GND		9	
GND		6	
OUTPUT	PIN 11		

PROGRAM NO. 4 DESIGN OF SECOND SECTION

WHAT TYPE OF FILTER SECTION? HIGHPASS-BANDPASS-LOWPASS-NOTCH-ALLPASS ? <u>NOTCH</u> INPUT FC AND Q VALUES ? <u>63.228877, 11.31813</u> (FROM LINES 2.5 AND 2.6) INPUT REAL POLE AND CAPACITOR VALUES IF NONE ENTER 0 ? <u>0</u> INPUT ZERO LOCATION

RETUNING INSTRUCTIONS REQUIRED ? ? <u>60.533361</u> (FROM LINE 2.2) ARE TUNING INSTRUCTIONS REQUIRED ? ? <u>YES</u>

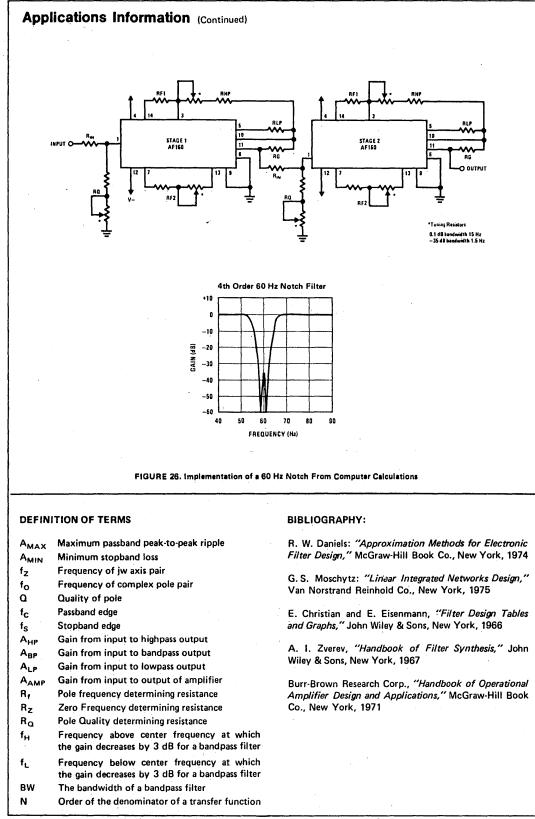
TUNING INSTRUCTION

PHASE SHIFT FROM INPUT TO PIN 13 SHOULD BE 180 DEG. AT 63.228877 HZ. IF TUNING IS REQUIRED RF2 FROM PINS 7 TO 13 SHOULD BE ADJUSTED PHASE SHIFT FROM INPUT TO PIN 13 SHOULD BE 135 DEG. AT 66.083802 HZ. OR 225 DEG. AT 60.497289 HZ. IF TUNING IS REQUIRED RQ FROM 1 OR 2 TO GROUND SHOULD BE ADJUSTED GAIN AT PIN 11 AT60.533361 SHOULD BE 0 IF NOT ADJUST RHP FROM PIN 3 TO 10 FOR NULL

FC= 63.228877	Q= 11.31813	F(L-3DB) = 60.497289	F(H-3DB)= 66.083802
---------------	-------------	----------------------	---------------------

GAIN A	AT	F ((FC=	.00DB
--------	----	-------	-----	-------

FUNCTION	CONN	ECTION	VALUE OF EXTERNAL
	FROM	то	RESISTORS IN OHMS
RIN	INPUT	1	100000.000
RQ	1	GND	2675.931
RF1	3	14	795984.596
RF2	7.	13	795984.596
RLP	5	10	100000.000
RHP	3	10	9165.552
RG	10	11	328044.920
+V		4	
V		12	• · · · · · · · · · · · · · · · · · · ·
GND		9	
GND		6	
OUTPUT	PIN 11		



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AF160