

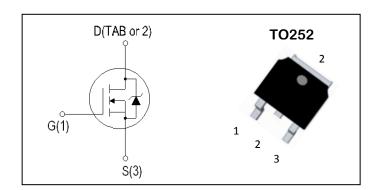
N-Channel Enhancement Mode Field Effect Transistor

FEATURES

- Fast Switching
- Ultra Low On-Resistance
- Optimized BVDSS Ruggedness
- Lead Free

PRODUCT SUMMARY

V _{DSS}	I _D	$R_{DS(ON)}$ (m Ω)
40V	116A	3.9m Ω



Absolute Maximum Ratings (Tc = 25°C unless otherwise specified)

Symbol	Parameter			Ratings	Unit
Common F	Ratings				
V _{DSS}	Drain-Source Voltage			40	V
V _{GSS}	Gate-Source Voltage			±20	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
TJ	Maximum Junction Temperature			150	°C
T _{STG}	Storage Temperature Range			-55 to 150	°C
Is	Diode Continuous Forward Current TC=25°C		116	А	
Mounted o	n Large Heat Sink				
IDM	300µs Pulse Drain Current Tested(1)	T _C =25°C		465	А
1-	Continuous Brain Current	Silicon Limited		116	А
lσ	Continuous Drain Current	Package Limited		80	А
PD	Maximum Power Dissipation	T _C =25°C		100	W

^{1.} Pulse width limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
RthJC	Thermal resistance junction-case max	1.5	°C/W
RthJA	Thermal resistance junction-ambient max	110	°C/W





Scan the QR code to view the latest product information





Electrical Characteristics (Tj=25°C Unless Otherwise Noted)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
On/off Charac	cteristics					
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	40			V
		V _{DS} = 32V, V _{GS} =0V			1	
loss	Zero Gate Voltage Drain Current	V _{DS} =32V, V _{GS} =0V T _J =125°C			10	uA
V _G S(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	1.5	2.5	V
Igss	Gate Leakage Current	V_{GS} = $\pm20V$, V_{DS} = $0V$			±250	nA
RDS(ON)	Drain-SourceOn-stateResistance(2)	V _{GS} = 10V, I _{DS} =70A		3.1	3.9	mΩ
Dynamic Chara	acteristics					
Ciss	Input Capacitance	V _{GS} =0V,		2990		
Coss	Output Capacitance	V _{DS} = 20V,		462		pF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz		246		
Switching Char	racteristics					
td(ON)	Turn-on Delay Time(1)	V _{DD} =20V,		16		
tr	Turn-on Rise Time(1)	I _D = 35A, V _{GS} = 10V,		59		
t d(OFF)	Turn-off Delay Time(1)	R_{GEN} =10 Ω , R_L =0.6 Ω		74		ns
tf	Turn-off Fall Time(1)			41		
Qg	Total Gate Charge(1)	V _{DD} =20V, V _{GS} = 10V,		51		
Qgs	Gate-Source Charge(1)	I _{DS} =70A		15		nC
Qgd	Gate-Drain Charge(1)			8		
Diode Charact	eristics					
VsD	Diode Forward Voltage(2)	I _{SD} = 70A, V _{GS} = 0			1.2	V
trr	Reverse Recovery Time	1 -70A dl /dt-400A/ -		49.2		ns
Q rr	Reverse Recovery Charge	I _{SD} =70A, dI _{SD} /dt=100A/μs		35.9		nC

NOTES:

- 1. Independent of operating temperature.
- 2. Pulse Test : Pulse width $\,\leqslant\,\,300\,\mu$ s, Duty cycle $\,\leqslant\,\,2\%$



Typical Performance Characteristics

Figure 1: On-Region Characteristics

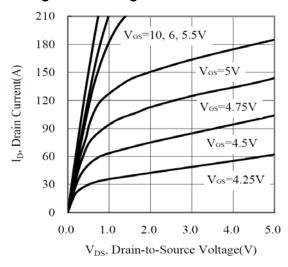


Figure 3: Drain Current

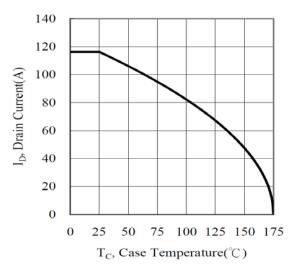


Figure 5: Capacitance Characteristics

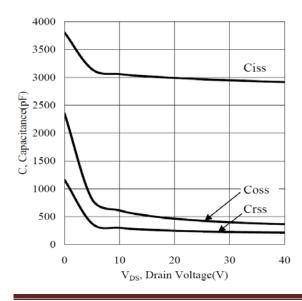


Figure 2: Power Dissipation

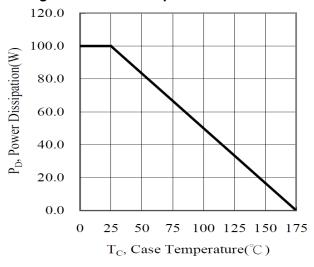


Figure 4: Gate Threshold Voltage

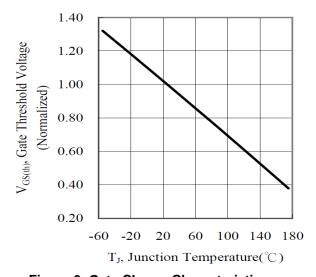


Figure 6: Gate Charge Characteristics

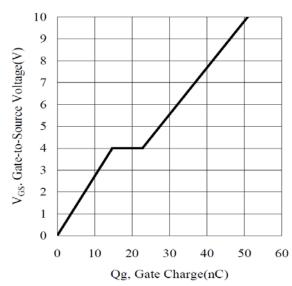




Figure 7: On-Resistance Variation vs.Temperature

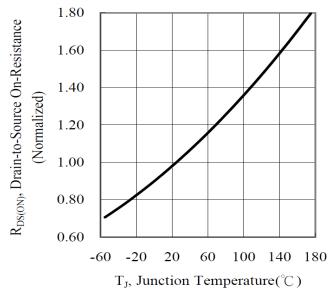


Figure 9: Avalance Characteristics

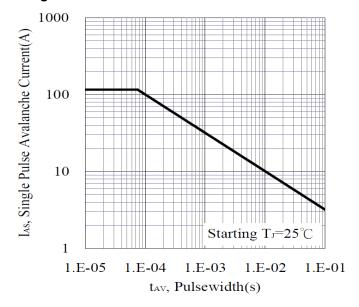
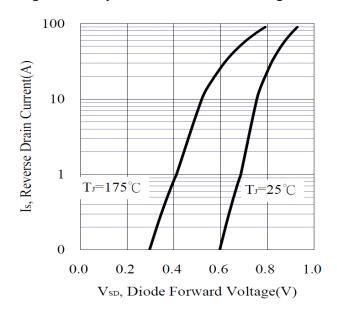


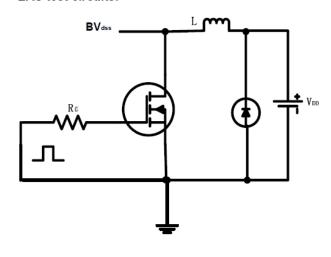
Figure 8: Body Diode Forward Voltage



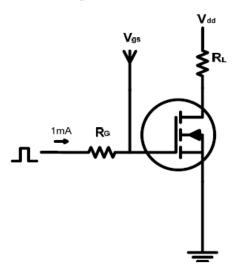


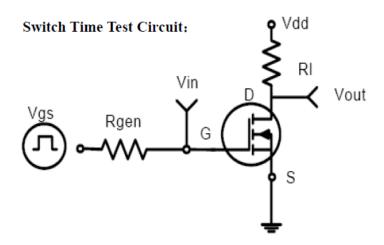
Test circuits and Waveforms

EAS test circuits:

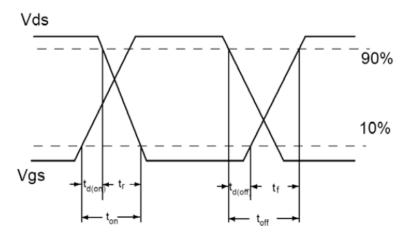


Gate charge test circuit:



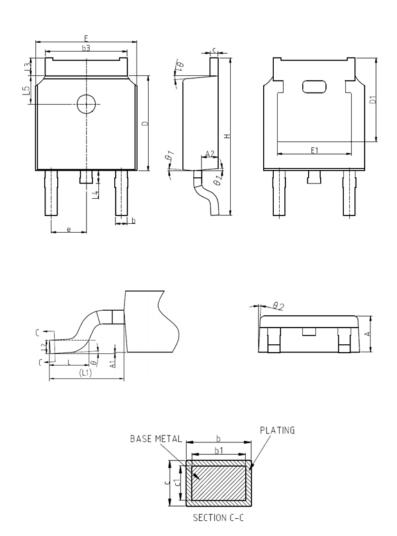


Switch Waveforms:





PACKAGE MECHANICAL DATA TO-252-2 Package Dimension



	Dimens	sions In	Dimensions In			
Symb	Millimeters		Inches			
ol	Min.	Max.	Min.	Max.		
Α	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
A2	0.970	1.170	0.038	0.046		
b	0.720	0.850	0.028	0.033		
b1	0.700	0.900	0.028	0.035		
b3	5.230	5.330	0.205	0.210		
С	0.430	0.580	0.017	0.023		
c1	0.430	0.580	0.017	0.023		
D	6.000	6.200	0236	0.244		
D1	5.300	TYP.	0.208 TYP.			
Е	6.500	6.600	0.255	0.260		
E1	4.700	4.920	0.185	0.193		
е	2.286	2.286TYP.		0.089 TYP.		
Н	9.900	10.100	0.389	0.397		
L	1.400	1.700	0.055	0.066		
L1	2.900 REF.		0.114 REF.			
L2	0.510REF.		0.020REF.			
L3	0.900	1.250	0.035	0.049		
L4	0.600	1.000	0.023	0.039		
L5	1.700	1.900	0.067	0.074		
θ	0°	8°				
θ1	5°	9°				
θ2	5°	9°				



Notice

- 1. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any ADV products listed in this document, please confirm the latest product information with a ADV sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by ADV such as that disclosed through our website. (http://www.advsemi.com)
- 2. ADV has used reasonable care in compiling the information included in this document, but ADV assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
- 3. You should use the products described herein within the range specified by ADV, especially with respective the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. ADV shall have no liability for malfunctions or damages arising out of the use of ADV products beyond such specified ranges.
- 4. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application. ADV makes no representations, warranties or guaranties regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or ADV products.
- 5. Although ADV endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a ADV product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by
- 6. This document is provided for reference purposes only so that ADV customers may select the appropriate ADV products for their use. ADV neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of ADV or any third party with respect to the information in this document.
- 7. ADV shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts, programs, algorithms, and application circuit examples.
- 8. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from ADV.

7/7 www.advsemi.com Feb,2013 -Rev.3.02