

Description

The ACE78LXX series of positive regulators are available in the SOT-89-3 package and with 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V and 24V fixed output voltages, marking it useful in a wide range of applications. These regulators can provide local on-card regulation, eliminating the distribution problems associated with single point regulation. Each type employs internal current limiting, thermal shut-down and safe operating area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 100mA output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents. ACE78LXX is characterized for operation from 0°C to +125°C.

Features

- Internal Short-Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required

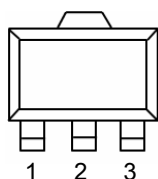
Absolute Maximum Ratings

Parameter		Max	Unit
Input Voltage	ACE78L05~10	30	V
	ACE78L12~18	35	
	ACE78L24	40	
Output current		100	mA
Operating junction temperature range		0 ~125	°C
Storage temperature range		- 55 ~ 150	°C
Power Dissipation		350*	mW

* When tested in free air condition, without heat sinking.

Packaging Type

SOT-89-3

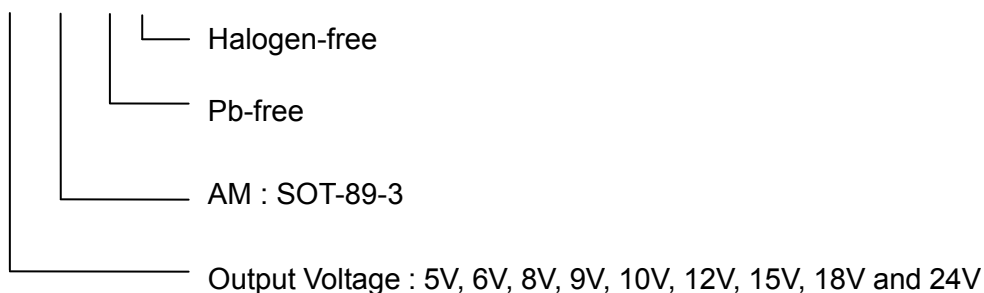


1	V _{OUT}
2	GND
3	V _{IN}

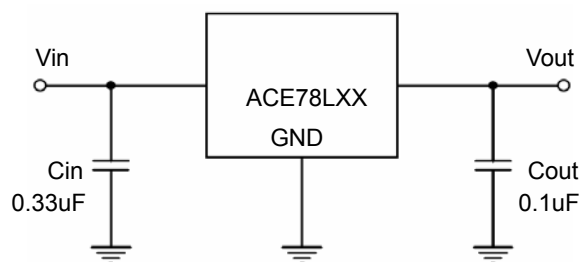
Ordering information

Selection Guide

ACE78L XX XX + H



Typical Application



Electrical Characteristics

ACE78L05 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=10\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)

(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	$V_{IN}=10\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$ $7\text{V} \leq V_{IN} \leq 20\text{V}$, $1\text{mA} \leq I_O \leq 40\text{mA}$	4.85	5.0	5.15	V
	B-Rank (5%)	$7\text{V} \leq V_{IN} \leq V_{max}$, $1\text{mA} \leq I_O \leq 70\text{mA}$ (Note2)	4.75		5.25	
ΔVO (Line Regulation)		$7\text{V} \leq V_{IN} \leq 20\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		18	75	mV
		$8\text{V} \leq V_{IN} \leq 20\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		10	64	
ΔVO (Load Regulation)		$V_{IN}=10\text{V}$, $1\text{mA} \leq I_O \leq 100\text{mA}$, $T_J=25^{\circ}\text{C}$		20	60	mV
		$V_{IN}=10\text{V}$, $1\text{mA} \leq I_O \leq 40\text{mA}$, $T_J=25^{\circ}\text{C}$		5	30	
IQ		$V_{IN}=10\text{V}$, $I_O=0\text{mA}$, $T_J=25^{\circ}\text{C}$		3.0	5.0	mA
ΔIQ		$V_{IN}=10\text{V}$, $1\text{mA} \leq I_O \leq 40\text{mA}$			0.1	mA
		$8\text{V} \leq V_{IN} \leq 20\text{V}$, $I_O=40\text{mA}$			1.0	
Vn		$10\text{Hz} \leq f \leq 100\text{KHz}$		40		μV
RR		$8\text{V} \leq V_{IN} \leq 20\text{V}$, $I_O=40\text{mA}$, $f=120\text{Hz}$, $T_J=25^{\circ}\text{C}$	47	62		dB
VD		$I_O=100\text{mA}$, $T_J=25^{\circ}\text{C}$		1.7		V
$\Delta VO/\Delta T_J$		$I_O=5\text{mA}$, $0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$		-0.65		$\text{mV}/^{\circ}\text{C}$

ACE78L06 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=12\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=12V, IO=40mA, TJ=25°C 8.5V≤VIN≤20V, 1mA≤IO≤40mA	5.82	6.0	6.18	V
	B-Rank (5%)	8.5V≤VIN≤Vmax, 1mA≤IO≤70mA (Note2)	5.70		6.30	
ΔVO (Line Regulation)		8.5V≤VIN≤20V, IO=40mA, TJ=25°C		65	175	mV
		9V≤VIN≤20V, IO=40mA, TJ=25°C		54	125	
ΔVO (Load Regulation)		VIN=12V, 1mA≤IO≤100mA, TJ=25°C		12.8	80	mV
		VIN=12V, 1mA≤IO≤70mA, TJ=25°C		5.8	40	
IQ		VIN=12V, IO=0mA, TJ=25°C		3.9	6.0	mA
ΔIQ		VIN=12V, 1mA≤IO≤40mA			0.1	mA
		9V≤VIN≤20V, IO=40mA			1.5	
Vn		10Hz≤f≤100KHz		49		μV
RR		10V≤VIN≤20V, IO=40mA, f=120Hz, TJ=25°C	40	46		dB
VD		IO=100mA, TJ=25°C		1.7		V
ΔVO/ΔTJ		IO=5mA, 0°C≤TJ≤125°C		0.75		mV/°C

ACE78L08 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=14\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=14V, IO=40mA, TJ=25°C 10.5V≤VIN≤23V, 1mA≤IO≤40mA	7.76	8.0	8.24	V
	B-Rank (5%)	10.5V≤VIN≤Vmax, 1mA≤IO≤70mA (Note2)	7.60		8.40	
ΔVO (Line Regulation)		10.5V≤VIN≤23V, IO=40mA, TJ=25°C		10	175	mV
		11V≤VIN≤23V, IO=40mA, TJ=25°C		8	125	
ΔVO (Load Regulation)		VIN=14V, 1mA≤IO≤100mA, TJ=25°C		15	80	mV
		VIN=14V, 1mA≤IO≤70mA, TJ=25°C		8	40	
IQ		VIN=14V, IO=0mA,TJ=25°C		2.0	5.5	mA
ΔIQ		VIN=14V, 1mA≤IO≤40mA			0.1	mA
		11V≤VIN≤23V, IO=40mA			1.5	
Vn		10Hz≤f≤100KHz		49		μV
RR		11V≤VIN≤21V, IO=40mA, f=120Hz,TJ=25°C	39	45		dB
VD		IO=100mA, TJ=25°C		1.7		V
ΔVO/ΔTJ		IO=5mA, 0°C≤TJ≤125°C		0.75		mV/°C

ACE78L09 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=15\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=15V, IO=40mA, TJ=25°C 11.5V≤VIN≤24V, 1mA≤IO≤40mA	8.73	9.0	9.27	V
	B-Rank (5%)	11.5V≤VIN≤Vmax, 1mA≤IO≤70mA (Note2)	8.55		9.45	
ΔVO (Line Regulation)		11.5V≤VIN≤24V, IO=40mA, TJ=25°C		90	200	mV
		13V≤VIN≤24V, IO=40mA, TJ=25°C		100	150	
ΔVO (Load Regulation)		VIN=15V, 1mA≤IO≤100mA, TJ=25°C		20	90	mV
		VIN=15V, 1mA≤IO≤40mA, TJ=25°C		10	45	
IQ		VIN=15V, IO=0mA,TJ=25°C		2.0	6.0	mA
ΔIQ		VIN=15V, 1mA≤IO≤40mA			0.1	mA
		13V≤VIN≤24V, IO=40mA			1.5	
Vn		10Hz≤f≤100KHz		49		μV
RR		12V≤VIN≤23V, IO=40mA, f=120Hz,TJ=25°C	38	44		dB
VD		IO=100mA, TJ=25°C		1.7		V
Δ VO/Δ TJ		IO=5mA, 0°C ≤ TJ≤ 125°C		0.75		mV/°C

ACE78L10 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=17\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=17V, IO=40mA, TJ=25°C 13V ≤ VIN ≤ 25V, 1mA ≤ IO ≤ 40mA	9.70	10.0	10.3	V
	B-Rank (5%)	13V ≤ VIN ≤ Vmax, 1mA ≤ IO ≤ 70mA (Note2)	9.50		10.5	
ΔVO (Line Regulation)		13V ≤ VIN ≤ 25V, IO=40mA, TJ=25°C		51	175	mV
		14V ≤ VIN ≤ 25V, IO=40mA, TJ=25°C		42	125	
ΔVO (Load Regulation)		VIN=17V, 1mA ≤ IO ≤ 100mA, TJ=25°C		20	90	mV
		VIN=17V, 1mA ≤ IO ≤ 40mA, TJ=25°C		11	40	
IQ		VIN=17V, IO=0mA, TJ=25°C		4.2	6.0	mA
ΔIQ		VIN=17V, 1mA ≤ IO ≤ 40mA			0.1	mA
		14V ≤ VIN ≤ 25V, IO=40mA			1.5	
Vn		10Hz ≤ f ≤ 100KHz		62		μV
RR		15V ≤ VIN ≤ 25V, IO=40mA, f=120Hz, TJ=25°C	37	44		dB
VD		IO=100mA, TJ=25°C		1.7		V

ACE78L12 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=19\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=19V, IO=40mA, TJ=25°C 14.5V≤VIN≤27V, 1mA≤IO≤40mA	11.64	12.0	12.36	V
	B-Rank (5%)	14.5V≤VIN≤Vmax, 1mA≤IO≤70Ma (Note2)	11.40		12.60	
ΔVO (Line Regulation)		14.5V≤VIN≤27V, IO=40mA, TJ=25°C		25	300	mV
		16V≤VIN≤27V, IO=40mA, TJ=25°C		20	250	
ΔVO (Load Regulation)		VIN=19V, 1mA≤IO≤100mA, TJ=25°C		25	150	mV
		VIN=19V, 1mA≤IO≤40mA, TJ=25°C		12	75	
IQ		VIN=19V, IO=0mA,TJ=25°C		2.0	6.0	mA
ΔIQ		VIN=19V, 1mA≤IO≤40mA			0.1	mA
		16V≤VIN≤27V, IO=40mA			1.5	
Vn		10Hz≤f≤100KHz		80		μV
RR		15V≤VIN≤25V, IO=40mA, f=120Hz,TJ=25°C	37	65		dB
VD		IO=100mA, TJ=25°C		1.7		V
ΔVO/ΔTJ		IO=5mA, 0°C≤TJ≤125°C		-1.0		mV/°C

ACE78L15 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=23\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	VIN=23V, IO=40mA, TJ=25°C 17.5V ≤ VIN ≤ 30V, 1mA ≤ IO ≤ 40mA 17.5V ≤ VIN ≤ Vmax, 1mA ≤ IO ≤ 70mA (Note2)	14.55	15.0	15.45	V
	B-Rank (5%)		14.25		15.75	
ΔVO (Line Regulation)		17.5V ≤ VIN ≤ 30V, IO=40mA, TJ=25°C		25	150	mV
		20V ≤ VIN ≤ 30V, IO=40mA, TJ=25°C		15	75	
ΔVO (Load Regulation)		VIN=23V, 1mA ≤ IO ≤ 100mA, TJ=25°C		20	150	mV
		VIN=23V, 1mA ≤ IO ≤ 70mA, TJ=25°C		25	150	
IQ		VIN=23V, IO=0mA, TJ=25°C		2.2	6.5	mA
ΔIQ		VIN=23V, 1mA ≤ IO ≤ 40mA			0.1	mA
		20V ≤ VIN ≤ 30V, IO=40mA			1.5	
Vn		10Hz ≤ f ≤ 100KHz		90		μV
RR		18.5V ≤ VIN ≤ 28.5V, IO=40mA, f=120Hz, TJ=25°C	34	63		dB
VD		IO=100mA, TJ=25°C		1.7		V
Δ VO/ Δ TJ		IO=5mA, 0°C ≤ TJ ≤ 125°C		-1.3		mV/°C

ACE78L18 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=27\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	$V_{IN}=27\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$ $21\text{V}\leq V_{IN}\leq 33\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$	17.46	18.0	18.54	V
	B-Rank (5%)	$21\text{V}\leq V_{IN}\leq V_{max}$, $1\text{mA}\leq I_O\leq 70\text{mA}$ (Note2)	17.10		18.9	
ΔV_O (Line Regulation)		$21\text{V}\leq V_{IN}\leq 33\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		145	300	mV
		$22\text{V}\leq V_{IN}\leq 33\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		135	250	
ΔV_O (Load Regulation)		$V_{IN}=27\text{V}$, $1\text{mA}\leq I_O\leq 100\text{mA}$, $T_J=25^{\circ}\text{C}$		30	170	mV
		$V_{IN}=27\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$, $T_J=25^{\circ}\text{C}$		15	85	
IQ		$V_{IN}=27\text{V}$, $I_O=0\text{mA}$, $T_J=25^{\circ}\text{C}$		2.0	6.0	mA
ΔI_Q		$V_{IN}=27\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$			0.1	mA
		$21\text{V}\leq V_{IN}\leq 33\text{V}$, $I_O=40\text{mA}$			1.5	
Vn		$10\text{Hz}\leq f\leq 100\text{KHz}$		150		μV
RR		$23\text{V}\leq V_{IN}\leq 33\text{V}$, $I_O=40\text{mA}$, $f=120\text{Hz}$, $T_J=25^{\circ}\text{C}$	34	48		dB
VD		$I_O=100\text{mA}$, $T_J=25^{\circ}\text{C}$		1.7		V
$\Delta V_O/\Delta T_J$		$I_O=5\text{mA}$, $0^{\circ}\text{C}\leq T_J\leq 125^{\circ}\text{C}$		-1.8		$\text{mV}/^{\circ}\text{C}$

ACE78L24 (Refer to the test circuits, $T_J=0\sim125^{\circ}\text{C}$, $I_O=40\text{mA}$, $V_{IN}=33\text{V}$, $C_{in}=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$ unless otherwise specified)
(Note1)

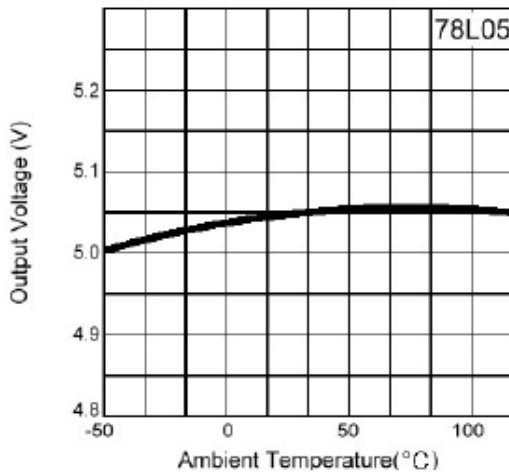
Symbol		Test Condition	Min	Typ	Max	Unit
VO	A-Rank (3%)	$V_{IN}=33\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$ $27\text{V}\leq V_{IN}\leq 38\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$	23.28	24.0	24.72	V
	B-Rank (5%)	$27\text{V}\leq V_{IN}\leq V_{max}$, $1\text{mA}\leq I_O\leq 70\text{mA}$ (Note2)	22.80		25.20	
ΔV_O (Line Regulation)		$27\text{V}\leq V_{IN}\leq 38\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		160	300	mV
		$28\text{V}\leq V_{IN}\leq 38\text{V}$, $I_O=40\text{mA}$, $T_J=25^{\circ}\text{C}$		150	250	
ΔV_O (Load Regulation)		$V_{IN}=33\text{V}$, $1\text{mA}\leq I_O\leq 100\text{mA}$, $T_J=25^{\circ}\text{C}$		40	200	mV
		$V_{IN}=33\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$, $T_J=25^{\circ}\text{C}$		20	100	
IQ		$V_{IN}=33\text{V}$, $I_O=0\text{mA}$, $T_J=25^{\circ}\text{C}$		2.2	6.0	mA
ΔI_Q		$V_{IN}=33\text{V}$, $1\text{mA}\leq I_O\leq 40\text{mA}$			0.1	mA
		$27\text{V}\leq V_{IN}\leq 38\text{V}$, $I_O=40\text{mA}$			1.5	
Vn		$10\text{Hz}\leq f\leq 100\text{KHz}$		200		μV
RR		$27\text{V}\leq V_{IN}\leq 38\text{V}$, $I_O=40\text{mA}$, $f=120\text{Hz}$, $T_J=25^{\circ}\text{C}$	34	45		dB
VD		$I_O=100\text{mA}$, $T_J=25^{\circ}\text{C}$		1.7		V
$\Delta V_O/\Delta T_J$		$I_O=5\text{mA}$, $0^{\circ}\text{C}\leq T_J\leq 125^{\circ}\text{C}$		-2.0		$\text{mV}/^{\circ}\text{C}$

Note 1: The Maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper of PCB. The data above represent pulse test conditions with junction temperatures specified at the initiation of test.

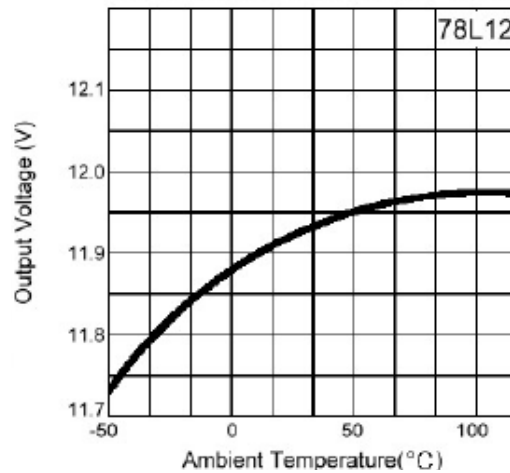
Note 2: Power dissipation<0.5W.

Characteristics Curve

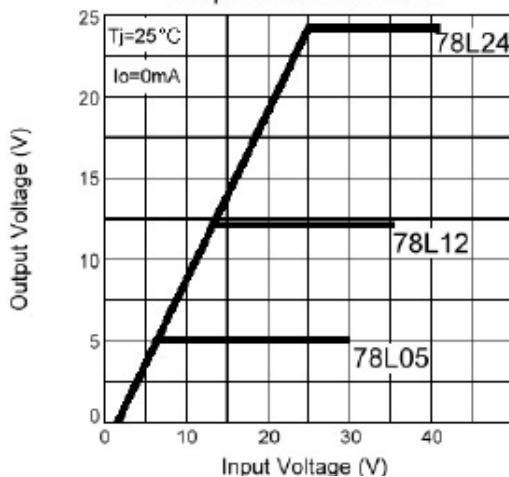
Output Voltage vs. Ambient Temp.



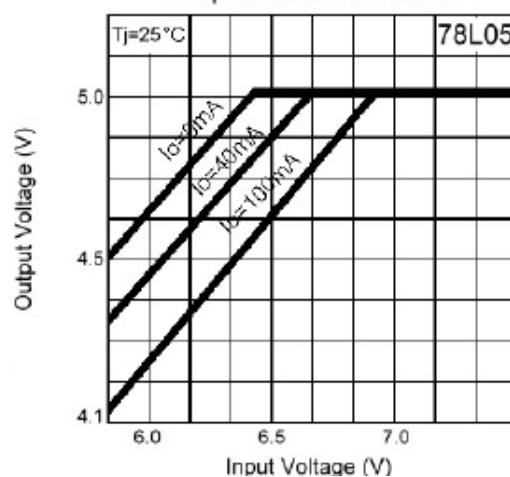
Output Voltage vs. Ambient Temp.



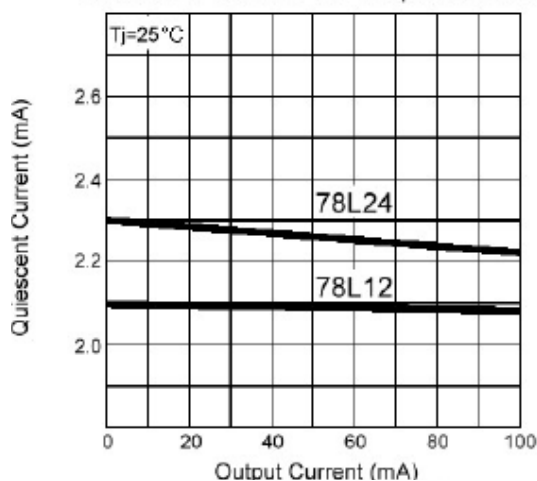
Output Characteristics



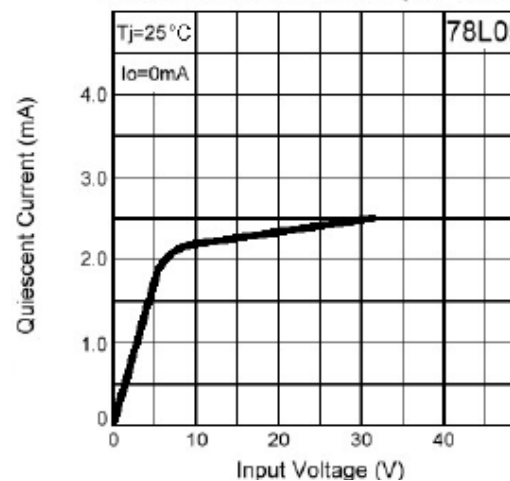
Dropout Characteristics



Quiescent Current vs. Output Current

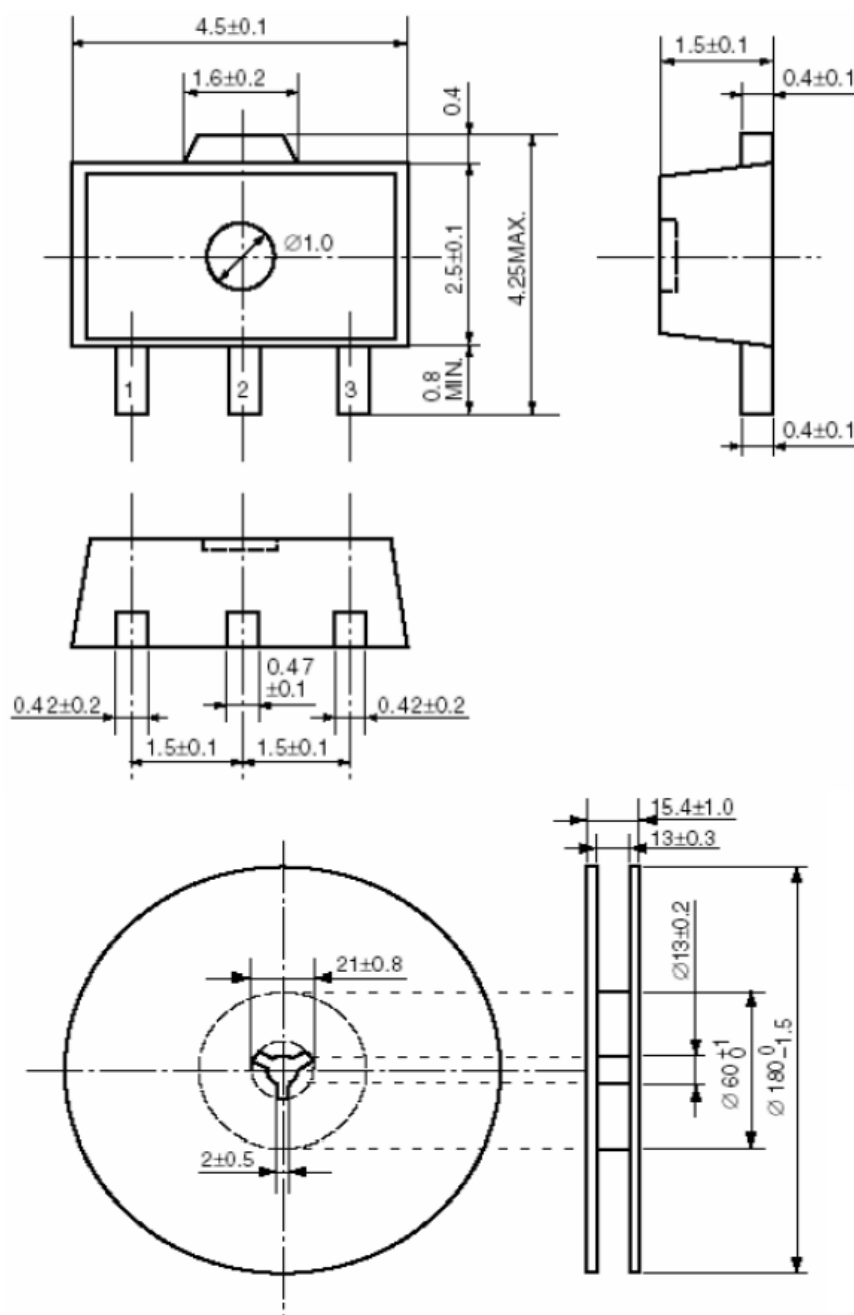


Quiescent Current vs. Input Voltage



Packing Information

SOT-89-3



Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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