



ACE1522B

P-Channel Enhancement Mode Field Effect Transistor

Description

Line current interrupter in telephone sets Relay High speed and line transformer drivers.

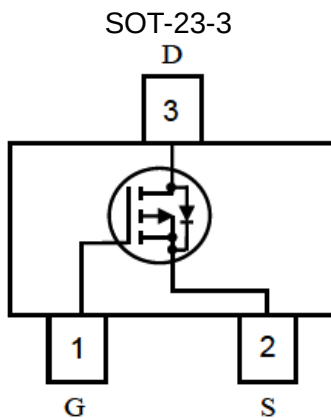
Features

- $V_{DS}(V) = -50V$, $I_D = -0.13A$
- $R_{DS(ON)} < 10\Omega @ V_{GS} = -5V$
- Voltage controlled p-channel small signal switch
- High density cell design for low $R_{DS(ON)}$

Absolute Maximum Ratings

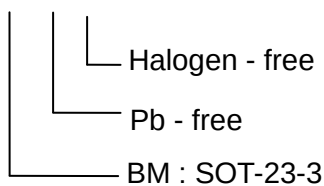
Parameter	Symbol	Max	Unit
Drain-Source Voltage	V_{DS}	-50	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current (Continuous)	I_D	-0.13	A
Drain Current (Pulse)	I_D	-0.52	
Power Dissipation ⁽¹⁾	P_D	0.35	W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^{\circ}C$

Packaging Type



Ordering information

ACE1522B XX + H





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Electrical Characteristics

$T_A=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On/Off characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250uA	-50			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-50V, V _{GS} =0V			-15	uA
		V _{DS} =-50V, V _{GS} =0V, T _J =125°C			-60	
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±10	nA
On characteristics ^b						
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =-5V, I _D =-0.1A			10	Ω
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-1mA	-0.8	-1.75	-2	V
Forward Transconductance	g _{FS}	V _{DS} =-25V, I _D =-0.1A	0.05	0.6		S
Switching characteristics ^b						
Total Gate Charge	Q _g	V _{DS} =-25V, I _D =-0.1A V _{GS} =-5V		0.9	1.3	nC
Gate-Source Charge	Q _{gs}			0.2		
Gate-Drain Charge	Q _{gd}			0.3		
Turn-On Delay Time	T _{d(on)}	V _{DD} =-30V, I _D =-0.27A, V _{GS} =-10V R _{GEN} =6Ω		2.5	5	ns
Turn-On Rise Time	t _f			6.3	13	
Turn-Off Delay Time	t _{d(off)}			10	20	
Turn-Off Fall Time	t _f			4.8	9.6	
Dynamic characteristics						
Input Capacitance	C _{iss}	V _{DS} =-10V, V _{GS} =0V f=200KHz		33		pF
Output Capacitance	C _{oss}			38		
Reverse Transfer Capacitance	C _{rss}			36		
Gate Resistance	R _G	V _{GS} =-15mV, f=1.0MHZ		9		Ω
Drain-source diode characteristics and maximum ratings ^b						
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-0.26A ⁽²⁾		-0.8	-1.2	V

Note: 1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2.0\%$



Typical Performance Characteristics

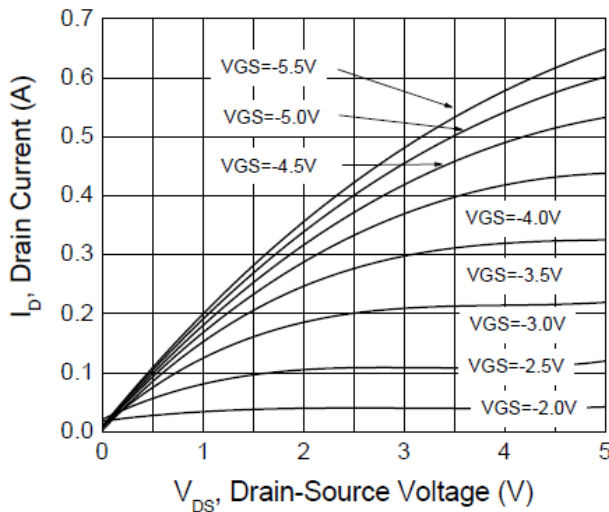


Figure 1. Output Characteristics

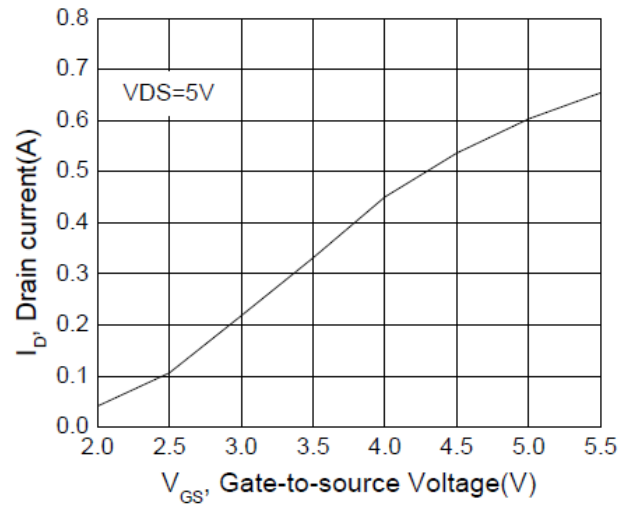


Figure 2. Transfer Characteristics

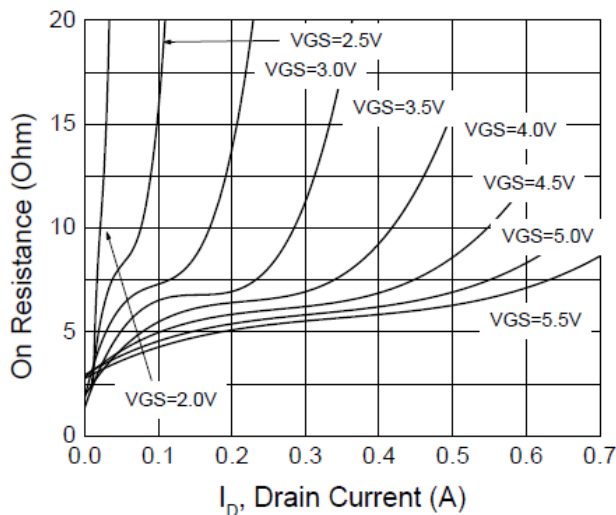


Figure 3. On Resistance VS I_d

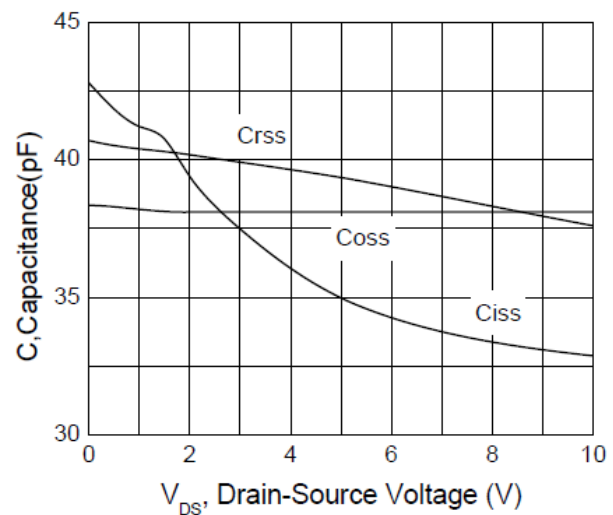


Figure 4. Capacitance

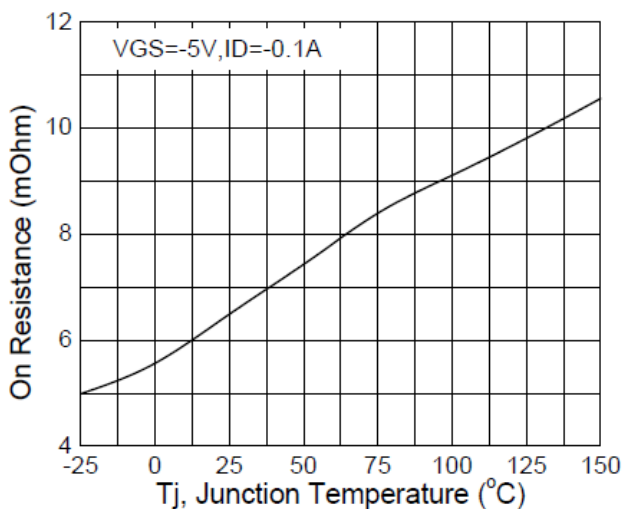


Figure 5. On Resistance Vs. Temperature

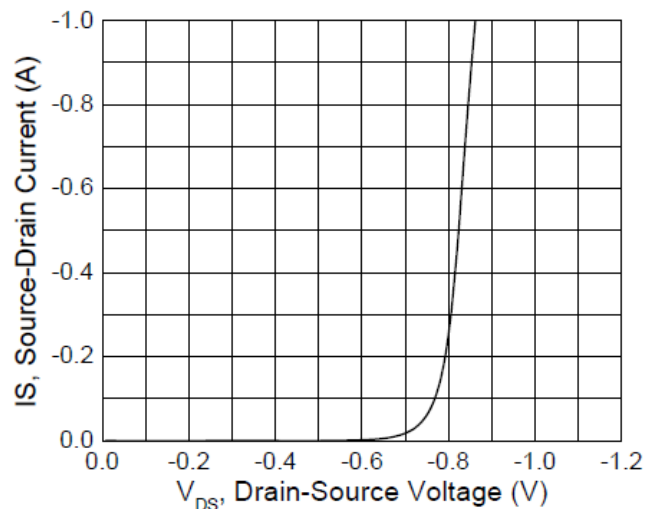
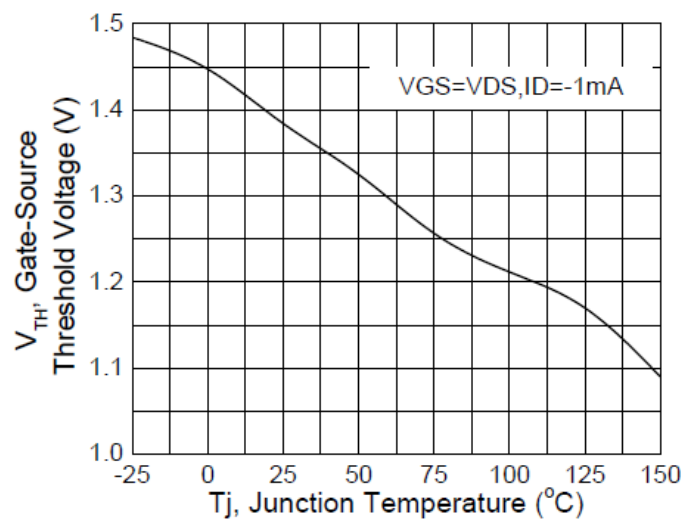
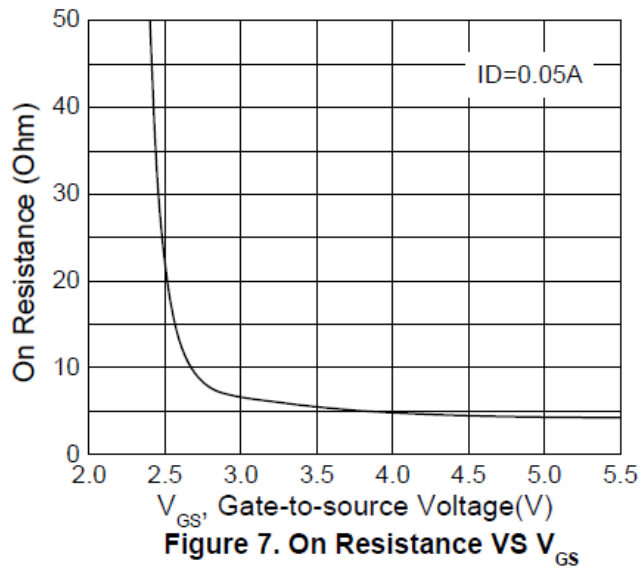


Figure 6. Source-Drain Diode Forward Voltage



Typical Performance Characteristics



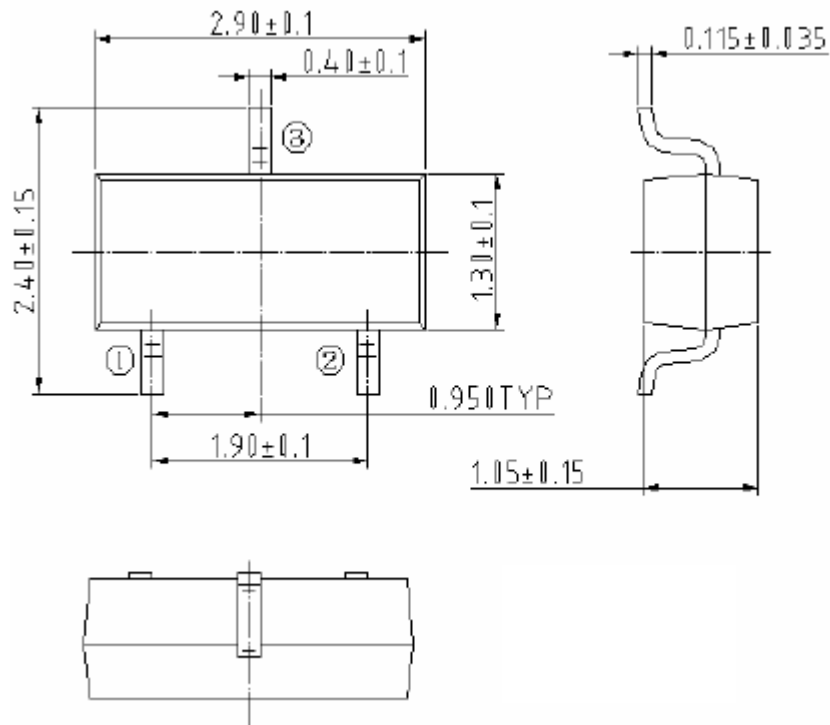


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Packing Information

SOT-23-3



Unit: mm



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Notes

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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