



DESCRIPTION

The A4783 is a low-Voltage, three-channel video amplifier with integrated reconstruction filter and input clamps. Specially suited for standard definition video signals, this device is ideal for a wide range of television and set-top box applications.

A4783 offers 6dB Gain rail-to-rail output driver and 5th order output reconstruction filter on all three channels. It has 8MHz -3dB bandwidth and 31.25V/ μ s slew rate. A4783 provides improved image quality compared with passive LC filters and discrete drivers solution.

A4783 can be DC-coupled or AC-coupled with input video signal, such as the output stage of DAC to eliminate out-of-band noise. The output in A4783 can be configured as DC or AC-coupled output. It operates over an ambient temperature range of -40°C to +85°C.

The A4783 is available in SOP8 package.

ORDERING INFORMATION

Package Type	Part Number	
SOP8	M8	A4783M8R
		A4783M8VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		
Suffix “ V ” means Halogen free Package		

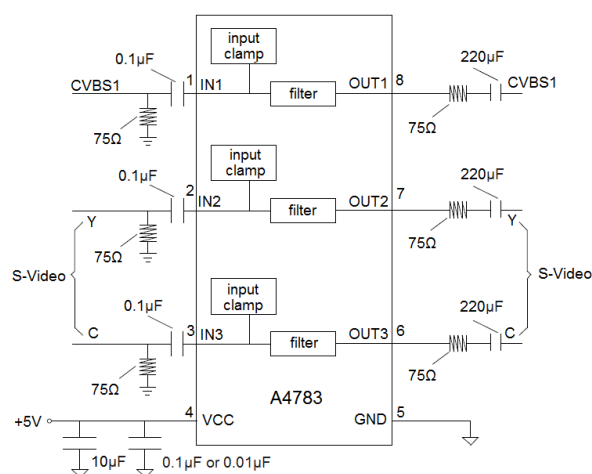
FEATURES

- Operation Temperature Range: -40°C to 85°C
- Three channel 5th Order 8MHz (SD) Filters
- Transparent Input Clamping
- 6dB Output Driver Gain
- Rail-to-Rail Output
- Input Voltage Range Includes Ground
- AC or DC Coupled Inputs / Outputs
- Operates from 3.3V to 5.5V Power Supply
- ESD: pass 8kV HBM test
- Available in SOP8 Package

APPLICATION

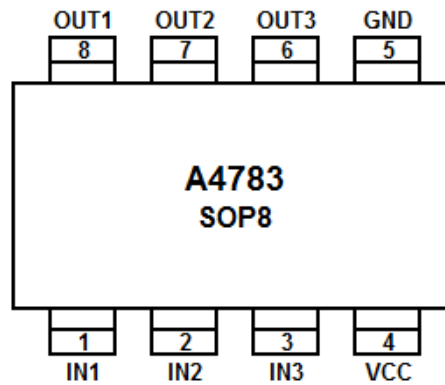
- Cable and Satellite Set-Top Boxes
- Video Amplifiers
- Communications Devices
- Personal Video Recorders
- Video on Demand
- DVD Players

TYPICAL APPLICATION





PIN DESCRIPTION



Top View

Pin #	Symbol	Function
1	IN1	Signal Input
2	IN2	Signal Input
3	IN3	Signal Input
4	V _{CC}	The power pad of the chip
5	GND	The ground pad of the chip
6	OUT3	Signal Output
7	OUT2	Signal Output
8	OUT1	Signal Output



ABSOLUTE MAXIMUM RATINGS

Supply Voltage V_{CC} to GND	-0.3V to 6V
Input Voltage	GND-0.3V to $V_{CC}+0.3V$
Storage Temperature Range	-65°C to 150°C
Work Temperature Range	-40°C to 85°C
T_A , Operating Temperature Range	-40°C to 85°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



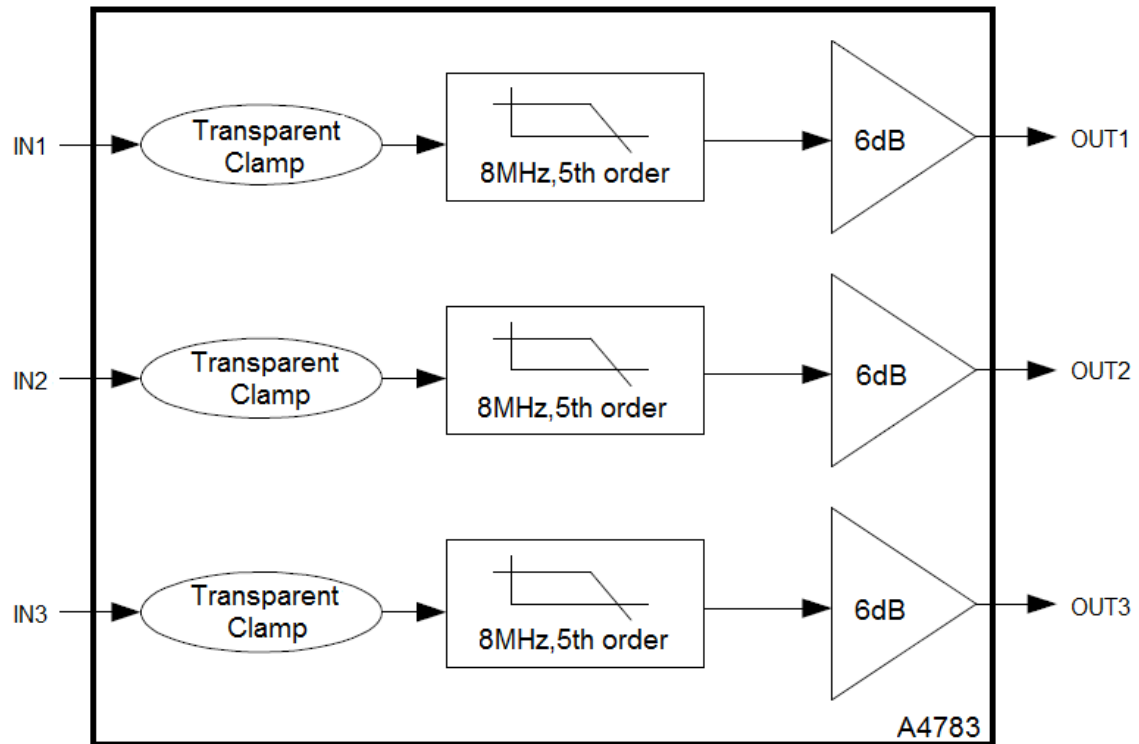
ELECTRICAL CHARACTERISTICS

At $V_{CC}=5V$, $R_L=150\Omega$ connected to GND, $V_{IN}=1V_{PP}$, and $C_{IN}=0.1\mu F$, all outputs AC coupled with $220\mu F$, referenced to 400kHz, unless otherwise noted

Parameter	Conditions		Min.	Typ.	Max.	Unit
DC ELECTRICAL CHARACTERISTICS						
I _Q	V _{IN} =0V	+25°C		24.5	28	mA
		-40 to +85°C			30	
V _{OLS}	V _{IN} =0V, No Load	+25°C		395	580	mV
		-40 to +85°C			670	
PSRR	DC	+25°C	58	69		dB
		-40 to +85°C	55			
Output Voltage High Swing	V _{IN} =3V, R _L =150Ω connected to GND	+25°C	4.4	4.76		V
		-40 to +85°C	4.3			
AC ELECTRICAL CHARACTERISTICS						
GAIN (A _v)	R _L =150Ωto GND	+25°C	5.7	6	6.4	dB
		-40 to +85°C	5.4		6.6	
BW (-0.1dB)	R _L =150Ωto GND	+25°C		5.5		MHz
BW (-3dB)	R _L =150Ωto GND	+25°C		7.5		MHz
Filter response	f _{IN} =27MHz	+25°C		55		dB
Crosstalk (channel to channel)	f=1MHz	+25°C		-66		dB
THD	f=1MHz, V _{OUT} =1.4V _{PP}	+25°C		0.2		%
SLEW RATE	2V outputstep 80% to 20%	+25°C		31.25		V/uS
Fall time	2V outputstep 80% to 20%	+25°C		38.4		nS
Rise time	2V outputstep 80% to 20%	+25°C		38.4		nS
D/DT	difference from 400KHz to 6.5MHz	+25°C		31		nS
DG	PAL DC coupled	+25°C		0.6		%
	PAL AC coupled			0.88		
DP	PAL DC coupled	+25°C		0.88		°C
	PAL AC coupled			1.6		



BLOCK DIAGRAM





DETAILED INFORMATION

Typical Application Diagram

The following schematic in Figure 2 is normally used for AC coupled output and DC-coupled input with DAC which has an output voltage range of 0V to 1.4V. AC coupled output offer slightly lower power dissipation and high ESD protection ability. The schematic in Figure 1 is also popular in design.

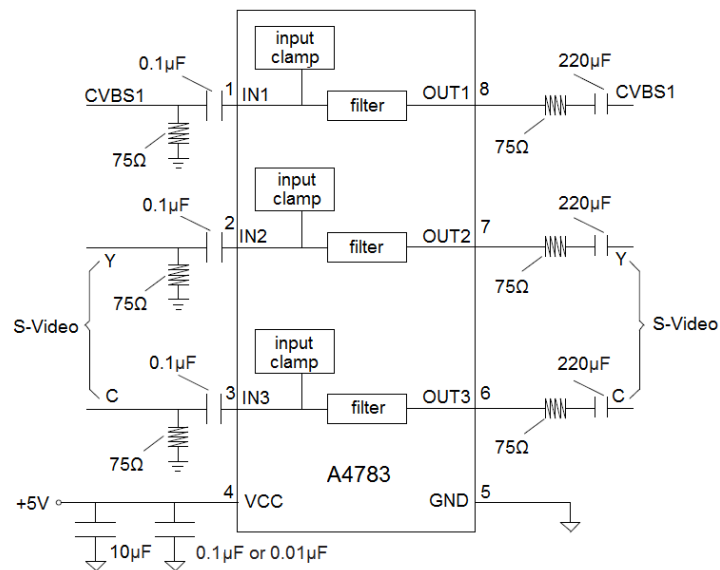


Figure 1 AC Coupling Application Schematic

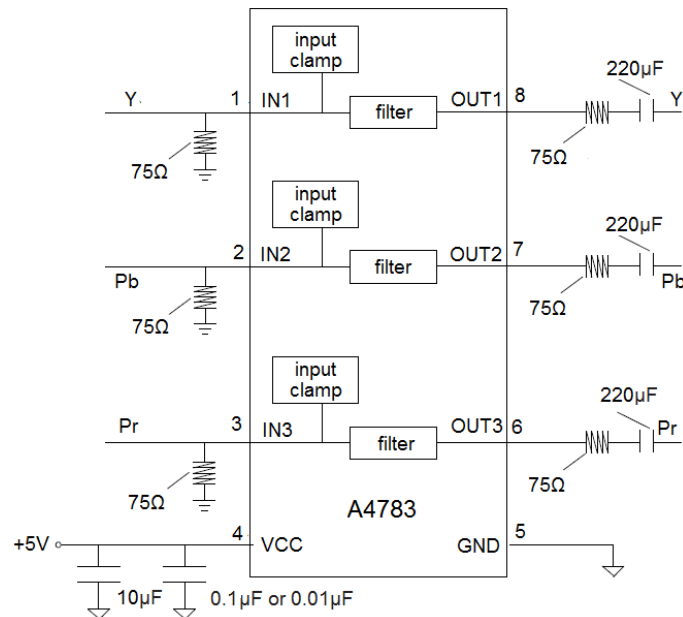


Figure 2 Input DC Coupling Output AC Coupling



Functional Description

A4783 operates from a single 3.3V to 5V supply. In application, A4783 is a fully integrated solution for filtering and buffering SDTV signals in front of video decoder or behind video encoder. For example, A4783 can replace three passive LC filters and three amplifier drivers at CVBS and S-video output side in set-top box and DVD player, this solution can help you save PCB size and production cost, it also improves video signal performance comparing with traditional design using discrete components. A4783 features a DC-coupled input buffer, 5-pole low-pass filter to eliminate out-of-band noise of video encoder, and a gain of +6dB in the output amplifier to drive 75Ω load. The AC or DC-coupled input buffer eliminates sync crush, droop, and field tilt. The output of A4783 also can be DC-coupled or AC-coupled.

Input Considerations

Besides AC coupling, the A4783 inputs also can be DC-coupled. In DC coupling application, No input coupling capacitors are needed because the amplitude of input video signal from DAC includes ground and extends up to 1.4V, then A4783 can be directly connected to the output of a single-supply, current-output DAC without any external bias network. Some time, if DAC's output level exceeds the range of 0V to 1.4V, or A4783 is driven by an unknown external source or a SCART switch which has its own clamping circuit, AC coupling is needed in such applications.

Output Considerations

The A4783 outputs can be DC-coupled or AC-coupled. When 0V is input, the A4783 output voltage is 401mV typically. In DC coupling design, one 75Ω resistor is used to connect A4783's output pin with external load directly, this serial back-termination resistor is used to match the impedance of the transmission line between A4783 and external load to cancel the signal reflection. The A4783 outputs can sink and source current allowing the device to be AC-coupled with external load, in AC coupling, 220μF at least capacitor will be used in order to eliminate field tilt.

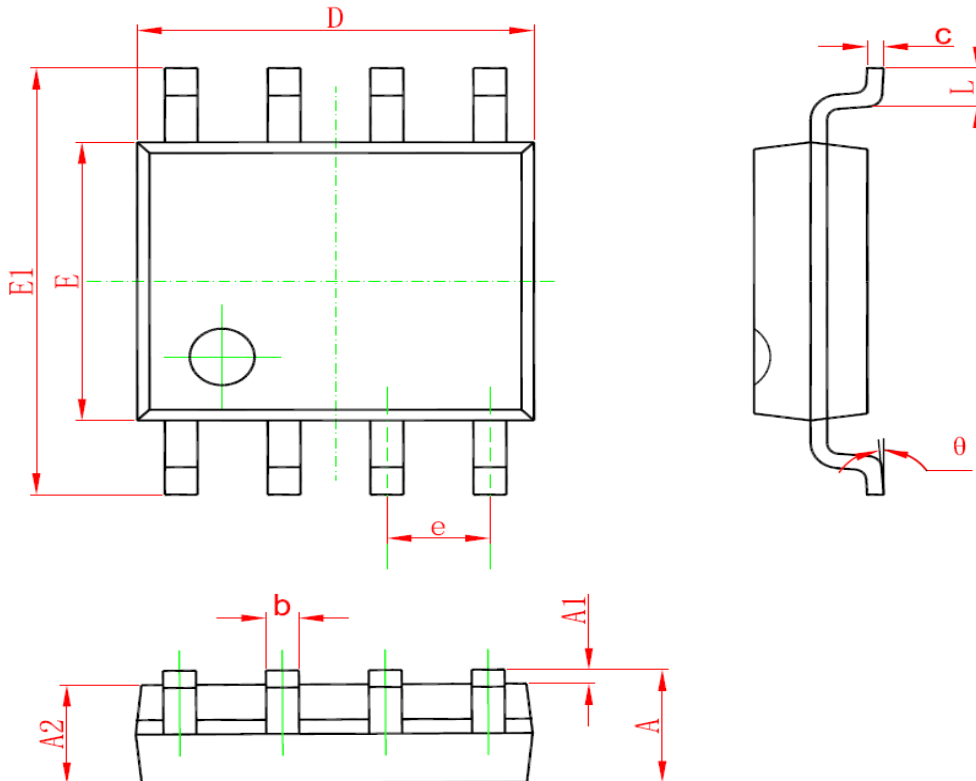
Power-Supply Bypassing and Layout

Correct power supply bypassing is very important for optimizing video performance in design. One 0.1μF and one 10μF capacitors are always used to Bypass V_{CC} pin of A4783, please place these two capacitors as close to the A4783 output pin as possible, a large ground plane is also needed to ensure optimum performance. The input and output termination resistors should be placed as close to the related pin of A4783 as possible to avoid performance degradation. The PCB traces at the output side should have 75Ω characteristic impedance in order to match the 75Ω characteristic impedance cable connecting external load. In design, please keep the board trace at the inputs and outputs of the A4783 as short as possible to minimize the parasitic stray capacitance and noise pickup.



PACKAGE INFORMATION

Dimension in SOP8 (Unit: mm)



Symbol	Min	Max
A	-	1.750
A1	0.100	0.230
A2	1.300	1.500
b	0.390	0.480
c	0.210	0.260
D	4.700	5.100
E	3.700	4.100
E1	5.800	6.200
e	1.270(BSC)	
L	0.500	0.800
θ	0°	8°



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