



QUADRUPLE 2-INPUT AND GATES

Description

The 74HC08 provides provides four independent 2-input AND gates with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 6.0V.

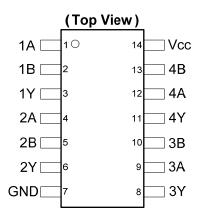
The gates perform the Boolean function:

$$Y = A \bullet B \text{ or } Y = \overline{\overline{A} + \overline{B}}$$

Features

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or Sources 4mA at V_{CC} = 4.5V
- CMOS Low Power Consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A)
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



SO-14 / TSSOP-14

Applications

- General Purpose Logic
- · Wide array of products such as:
 - PCs, Networking, Notebooks, Netbooks
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box

Notes:

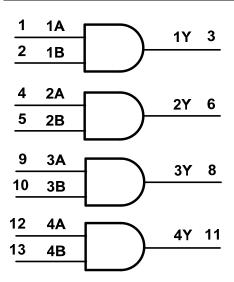
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н



Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	ESD HBM Human Body Model ESD Protection		KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current $V_I < -0.5V$ or $V_{CC} + 0.5V$	±20	mA
lok	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} +0.5V$	±20	mA
Io	Continuous output current -0.5V < V _O V _{CC} +0.5V	+/- 25	mA
Icc	Continuous current through V _{CC}	50	mA
I _{GND}	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG} Storage Temperature		-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Notes:

Recommended Operating Conditions (Note 6) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	Supply Voltage		2.0	6.0	V
VI	Input Voltage		0	V _{CC}	V
Vo	Output Voltage		0	V _{CC}	V
		$V_{CC} = 2.0V$		625	
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	V_{CC} = 4.5 V	_	140	ns/V
		$V_{CC} = 6.0V$	_	85	
T _A	Operating Free-Air Temperature		-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.

^{4.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{5.} Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cumbal	Parameter	Test Conditions		T _A = -40°	C to +85°C	to +85°C T _A = -40°C		Unit
Symbol	Parameter	rest Conditions	V _{CC}	Min	Max	Min	Max	Unit
			2.0V	1.5	_	1.5	_	
V _{IH} High-level Input Voltage			4.5V	3.15	_	3.15	_	V
			6.0V	4.2	_	4.2	_	
			2.0V	_	0.5	_	0.5	
V_{IL}	Low-level Input Voltage		4.5V	_	1.35	_	1.35	V
			6.0V	_	1.8		1.8	
		$I_{OH} = -20 \mu A$	2.0V	1.9		1.9	_	
	High-level Output Voltage	I _{OH} = -20μA	4.5V	4.4	_	4.4	_	V
V _{OH}		I _{OH} = -20μA	6.0V	5.9	_	5.9	_	
		$I_{OH} = -4.0 \text{mA}$	4.5V	3.84	_	3.7	_	
		I _{OH} = -5.2mA	6.0V	5.34	_	5.2	_	
		I _{OL} = 20μA	2.0V	_	0.1	-	0.1	
		$I_{OL} = 20\mu A$	4.5V	_	0.1	_	0.1	
V_{OL}	Low-level Output Voltage	I _{OL} = 20μA	6.0V	_	0.1	_	0.1	V
		I _{OL} = 4mA	4.5V	_	0.33	_	0.44	
		I _{OL} = 5.2 mA	6.0V	_	0.33	_	0.44	
IĮ	Input Current	V _I =GND to 5.5V	6.0V	_	± 1	_	± 1	μΑ
I _{CC}	Supply Current	$V_I = GND \text{ or } V_{CC},$ $I_O = 0$	6.0V	_	20	_	40	μA

Switching Characteristics

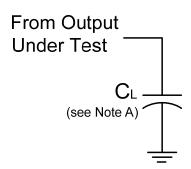
Symbol	Parameter	Test	Vcc		T _A = +25°(;	-40°C to +85°C	-40°C to +125°C	Unit
Syllibol	Parameter	Conditions	VCC	Min	Тур.	Max	Max	Max	Ullit
	Dropogation	Figure 1	2.0V	_	25	90	115	125	
t _{PD}	t_{PD} Propagation Figure 1 Delay A_N to Y_N $C_L = 50$ pl	Ŭ	4.5V	_	9	18	23	27	ns
		CL = 50PF	6.0V	_	7	15	20	23	
		Figure 1	2.0V	_	19	75	95	110	
t _t Transition Time	Figure 1 — C _L = 50pF —	4.5V	_	7	15	19	22	ns	
		CL = 30pi	6.0V	_	6	13	16	19	

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

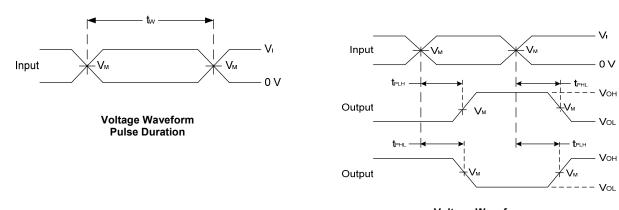
Parameter		Test Conditions	V _{CC} = 6V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1 MHz	20	pF
Cı	Input Capacitance	$V_1 = V_{CC} - \text{or GND}$	4	pF



Parameter Measurement Information



V _{CC}	Inputs		V _M	CL
	Vı	t _r /t _f		
2.0V to 6.0V	V _{CC}	6ns	V _{CC} /2	15pF,50pF



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

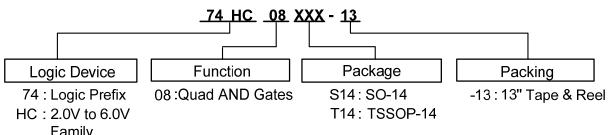
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz
- C. Inputs are measured separately one transition per measurement
- D. t_{PLH} and t_{PHL} are the same as t_{PD}

Figure 1 Load Circuit and Voltage Waveforms



Ordering Information

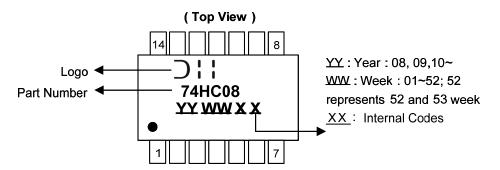


1 annly		
Device	Package Code	

	Davisa	Dookona Coda	Dookooina	7" Тар	e and Reel
	Device	Package Code	Packaging	Quantity	Part Number Suffix
O Green	74HC08S14-13	S14	SO-14	2500/Tape & Reel	-13
Green	74HC08T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14



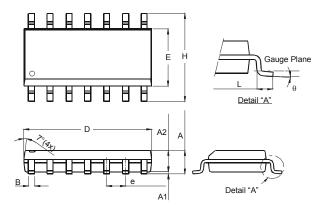
Part Number	Package
74HC08S14	SO-14
74HC08T14	TSSOP-14



Package Outline Dimensions (All dimensions in mm.)

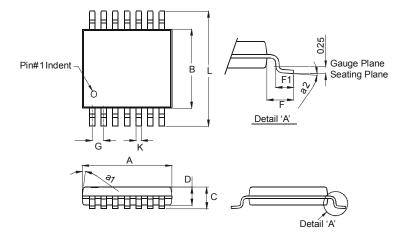
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45 Typ				
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
L	0.38	1.27			
θ	0°	8°			
All Di	All Dimensions in mm				

Package Type: TSSOP-14

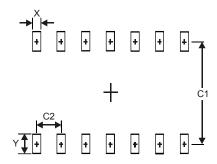


TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С	_	1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.

Package Type: SO-14

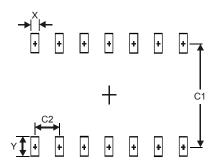


Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27



Suggested Pad Layout (cont.)

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Υ	1.45
C1	5.9
C2	0.65

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