## INTEGRATED CIRCUITS

## DATA SHEET

# 74ALS1518-input multiplexer

Product specification

1991 Feb 08

IC05 Data Handbook





## 8-input multiplexer

74ALS151

#### **FEATURES**

- 8-to-1 multiplexing
- On chip decoding
- Multi-function capability
- Complementary outputs
- See 74ALS251 for 3-State version

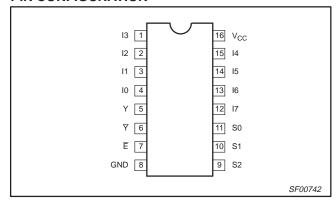
#### **DESCRIPTION**

The 74ALS151 is a logic implementation of a single 8-position switch with the switch position controlled by the state of three select (S0, S1, S2) inputs. True (Y) and complementary  $(\overline{Y})$  outputs are both provided.

The enable  $(\overline{E})$  is active-Low. When  $\overline{E}$  is High, Y output is Low and the  $\overline{Y}$  output is High regardless of all other inputs.

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74ALS151	8.0ns	8.0mA

#### **PIN CONFIGURATION**



#### **ORDERING INFORMATION**

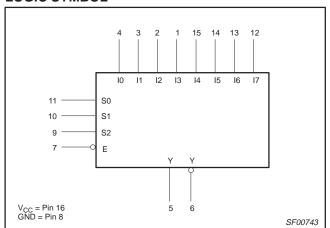
	ORDER CODE		
DESCRIPTION	COMMERCIAL RANGE $V_{CC}$ = 5V ±10%, $T_{amb}$ = 0°C to +70°C	DRAWING NUMBER	
16-pin plastic DIP	74ALS151N	SOT38-4	
16-pin plastic SO	74ALS151D	SOT109-1	

#### INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

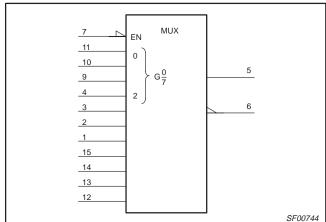
PINS	DESCRIPTION	74ALS (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW		
10 – 17	Data inputs	1.0/1.0	20μA/0.1mA		
S0 – S2	Select inputs	1.0/1.0	20μA/0.1mA		
₽	Enable input (active-Low)	1.0/1.0	20μA/0.1mA		
Y, $\overline{Y}$	Data outputs	130/240	2.6mA/24mA		

 $\textbf{NOTE:} \quad \text{One (1.0) ALS unit load is defined as: } 20 \mu \text{A in the High state and 0.1mA in the Low state.}$ 

#### **LOGIC SYMBOL**



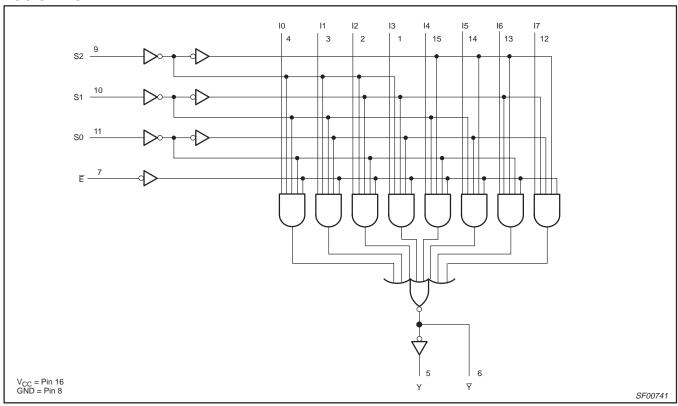
#### **IEC/IEEE SYMBOL**



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#### **LOGIC DIAGRAM**



#### **FUNCTION TABLE**

	INP	JTS		OUTF	PUTS
S2	S1	S0	Ē	Υ	Y
Х	Х	Х	Н	L	Н
L	L	L	L	10	Ī0
L	L	Н	L	I1	Ī1
L	Н	L	L	12	Ī2
L	Н	Н	L	13	Ī3
Н	L	L	L	14	Ī4
Н	L	Н	L	15	Ī5
Н	Н	L	L	16	Ī6
Н	Н	Н	Ĺ	17	Ī7

H = High voltage level
L = Low voltage level
X = Don't care

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#### **ABSOLUTE MAXIMUM RATINGS**

(Operation beyond the limit set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V <sub>CC</sub>	Supply voltage	-0.5 to +7.0	V
V <sub>IN</sub>	Input voltage	-0.5 to +7.0	V
I <sub>IN</sub>	Input current	-30 to +5	mA
V <sub>OUT</sub>	Voltage applied to output in High output state	–0.5 to V <sub>CC</sub>	V
I <sub>OUT</sub>	Current applied to output in Low output state	48	mA
T <sub>amb</sub>	Operating free-air temperature range	0 to +70	°C
T <sub>stg</sub>	Storage temperature range	-65 to +150	°C

#### RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER		LIMITS		UNIT
		MIN	NOM	MAX	
V <sub>CC</sub>	Supply voltage	4.5	5.0	5.5	V
V <sub>IH</sub>	High-level input voltage	2.0			V
V <sub>IL</sub>	Low-level input voltage			0.8	V
I <sub>lk</sub>	Input clamp current			-18	mA
I <sub>OH</sub>	High-level output current			-2.6	mA
I <sub>OL</sub>	Low-level output current		·	24	mA
T <sub>amb</sub>	Operating free air temperature range	0		+70	°C

#### DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

CVMDOL	DADAMETED	TEST COMPLETE	TEST CONDITIONS <sup>1</sup>				
SYMBOL	PARAMETER	TEST CONDITIO	MIN	TYP <sup>2</sup>	MAX	UNIT	
V	High-level output voltage	$V_{CC} = \pm 10\%, V_{IL} = MAX,$	$I_{OH} = -0.4$ mA	V <sub>CC</sub> – 2			V
V <sub>OH</sub>	nigri-ievei output voitage	V <sub>IH</sub> = MIN	I <sub>OH</sub> = MAX	2.4	3.2		V
V	Low-level output voltage	V <sub>CC</sub> = MIN, V <sub>IL</sub> = MAX,	I <sub>OL</sub> = 12mA		0.25	0.40	V
V <sub>OL</sub>	Low-level output voltage	V <sub>IH</sub> = MIN	I <sub>OL</sub> = 24mA		0.35	0.50	V
$V_{IK}$	Input clamp voltage	$V_{CC} = MIN, I_I = I_{IK}$			-0.73	-1.5	V
II	Input current at minimum input voltage	$V_{CC} = MAX, V_I = 7.0V$				0.1	mA
I <sub>IH</sub>	High-level input current	$V_{CC} = MAX, V_I = 2.7V$				20	μА
I <sub>IL</sub>	Low-level input current	$V_{CC} = MAX, V_I = 0.4V$				-0.1	mA
IO	Output current <sup>3</sup>	$V_{CC} = MAX, V_O = 2.25V$		-30		-112	mA
I <sub>CC</sub>	Supply current (total)	V <sub>CC</sub> = MAX			8.0	12	mA

#### NOTES:

- 1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- 2. All typical values are at  $V_{CC}$  = 5V,  $T_{amb}$  = 25°C.
- 3. The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

## 8-input multiplexer

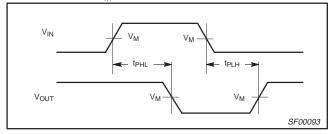
74ALS151

#### AC ELECTRICAL CHARACTERISTICS

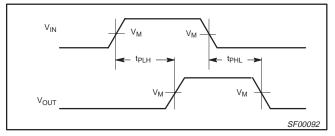
			LIM	ITS	
SYMBOL	PARAMETER	TEST CONDITION	T <sub>amb</sub> = 0°C V <sub>CC</sub> = +5. C <sub>L</sub> = 50pF,	UNIT	
			MIN	MAX	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay In to Y	Waveform 1	3.0 5.0	12.0 12.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay In to $\overline{Y}$	Waveform 2	3.0 5.0	15.0 15.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay Sn to Y	Waveform 1, 2	5.0 7.0	15.0 16.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay Sn to Y	Waveform 1, 2	5.0 5.0	15.0 16.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay E to Y	Waveform 1	4.0 4.0	12.0 12.0	ns
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation delay $\overline{\mathbf{E}}$ to $\overline{\mathbf{Y}}$	Waveform 1	4.0 5.0	12.0 14.0	ns

#### **AC WAVEFORMS**

For all waveforms,  $V_M = 1.3V$ .



Waveform 1. Propagation Delay for Inverting Output

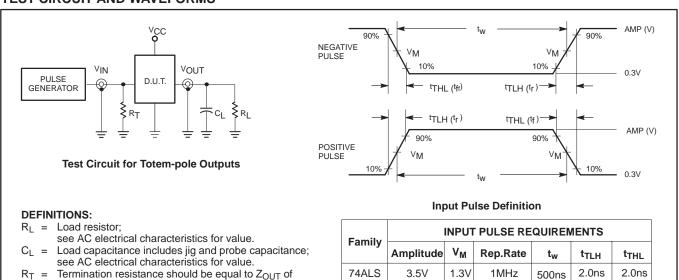


Waveform 2. Propagation Delay for Non-inverting Output

SC00005

#### **TEST CIRCUIT AND WAVEFORMS**

pulse generators.

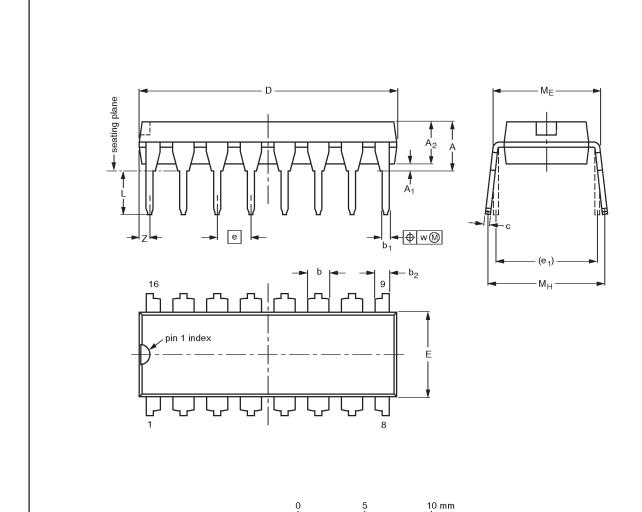


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#### DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4



## scale

#### DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub> min.	A <sub>2</sub> max.	b	b <sub>1</sub>	b <sub>2</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	е	e <sub>1</sub>	L	ME	M <sub>H</sub>	w	Z <sup>(1)</sup> max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	1.25 0.85	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	0.76
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.049 0.033	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.030

#### Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

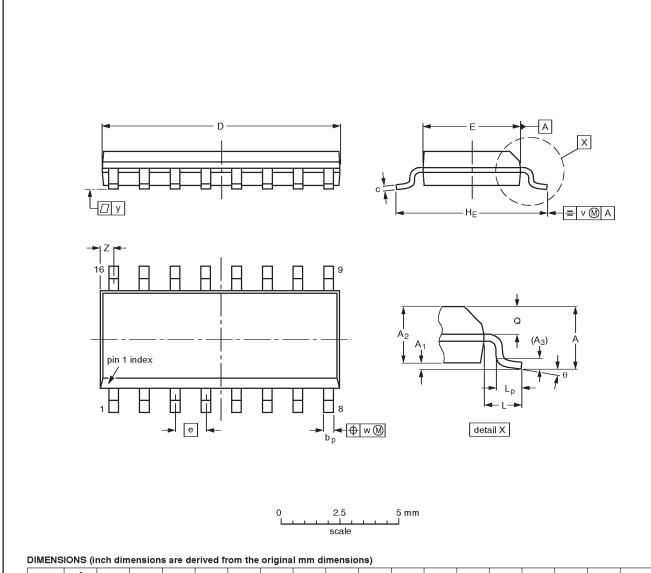
OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT38-4						<del>-92-11-17</del> 95-01-14

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#### SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



UNIT	A max.	Α1	A <sub>2</sub>	<b>A</b> <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	HE	٦	Lp	Q	v	w	у	Z <sup>(1)</sup>	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	10.0 9.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.0098 0.0039		0.01	l	0.0098 0.0075	0.39 0.38	0.16 0.15	0.050	0.24 0.23	0.041	0.039 0.016	0.028 0.020	0.01	0.01	0.004	0.028 0.012	0°

#### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT109-1	076E07S	MS-012AC				<del>91-08-13</del> 95-01-23

## 8-input multiplexer

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DEFINITIONS		
Data Sheet Identification	Product Status	Definition
Objective Specification	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.
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