

4.0A, 700V N-CHANNEL POWER MOSFET

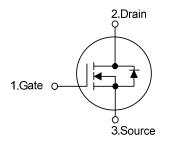
DESCRIPTION

The UTC **4N70-ML** is a high voltage power MOSFET combines advanced planar MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

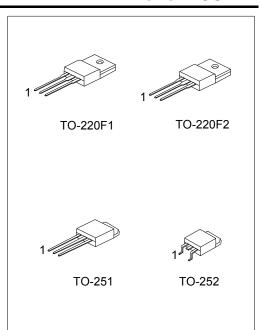
- * $R_{DS(ON)} \le 3.0 \ \Omega$ @ V_{GS} =10V, I_D =2.0A
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL



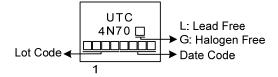
ORDERING INFORMATION

Ordering Number		Daakaga	Pin	Decking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N70L-TF1-T	TF1-T 4N70G-TF1-T		G	D	S	Tube	
4N70L-TF2-T	4N70L-TF2-T 4N70G-TF2-T		G	D	S	Tube	
4N70L-TM3-T	4N70G-TM3-T	TO-251	G	D	S	Tube	
4N70L-TN3-R 4N70G-TN3-R		TO-252	G	D	S	Tape Reel	
Note: Pin Assignment: G: Gate D: Drain S: Source							
4N70G-TF1-T	 (1) T: Tube, R: Tape Reel (2) TF1: TO-220F1, TF2: TO-220F2, TM3: TO-251 TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free 						



Power MOSFET

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	700	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current		I _D	4	А
Pulsed Drain Current (Note 2)		I _{DM}	8	А
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	163	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.6	V/ns
Power Dissipation	TO-220F1/TO-220F2	D	32	W
	TO-251/TO-252	PD	49	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

- 3. L = 30mH, I_{AS} = 3.3A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F1/TO-220F2	0	62.5	°C/W
	TO-251/TO-252	θ _{JA}	110	°C/W
Junction to Case	TO-220F1/TO-220F2	0	3.9	°C/W
	TO-251/TO-252	θις	2.55 (Note)	°C/W

Note: Device mounted on FR-4 substrate Pc board, 2oz copper, with 1inch square copper plate.



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PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		-			-		
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250µA	700			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =700V, V _{GS} =0V			10	μA
Gate- Source Leakage Current	Forward	– I _{GSS}	V _{GS} =30V, V _{DS} =0V			100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS		-					-
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250µA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =2.0A			3.0	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance	nput Capacitance				560		pF
Output Capacitance		C _{OSS}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		50		pF
Reverse Transfer Capacitance		C _{RSS}			4		рF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge (Note 1)		Q _G			13		nC
Gate-Source Charge		Q _{GS}	V _{DS} =560V, V _{GS} =10V, I _D =4A I _G =1mA (Note 1, 2)		4		nC
Gate-Drain Charge	ate-Drain Charge				2		nC
Turn-On Delay Time (Note 1)		t _{D(ON)}			8		ns
Turn-On Rise Time		t _R	V _{DS} =100V, V _{GS} =10V, I _D =4A,		16		ns
Turn-Off Delay Time		t _{D(OFF)}	R _G =25Ω (Note 1, 2)		36		ns
Turn-Off Fall Time	all Time				24		ns
DRAIN-SOURCE DIODE CHARA	ACTERISTICS	AND MAXI	MUM RATINGS				
Maximum Body-Diode Continuous Current		ls				4	Α
Maximum Body-Diode Pulsed Current		I _{SM}				8	Α
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =4A , V _{GS} =0V			1.4	V
Reverse Recovery Time (Note 1)		t _{rr}	I _S =4A , V _{GS} =0V		265		ns
Reverse Recovery Charge		Qrr	di/dt=100A/µs		4.5		μC

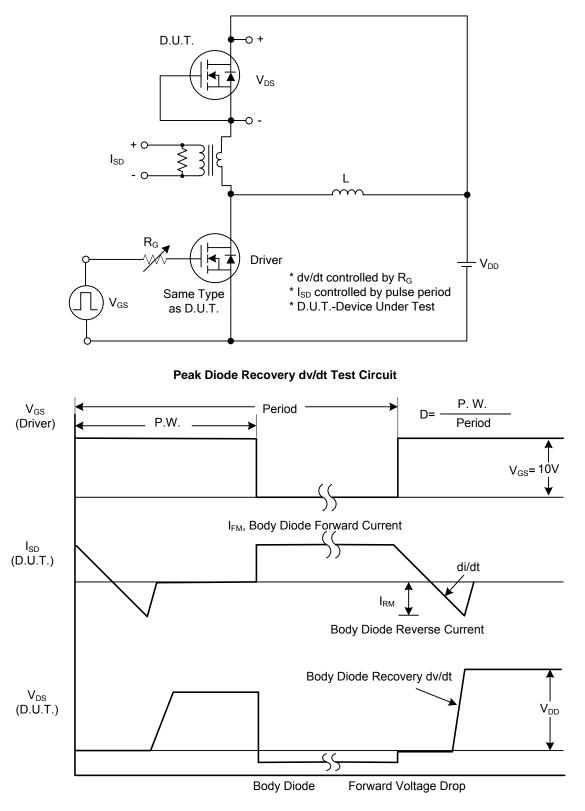
■ ELECTRICAL CHARACTERISTICS (TJ=25°C, unless otherwise specified)

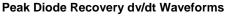
Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

2. Essentially independent of operating temperature.



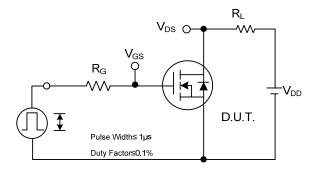
TEST CIRCUITS AND WAVEFORMS



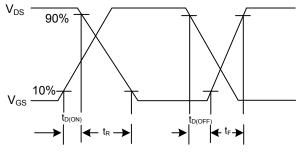




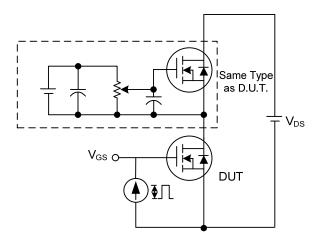
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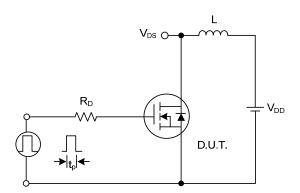




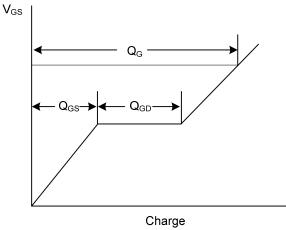
Switching Waveforms



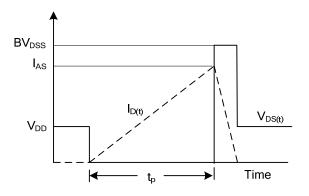
Gate Charge Test Circuit



Unclamped Inductive Switching Test Circuit





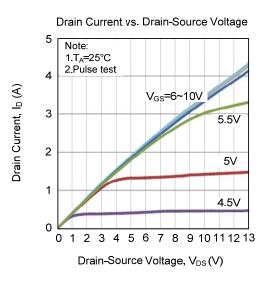


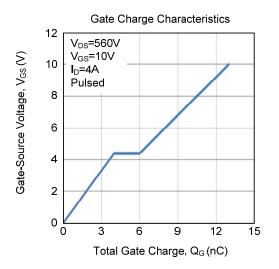
Unclamped Inductive Switching Waveforms

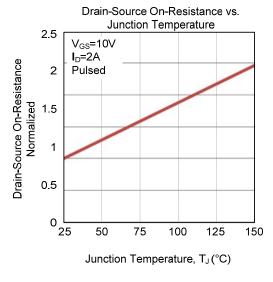


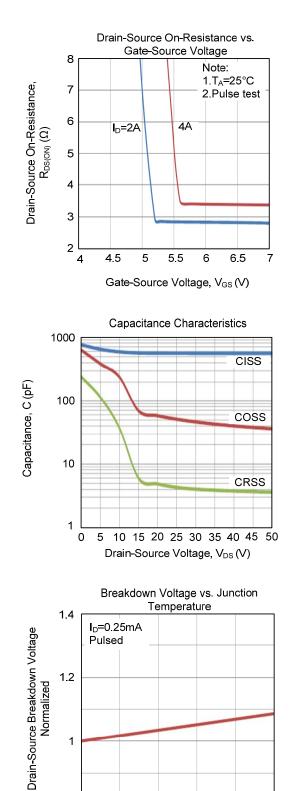
Power MOSFET

TYPICAL CHARACTERISTICS









0.8

25

50

75

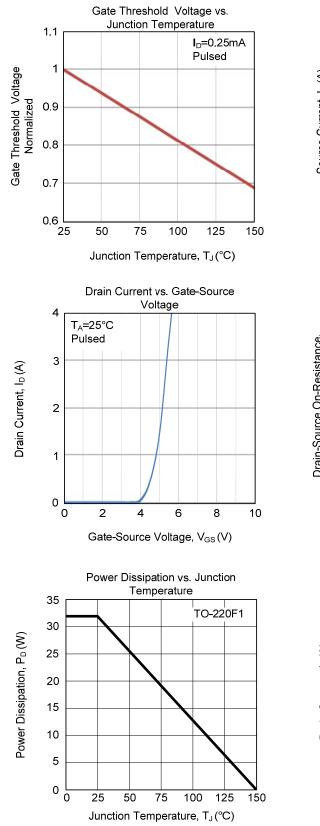
Junction Temperature, T_J (°C)

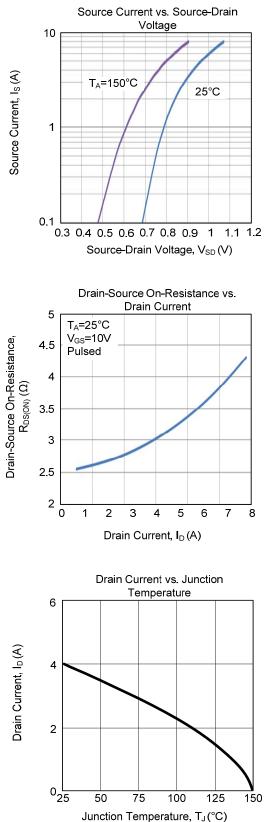
100

125

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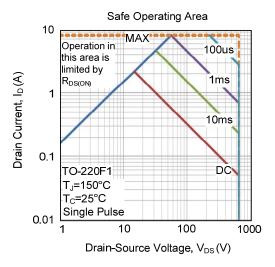
TYPICAL CHARACTERISTICS (Cont.)







TYPICAL CHARACTERISTICS (Cont.)



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