



Input voltage range up to 72 V DC
1 or 2 outputs up to 30 V DC
3500 V DC I/O electric strength test voltage

- High input to output isolation
- Wide input range
- Short circuit protection

Selection chart

Output 1 $U_{o \text{ nom}}$ [V DC] $I_{o \text{ nom}}$ [mA]	Output 2 $U_{o \text{ nom}}$ [V DC] $I_{o \text{ nom}}$ [mA]	Input voltage U_i [V DC]	Rated power $T_A = 71^\circ\text{C}$ $P_{o \text{ tot}}$ [W]	Type	Option
5 500	- -	9...36	2.5	24 IXP 3-05-7	S
5 500	- -	18...72	2.5	48 IXP 3-05-7	S
12 250	- -	9...36	3	24 IXP 3-12-7	S
12 250	- -	18...72	3	48 IXP 3-12-7	S
15 200	- -	9...36	3	24 IXP 3-15-7	S
15 200	- -	18...72	3	48 IXP 3-15-7	S
+5 250	-5 250	9...36	2.5	24 IXP 3-0505-7	S
+5 250	-5 250	18...72	2.5	48 IXP 3-0505-7	S
+12 125	-12 125	9...36	3	24 IXP 3-1212-7	S
+12 125	-12 125	18...72	3	48 IXP 3-1212-7	S
+15 100	-15 100	9...36	3	24 IXP 3-1515-7	S
+15 100	-15 100	18...72	3	48 IXP 3-1515-7	S
5 250	5 250	9...36	2.5	24 IXP 3-05-05-7	-
5 250	5 250	18...72	2.5	48 IXP 3-05-05-7	-
12 125	12 125	9...36	3	24 IXP 3-12-12-7	-
12 125	12 125	18...72	3	48 IXP 3-12-12-7	-
15 100	15 100	9...36	3	24 IXP 3-15-15-7	-
15 100	15 100	18...72	3	24 IXP 3-15-15-7	-

Input

Input voltage	continuous range, 24 V	9...36 V DC
	continuous range, 48 V	18...72 V DC

Reverse voltage protection shunt diode

Output

Output voltage setting accuracy	$U_{o \text{ nom}}, I_{o \text{ nom}}$	$\pm 2\% U_{o \text{ nom}}$
Minimum load	recommended	$20\% I_{o \text{ nom}}$
Line regulation	$U_{i \text{ min}} \dots U_{i \text{ max}}, I_{o \text{ nom}}$	$\pm 1\% U_{o \text{ nom}}$
Load regulation	$U_{i \text{ nom}}, 0 \dots 100\% I_{o \text{ nom}}$, single output models	$2\% U_{o \text{ nom}}$
	dual output models	max. 3% $U_{o \text{ nom}}$
Output voltage switching noise	$U_{i \text{ nom}}, 20 \dots 100\% I_{o \text{ nom}}$, peak-peak, total	max. 3% $U_{o \text{ nom}}$
Efficiency	$U_{i \text{ nom}}, I_{o \text{ nom}}$	up to 81%

Control and protection

Overload protection	$U_{i \text{ min}}$, full load	125% $P_{i \text{ nom}}$
No-load protection		
Remote Shut down	positive logic (floating or high signal = on)	

Safety and EMC

Electric strength test voltage	I/O	3500 V DC
Electromagnetic interference	conducted with external filter	class B

Environmental

Operating ambient temperature	$U_{i \text{ nom}}, I_{o \text{ nom}}$	-25...71°C
Storage temperature	non operational	-40...100°C
Relative humidity	non condensing	95%
MTBF	MIL-HDBK-217F, N2	>3'700'000 h

Options

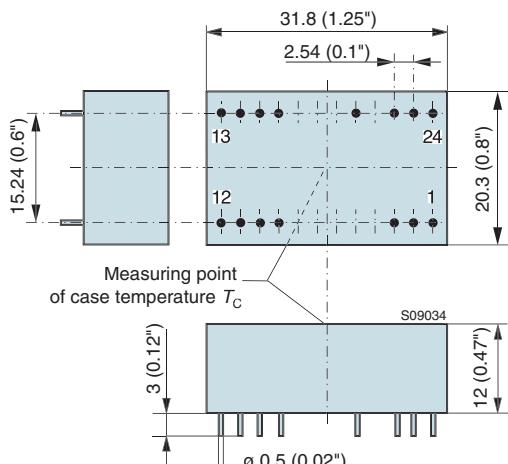
Industry standard pinout	S
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Accessories

DIN and chassis mounting bracket	
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Mechanical data

Tolerances ± 0.3 mm (0.012") unless otherwise indicated.

**Industry standard**

Pin	Single output	Dual output
2	Vi-	Vi-
3	Vi-	Vi-
9	n.c.	COM
10	n.c.	n.c.
11	n.c.	Vo-
14	Vo+	Vo+
15	n.c.	n.c.
16	Vo-	COM
22	Vi+	Vi+
23	Vi+	Vi+

Alternative pinout

Pin	Single output	Dual output	Double output
1	Vi+	Vi+	Vi+
2	Vi+	Vi+	Vi+
9	-	-	Go1
10	-	COM	-
11	-	COM	-
12	Vo-	-	Vo1
13	Vo+	Vo-	Vo2
15	-	Vo+	-
16	-	-	Go2
20	SD	SD	SD
23	Vi-	Vi-	Vi-
24	Vi-	Vi-	Vi-