

Description

The 3400L uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

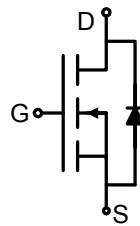
General Features

V_{DSS}	$R_{DS(ON)}$ @ 10V (Typ.)	$R_{DS(ON)}$ @ 4.5V (Typ.)	$R_{DS(ON)}$ @ 2.5V (Typ.)	I_D
30V	18 mΩ	21 mΩ	38 mΩ	5.6A

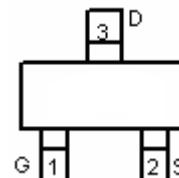
- High power and current handing capability
- Surface mount package
- RoHS Compliant

Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23-3L

Ordering Information

Part Number	Marking	Case	Packaging
3400L	3400L	SOT-23-3L	3000pcs/Reel

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	5.6	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	30	A
Maximum Power Dissipation	P_D	1.4	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	89	°C/W
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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}$ $I_D=250\mu\text{A}$	30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30\text{V}, V_{GS}=0\text{V}$	-	-	0.3	μA

Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics <small>(Note 3)</small>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.7	1.15	1.4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=2.5V, I_D=2.8A$	-	38	59	$m\Omega$
		$V_{GS}=4.5V, I_D=2.8A$	-	21	33	$m\Omega$
		$V_{GS}=10V, I_D=2.8A$	-	18	27	$m\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=5A$	10	-	-	S
Dynamic Characteristics <small>(Note 4)</small>						
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V, F=1.0MHz$	-	820	-	PF
Output Capacitance	C_{oss}		-	99	-	PF
Reverse Transfer Capacitance	C_{rss}		-	77	-	PF
Switching Characteristics <small>(Note 4)</small>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V, R_L=2.7\Omega$ $V_{GS}=10V, R_{GEN}=3\Omega$	-	3.3	-	nS
Turn-on Rise Time	t_r		-	4.8	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	26	-	nS
Turn-Off Fall Time	t_f		-	4	-	nS
Total Gate Charge	Q_g	$V_{DS}=15V, I_D=5.6A,$ $V_{GS}=4.5V$	-	9.5	-	nC
Gate-Source Charge	Q_{gs}		-	1.5	-	nC
Gate-Drain Charge	Q_{gd}		-	3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage <small>(Note 3)</small>	V_{SD}	$V_{GS}=0V, I_s=2A$	-	-	1	V
Diode Forward Current <small>(Note 2)</small>	I_s		-	-	5.6	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

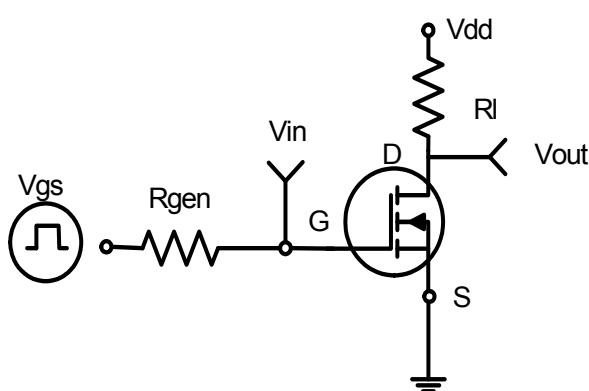


Figure 1:Switching Test Circuit

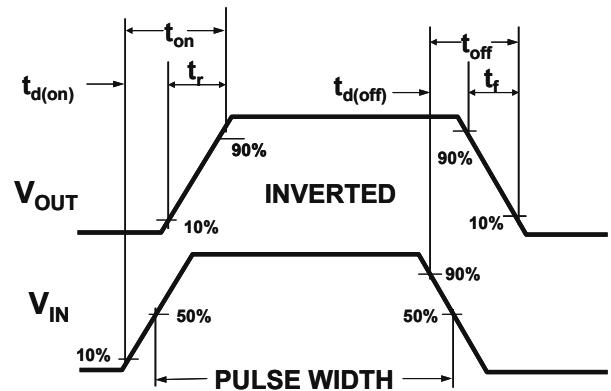


Figure 2:Switching Waveforms

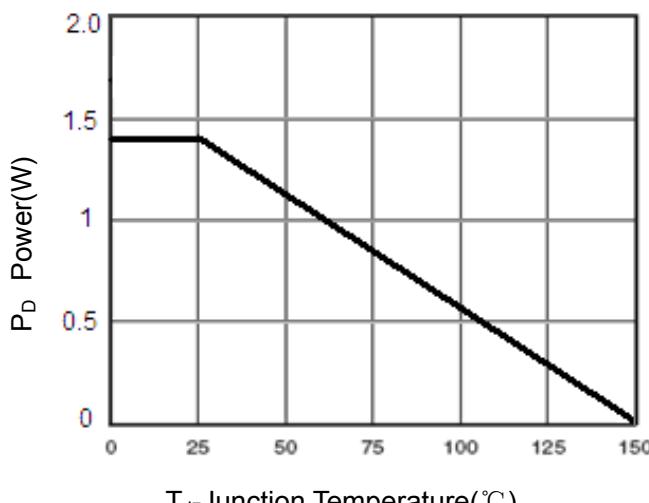


Figure 3 Power Dissipation

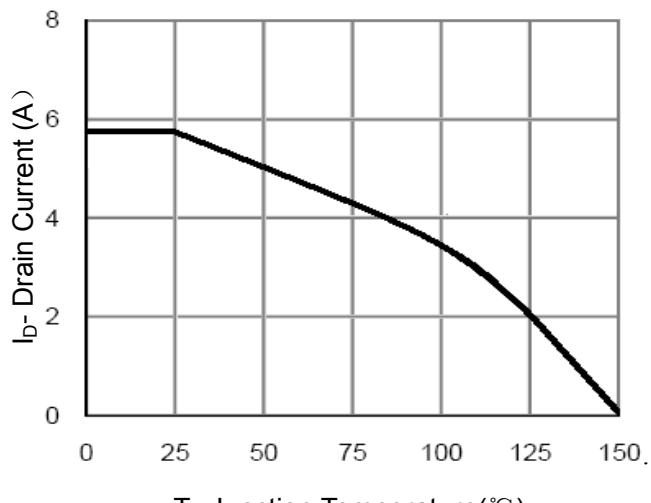


Figure 4 Drain Current

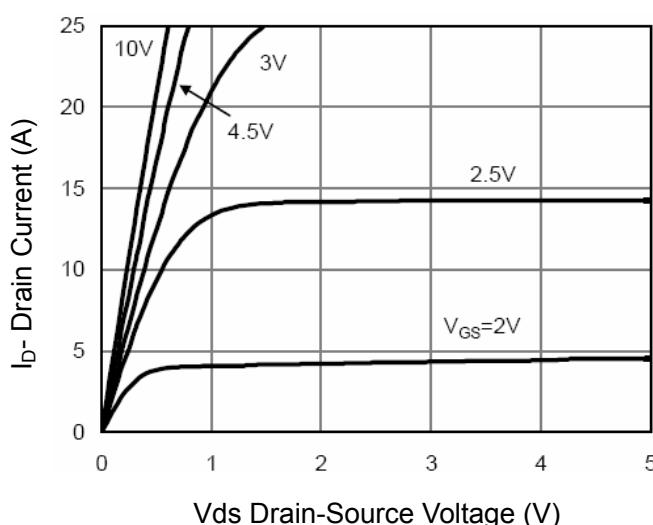


Figure 5 Output Characteristics

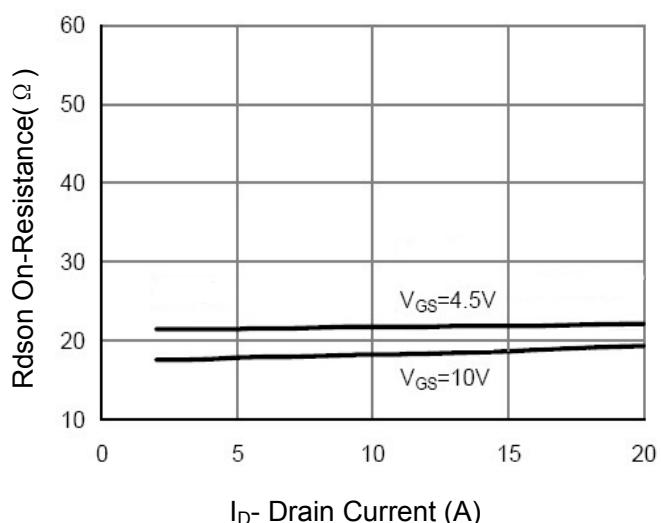


Figure 6 Drain-Source On-Resistance

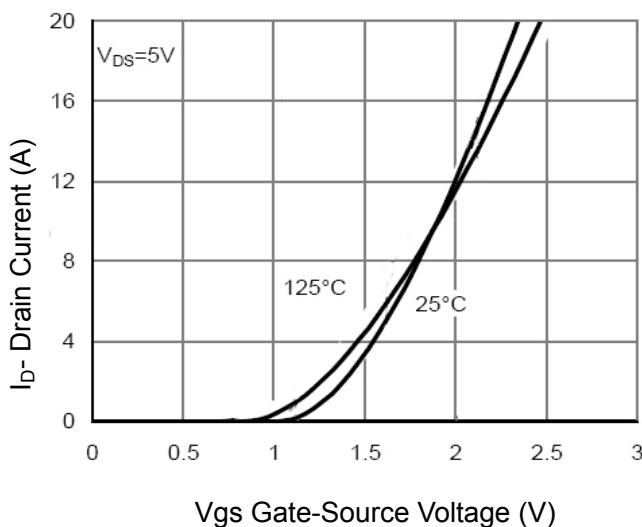


Figure 7 Transfer Characteristics

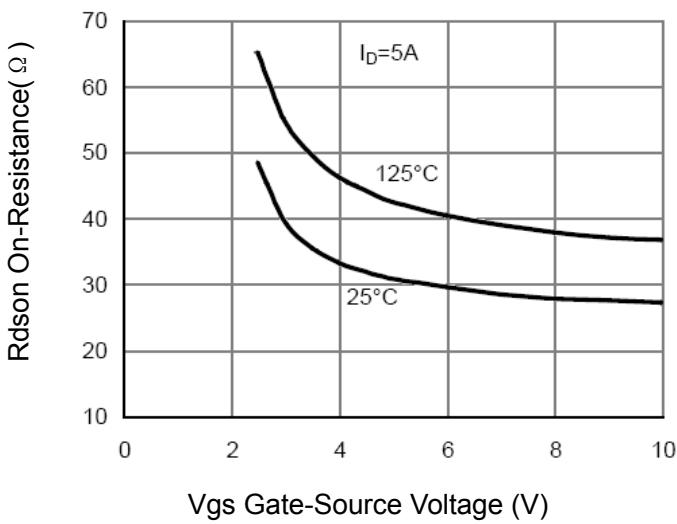


Figure 9 $R_{DS(on)}$ vs V_{GS}

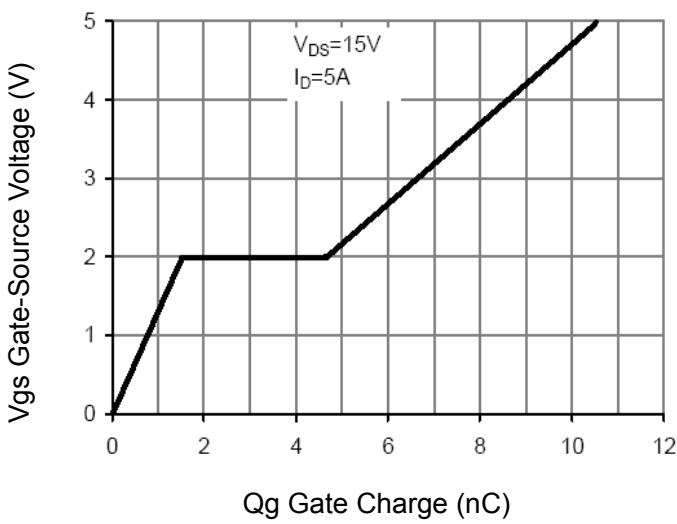


Figure 11 Gate Charge

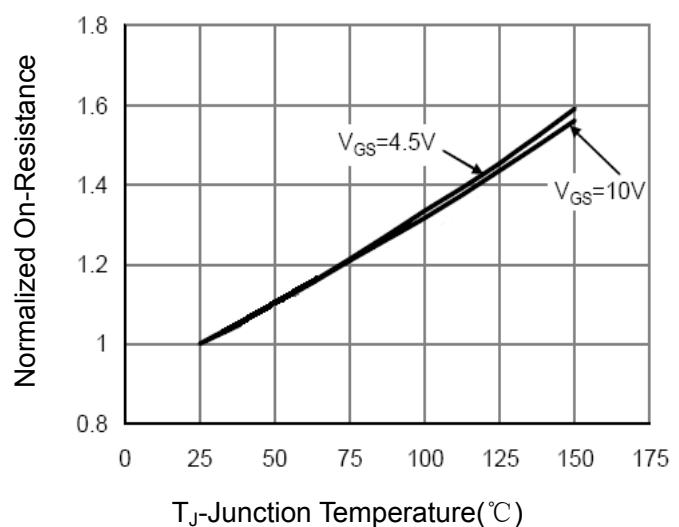


Figure 8 Drain-Source On-Resistance

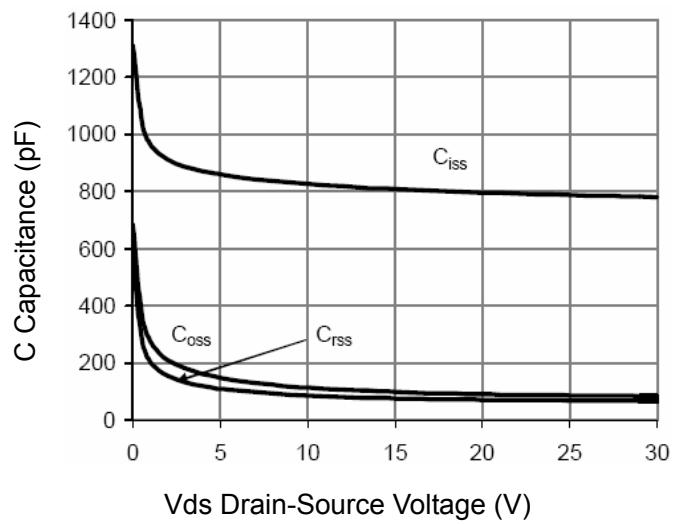


Figure 10 Capacitance vs V_{DS}

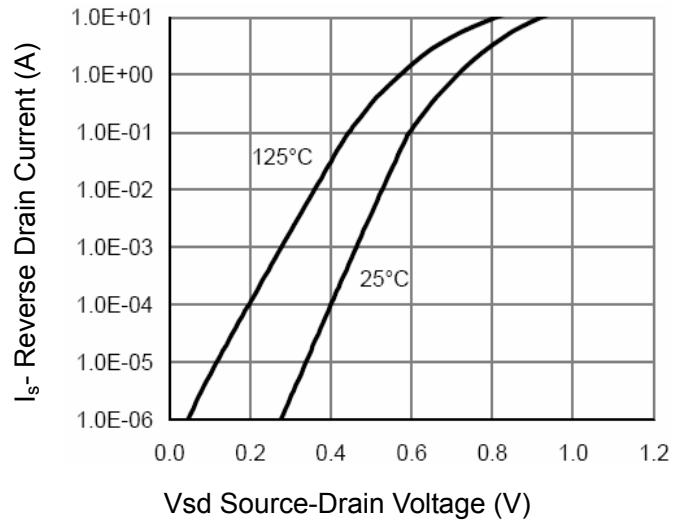


Figure 12 Source- Drain Diode Forward

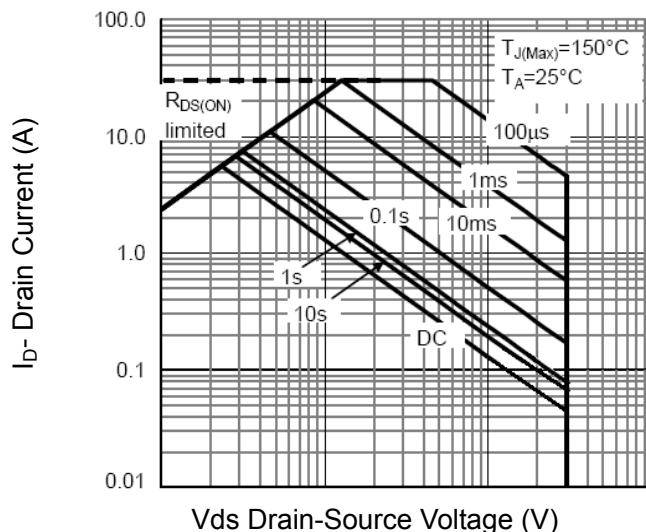


Figure 13 Safe Operation Area

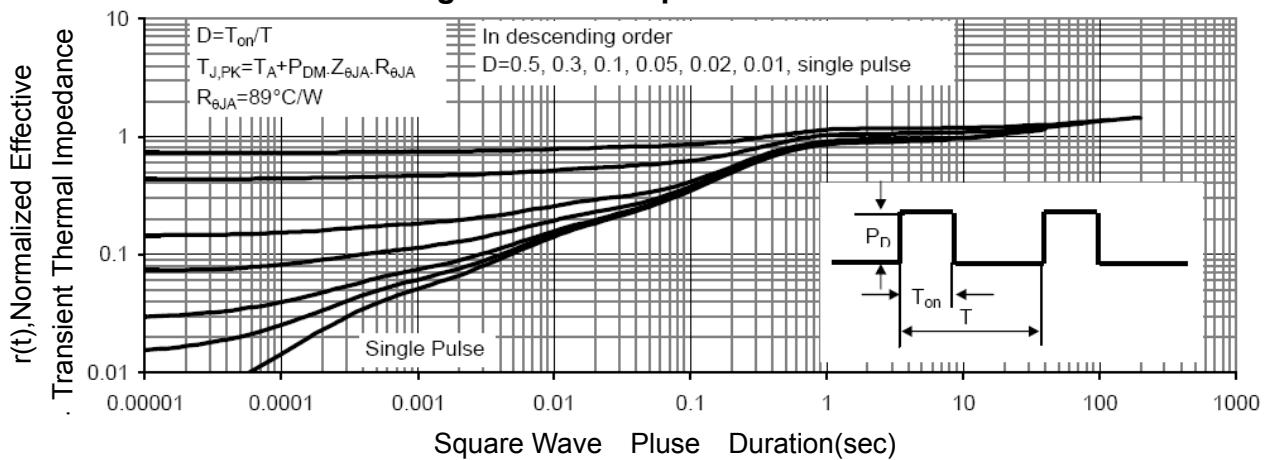
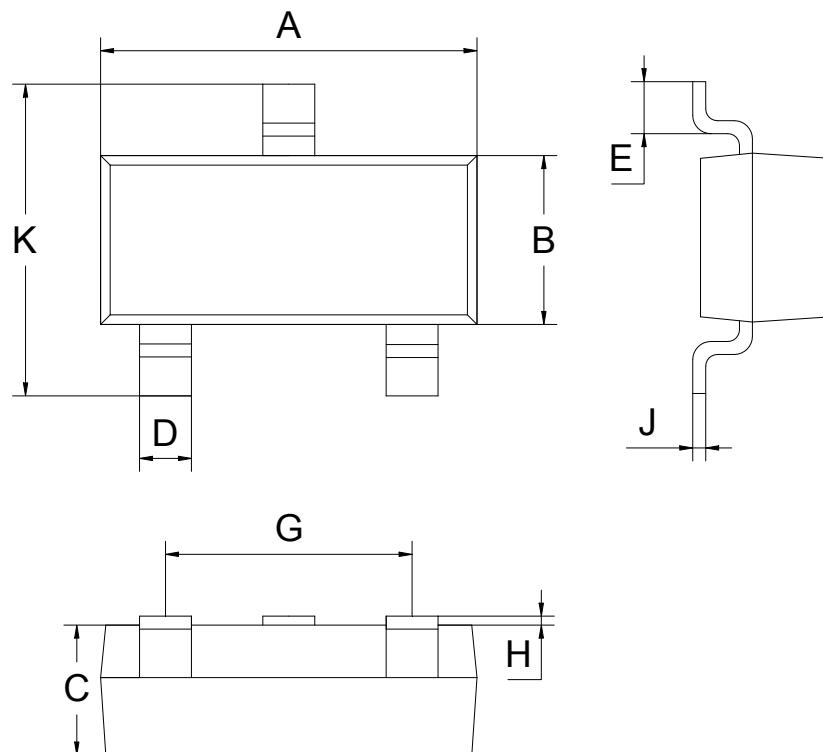


Figure 14 Normalized Maximum Transient Thermal Impedance

SOT-23-3L Package information

SOT-23-3L			
Dim	MIN	NOM	MAX
A	2.80	2.90	3.00
B	1.50	1.60	1.70
C	1.00	1.10	1.20
D	0.30	0.40	0.50
E	0.25	0.40	0.55
G	1.90		
H	0.00	-	0.10
J	0.047	0.127	0.207
K	2.60	2.80	3.00
All Dimensions in mm			