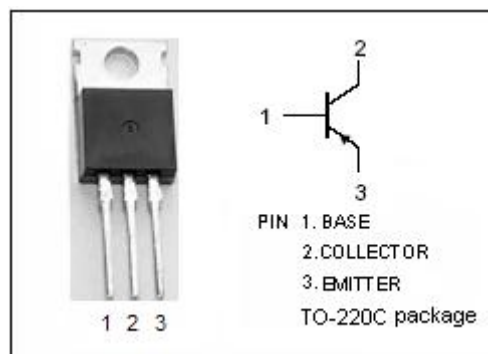


# isc Silicon PNP Power Transistor

## 2T837A

### DESCRIPTION

- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = -80V(\text{Min})$
- With TO-220 packaging
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

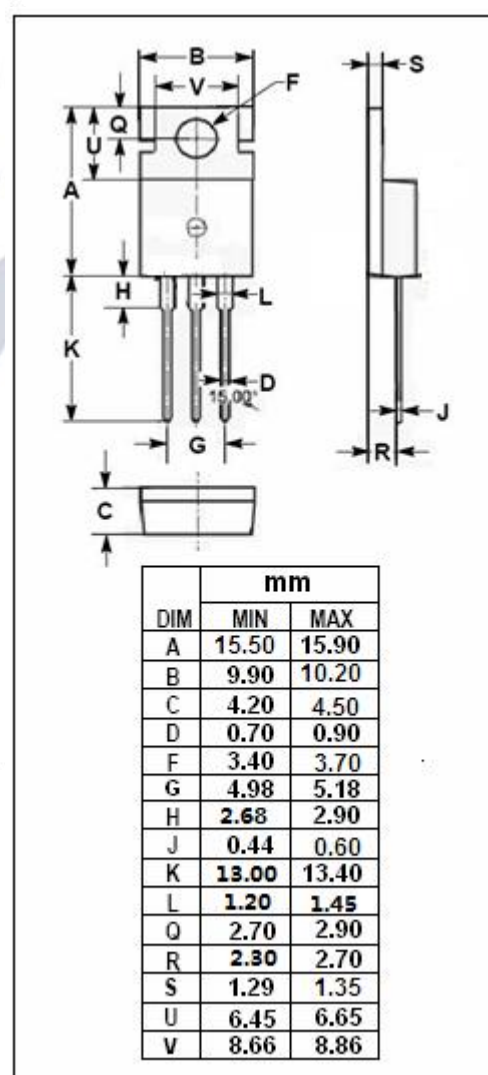


### APPLICATIONS

- Designed for low frequency power amplifier applications.

### ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-100	V
$V_{CEO}$	Collector-Emitter Voltage	-80	V
$V_{EBO}$	Emitter-Base Voltage	-7	V
$I_C$	Collector Current-Continuous	-5	A
$P_C$	Total Power Dissipation@ $T_c=25^\circ\text{C}$	80	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon PNP Power Transistor****2T837A****ELECTRICAL CHARACTERISTICS****T<sub>C</sub>=25℃ unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -0.1mA; I <sub>E</sub> = 0	-100			V
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -30mA; R <sub>BE</sub> = ∞	-80			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -1mA; I <sub>C</sub> = 0	-6			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.37A			-0.2	V
V <sub>BE(on)</sub>	Collector-Emitter On Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.37A			-1.0	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -100V; I <sub>E</sub> = 0			-10	μ A
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -2A; V <sub>CE</sub> = -5V	60		120	

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