

## isc N-Channel MOSFET Transistor

2SK754

## DESCRIPTION

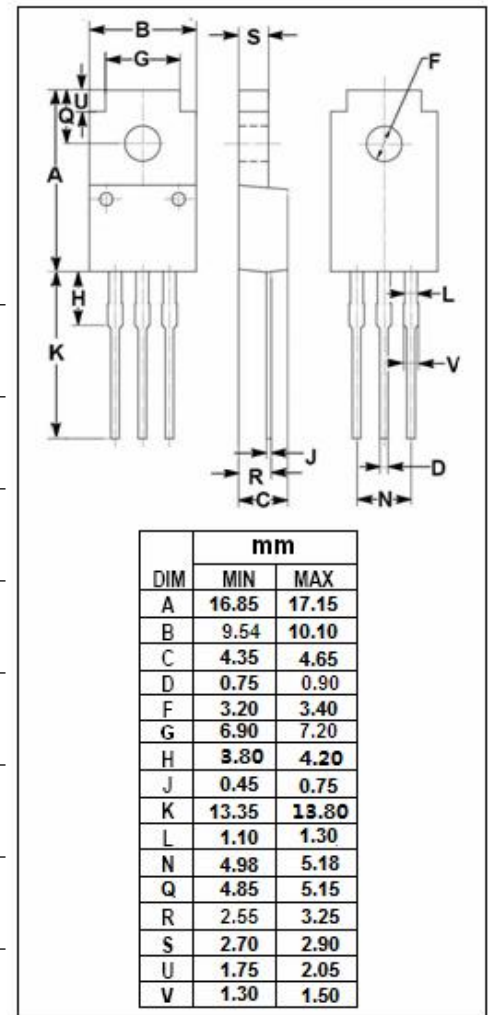
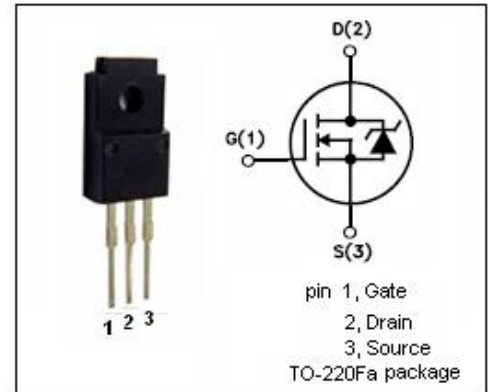
- Drain Current  $-I_D=10A@ T_C=25^{\circ}C$
- Drain Source Voltage-  
:  $V_{DSS}= 160V(\text{Min})$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## APPLICATIONS

- High speed power switching

ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}C$ )

SYMBOL	ARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage ( $V_{GS}=0$ )	160	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-continuous@ $TC=25^{\circ}C$	10	A
$P_{tot}$	Total Dissipation@ $TC=25^{\circ}C$	50	W
$T_j$	Max. Operating Junction Temperature	150	$^{\circ}C$
$T_{stg}$	Storage Temperature Range	-55~150	$^{\circ}C$



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• ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0; I <sub>D</sub> = 10mA	160			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =10 V <sub>GS</sub> ; I <sub>D</sub> =1mA	1.0		5.0	V
R <sub>DS(on)</sub>	Drain-Source On-stage Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> = 5A			0.22	Ω
I <sub>GSS</sub>	Gate Source Leakage Current	V <sub>GS</sub> = ± 16V; V <sub>DS</sub> = 0			± 10	uA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =120V; V <sub>GS</sub> = 0			250	uA
V <sub>SD</sub>	Diode Forward Voltage	I <sub>F</sub> =10A; V <sub>GS</sub> =0		1.0		V

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