

## SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET

## 2SK4198FS — General-Purpose Switching Device **Applications**

#### **Features**

- ON-resistance RDS(on)=1.8 $\Omega$  (typ.)
- Input capacitance Ciss=360pF (typ.)
- 10V drive
- · Repetitive avalanche guarantee

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		600	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I <sub>Dc</sub> *1	Limited only by maximum temperature Tch=150°C	5	А
	IDpack *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	4	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	18	А
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	30	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		55	mJ
Avalanche Current *5	I <sub>AV</sub>		4.5	А
Avalanche Energy (Repetition)	EAR	Limited only by maximum temperature Tch=150°C	3	mJ

Note: \*1 Shows chip capability.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Marking: K4198FS

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http://semicon.sanyo.com/en/network

<sup>\*2</sup> Package limited.

<sup>\*3</sup> SANYO's condition is radiation from backside.

<sup>\*4</sup> V<sub>DD</sub>=50V, L=5mH, I<sub>AV</sub>=4.5A

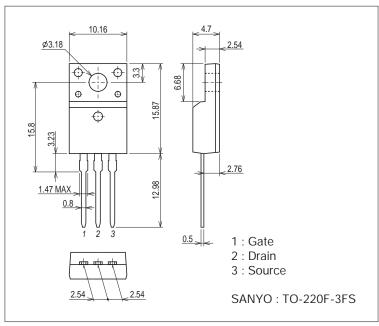
<sup>\*5</sup> L≤5mH, Single pulse

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	600			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =480V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V			±100	nA
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =2.5A	1.2	2.4		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =2.5A, V <sub>G</sub> S=10V		1.8	2.34	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		360		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		69		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =30V, f=1MHz		15		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		13		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		28		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		39		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit.		15		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		14.3		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		3.0		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		8.2		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V		0.9	1.2	V

#### **Package Dimensions**

unit : mm (typ) 7528-001

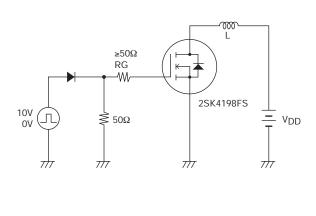


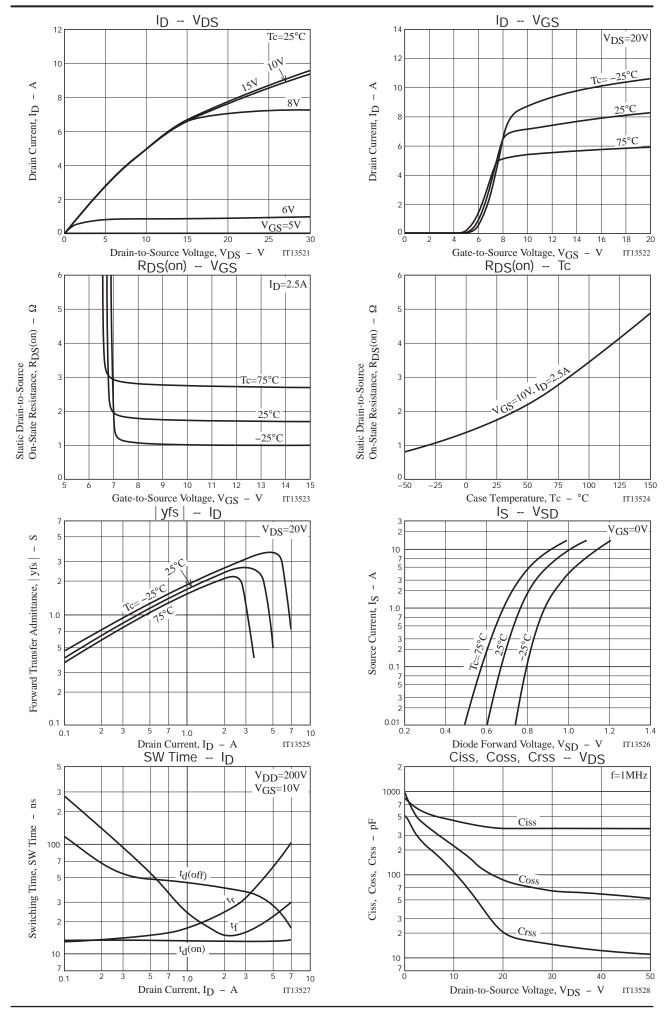
### Switching Time Test Circuit

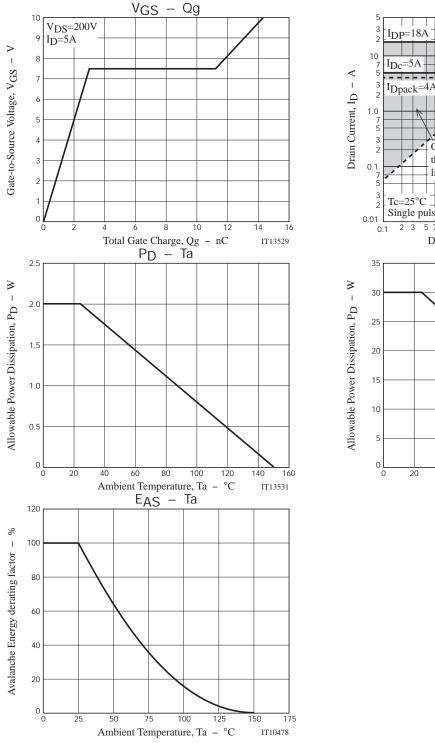
# PW=10μs D.C.≤0.5% VDD=200V ID=2.5A RL=80Ω VGS=10V P.G RGS=50Ω RGS=50Ω 2SK4198FS

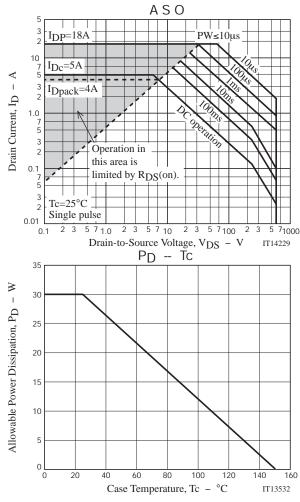
*m* 

#### **Avalanche Resistance Test Circuit**









Note on usage: Since the 2SK4198FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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