

2SK4146

MOS FIELD EFFECT TRANSISTOR

R07DS0130EJ0100 Rev.1.00 Sep 24, 2010

Description

The 2SK4146 is N-channel MOS Field Effect Transistor designed for high current switching applications.

Features

- Low on-state resistance
 - --- $R_{DS(on)}$ = 10.1 mΩ MAX. (V_{GS} = 10 V, I_D = 40 A)
- Low input capacitance
 - Ciss = 3500 pF TYP. $(V_{DS} = 10 \text{ V})$

Ordering Information

Part No.	LEAD PLATING	PACKING	Package
2SK4146-S19-AY *1	Pure Sn (Tin)	50 pcs/tube	TO-220, S19 tube

Note: *1. Pb-free (This product does not contain Pb in the external electrode.)

Absolute Maximum Ratings $(T_A = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V _{GS} = 0 V)	V _{DSS}	75	V
Gate to Source Voltage (V _{DS} = 0 V)	V _{GSS}	±20	V
Drain Current (DC) (T _C = 25°C)	I _{D(DC)}	±80	Α
Drain Current (pulse) *1	I _{D(pulse)}	±200	Α
Total Power Dissipation (T _C = 25°C)	P _{T1}	84	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.5	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	−55 to +150	°C
Repetitive Avalanche Current *2	lar	33	Α
Repetitive Avalanche Energy *2	E _{AR}	109	mJ

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

Thermal Resistance

Channel to Case Thermal Resistance	$R_{th(ch-C)}$	1.49	°C/W
Channel to Ambient Thermal Resistance	R _{th(ch-A)}	83.3	°C/W

^{*2.} Starting $T_{ch} = 25^{\circ}C$, $V_{DD} = 38 \text{ V}$, $R_G = 25 \Omega$, $V_{GS} = 20 \rightarrow 0 \text{ V}$, $L = 100 \mu H$

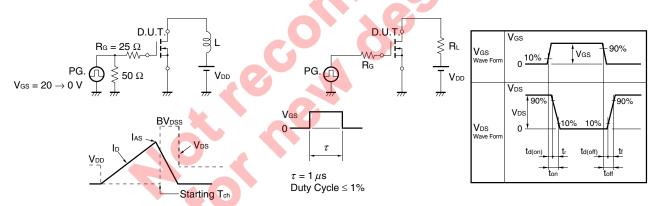
Electrical Characteristics ($T_A = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			10	μΑ	V _{DS} = 75 V, V _{GS} = 0 V
Gate Leakage Current	I _{GSS}			±100	nA	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate to Source Cut-off Voltage	$V_{GS(off)}$	2.0	3.0	4.0	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Forward Transfer Admittance *1	y _{fs}	15	32		S	V _{DS} = 10 V, I _D = 40 A
Drain to Source On-state Resistance *1	R _{DS(on)}		7.8	10.1	mΩ	V _{GS} = 10 V, I _D = 40 A
Input Capacitance	C _{iss}		3500		pF	V _{DS} = 10 V,
Output Capacitance	Coss		620		pF	$V_{GS} = 0 V$,
Reverse Transfer Capacitance	C _{rss}		160		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		26		ns	$V_{DD} = 38 \text{ V}, I_D = 40 \text{ A},$
Rise Time	t _r		20		ns	$V_{GS} = 10 V$,
Turn-off Delay Time	$t_{d(off)}$		85		ns	$R_G = 0 \Omega$
Fall Time	t _f		17		ns	
Total Gate Charge	Q_G		61		nC	$V_{DD} = 60 \text{ V},$
Gate to Source Charge	Q _{GS}		16		nC	$V_{GS} = 10 V,$
Gate to Drain Charge	Q_{GD}		20		nC	I _D = 80 A
Body Diode Forward Voltage *1	$V_{F(S-D)}$		1.0	1.5	V	I _F = 80 A, V _{GS} = 0 V
Reverse Recovery Time	t _{rr}		58		ns	I _F = 80 A, V _{GS} = 0 V,
Reverse Recovery Charge	Q _{rr}		125		nC	di/dt = 100 A/μs

Note: *1. Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME



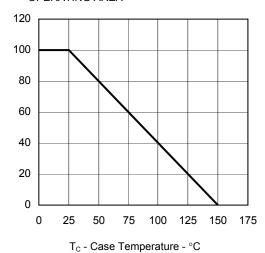
TEST CIRCUIT 3 GATE CHARGE

dT - Percentage of Rated Power - %

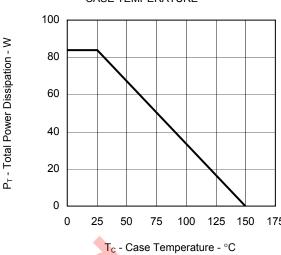
Ip - Drain Current - A

Typical Characteristics ($T_A = 25^{\circ}C$)

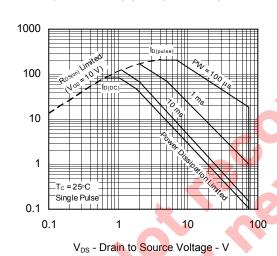
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



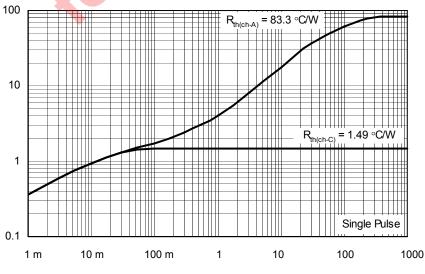
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA

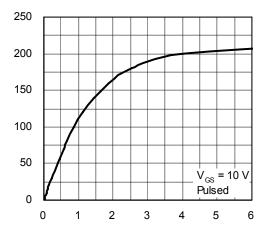


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



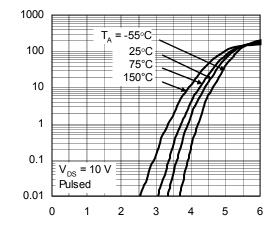
I_D - Drain Current - A

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



V_{DS} - Drain to Source Voltage - V

FORWARD TRANSFER CHARACTERISTICS

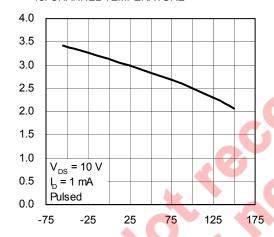


Ip - Drain Current - A

ys | - Forward Transfer Admittance - S

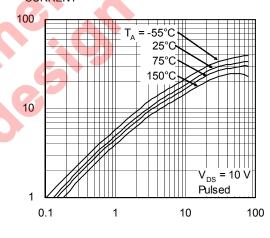
V_{GS} - Gate to Source Voltage - V

GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



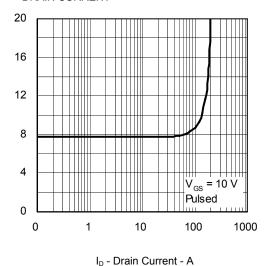
T_{ch} - Channel Temperature - °C

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

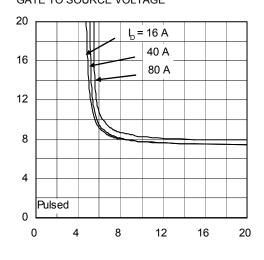


ID - Drain Current - A

DRAIN TO SOURCE ON-STATE RESISTANCE vs. **DRAIN CURRENT**



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



V_{GS} - Gate to Source Voltage - V

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

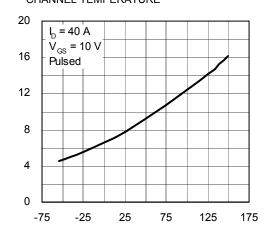
 $R_{DS(on)}$ - Drain to Source On-state Resistance - $m\Omega$

V_{GS(off)} - Gate to Source Cut-off Voltage - V

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$ - Drain to Source On-state Resistance - $m\Omega$

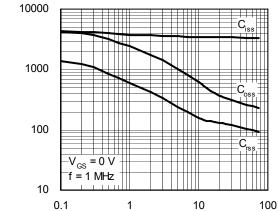
t_{d(on)}, t_r, t_{d(off)}, t_f - Switching Time - ns

DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



T_{ch} - Channel Temperature - $^{\circ}\text{C}$

Ciss, Coss, Crss - Capacitance - pF

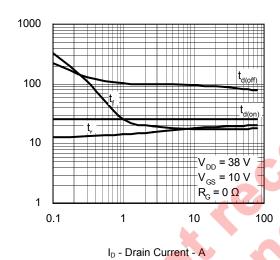


CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

V_{DS} - Drain to Source Voltage - V

DYNAMIC INPUT/OUTPUT CHARACTERISTICS

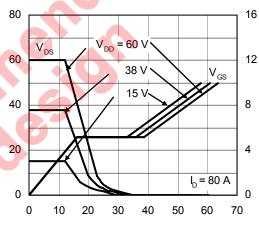
SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



t_{rr} - Reverse Recovery Time - ns

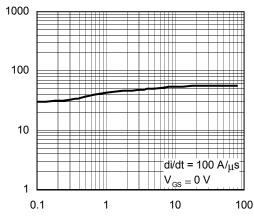


Q_G - Gate Charge - nC

1000 Pulsed 100 Pulsed 10.1 0.0 0.5 1.0 1.5

 $V_{\text{F(S-D)}}$ - Source to Drain Voltage - V

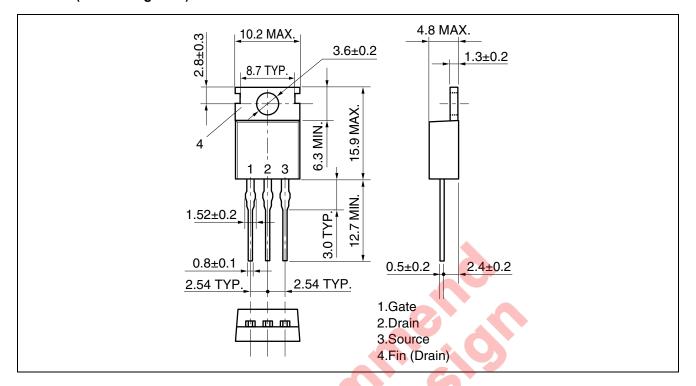
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



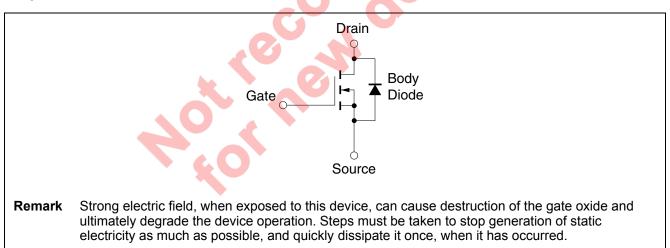
I_F - Diode Forward Current - A

Package Drawings (Unit: mm)

TO-220 (Mass: 1.9 g TYP.)



Equivalent Circuit



2SK4146

		Description				
Rev.	Date	Page	Summary			
1.00	Sep 24, 2010	-	First Edition Issued			



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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited

1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K

Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2886-9318, Fax: +852 2886-9022/9044

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Renesas Electronics Korea Co., Ltd.
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