

DATA SHEET



# N-Channel Silicon MOSFET 2SK4121LS — General-Purpose Switching Device **Applications**

### Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

# **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		450	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I <sub>Dc</sub> *1	Limited only by maximum temperature	12	А
	I <sub>Dpack</sub> *2	SANYO's ideal heat dissipation condition	9	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	42	А
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *3	EAS		183	mJ
Avalanche Current *4	IAV		12	А

\*1 Shows chip capability

\*2 Package limited

\*3 VDD=99V, L=2mH, IAV=12A

\*4 L≤2mH, single pulse

Marking: K4121

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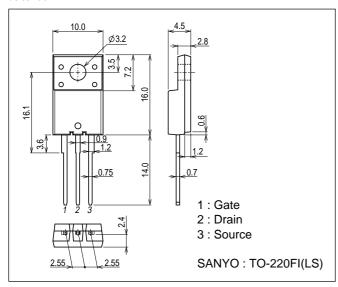
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# Electrical Characteristics at Ta= $25^{\circ}C$

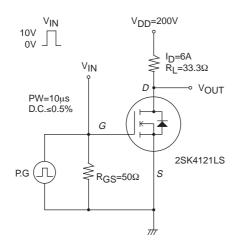
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	450			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =360V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±30V, VDS=0V			±100	nA
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =6A	3.3	6.5		S
Static Drain-to-Source On-State Resistance	RDS(on)	ID=6A, VGS=10V		0.43	0.56	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		750		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		165		pF
Reverse Transfer Capacitance	Crss	VDS=30V, f=1MHz		38		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		16		ns
Rise Time	tr	See specified Test Circuit.		54		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit.		98		ns
Fall Time	tf	See specified Test Circuit.		50		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =12A		30.3		nC
Gate-to-Source Charge	Qgs	VDS=200V, VGS=10V, ID=12A		5.2		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =12A		18.5		nC
Diode Forward Voltage	VSD	IS=12A, VGS=0V		0.9	1.2	V

## Package Dimensions

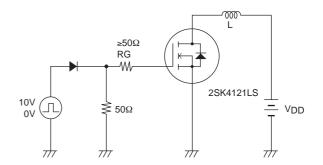
unit : mm (typ) 7509-002

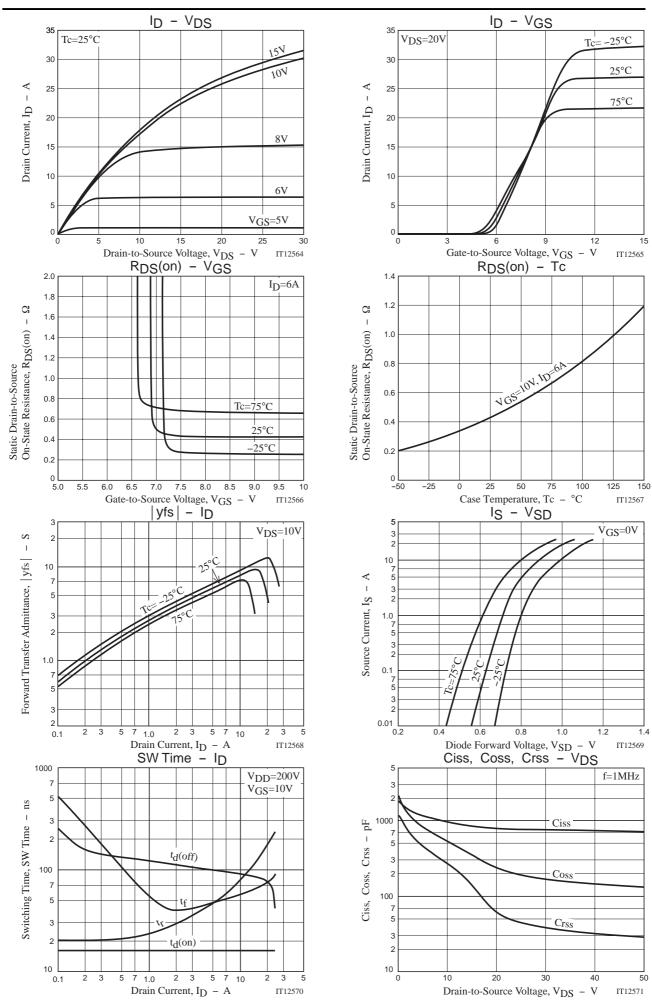


#### **Switching Time Test Circuit**

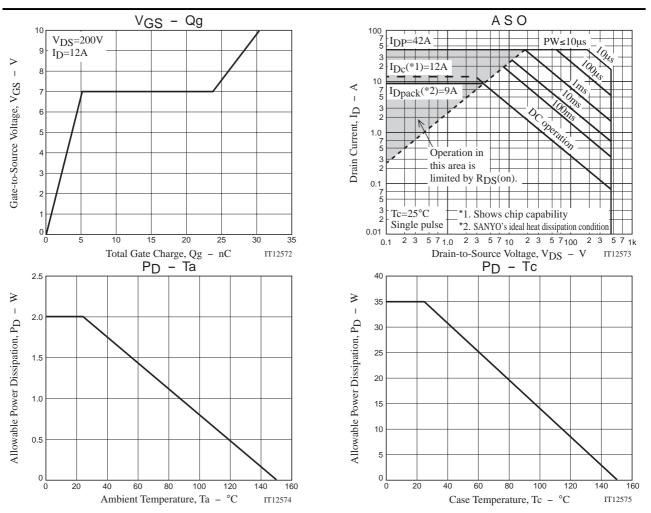


#### Avalanche Resistance Test Circuit





No. A0824-3/4



Note on usage : Since the 2SK4121LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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