



# N-Channel Silicon MOSFET **2SK4120LS** — General-Purpose Switching Device **Applications**

### Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

# **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		450	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I <sub>Dc</sub> *1	Limited only by maximum temperature	10	А
	I <sub>Dpack</sub> *2	SANYO's ideal heat dissipation condition	7.9	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	34	А
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)	33	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *3	EAS		318	mJ
Avalanche Current *4	IAV		10	А

\*1 Shows chip capability

\*2 Package limited

\*3 VDD=99V, L=5mH, IAV=10A

\*4 L≤5mH, single pulse

Marking: K4120

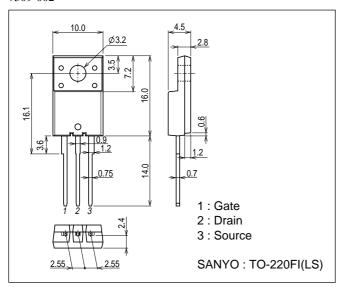
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# Electrical Characteristics at Ta= $25^{\circ}C$

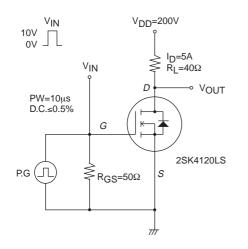
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	450			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =360V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±30V, VDS=0V			±100	nA
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =5A	2.4	4.8		S
Static Drain-to-Source On-State Resistance	RDS(on)	ID=5A, VGS=10V		0.52	0.68	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		630		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		140		pF
Reverse Transfer Capacitance	Crss	VDS=30V, f=1MHz		32		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		18		ns
Rise Time	tr	See specified Test Circuit.		54		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit.		73		ns
Fall Time	tf	See specified Test Circuit.		32		ns
Total Gate Charge	Qg	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A		24		nC
Gate-to-Source Charge	Qgs	VDS=200V, VGS=10V, ID=10A		4.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A		15		nC
Diode Forward Voltage	VSD	IS=10A, VGS=0V		0.9	1.2	V

## **Package Dimensions**

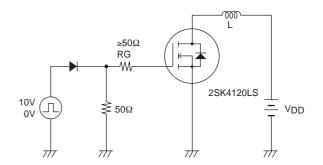
unit : mm (typ) 7509-002

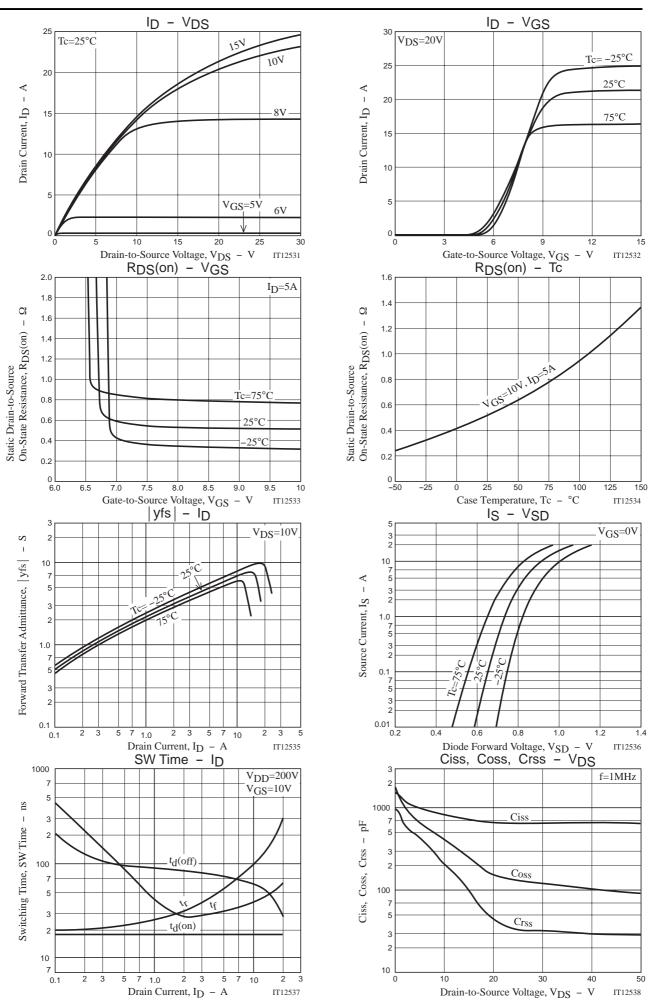


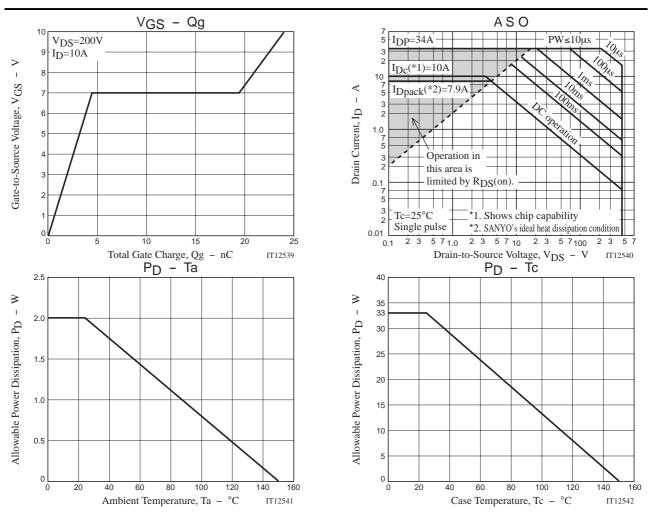
### **Switching Time Test Circuit**



### Avalanche Resistance Test Circuit







Note on usage : Since the 2SK4120LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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