

2SK4101FS — General-Purpose Switching Device Applications

Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		650	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	I _{Dc} *1	Limited only by maximum temperature Tch=150°C	7	А
	IDpack *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	6.4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	28	А
Allowable Power Dissipation	De		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		194	mJ
Avalanche Current *5	IAV		6	А

Note :*1 Shows chip capability.

*2 Package limited.

*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

*4 VDD=50V, L=10mH, IAV=6A

*5 L≤10mH, Single pulse

Marking : K4101

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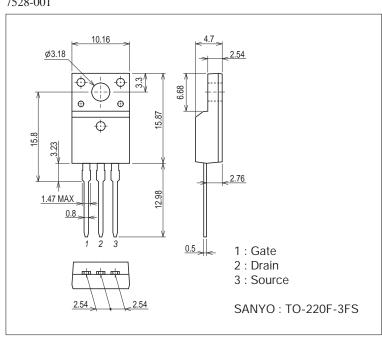
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Electrical Characteristics at Ta=25°C

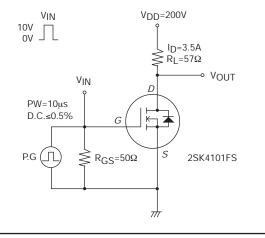
Parameter	Symbol	Conditions	Ratings			1.1
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	650			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =520V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3.5A	2.3	4.6		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	ID=3.5A, VGS=10V		0.85	1.1	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		750		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		136		pF
Reverse Transfer Capacitance	Crss	VDS=30V, f=1MHz		28		рF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		21		ns
Rise Time	tr	See specified Test Circuit.		40		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		89		ns
Fall Time	tf	See specified Test Circuit.		31		ns
Total Gate Charge	Qg	VDS=200V, VGS=10V, ID=7A		28.5		nC
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =7A		5.2		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =200V, V _{GS} =10V, I _D =7A		16		nC
Diode Forward Voltage	V _{SD}	IS=7A, VGS=0V		0.9	1.2	V

Package Dimensions

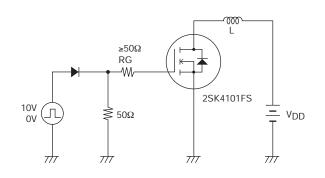
unit : mm (typ) 7528-001

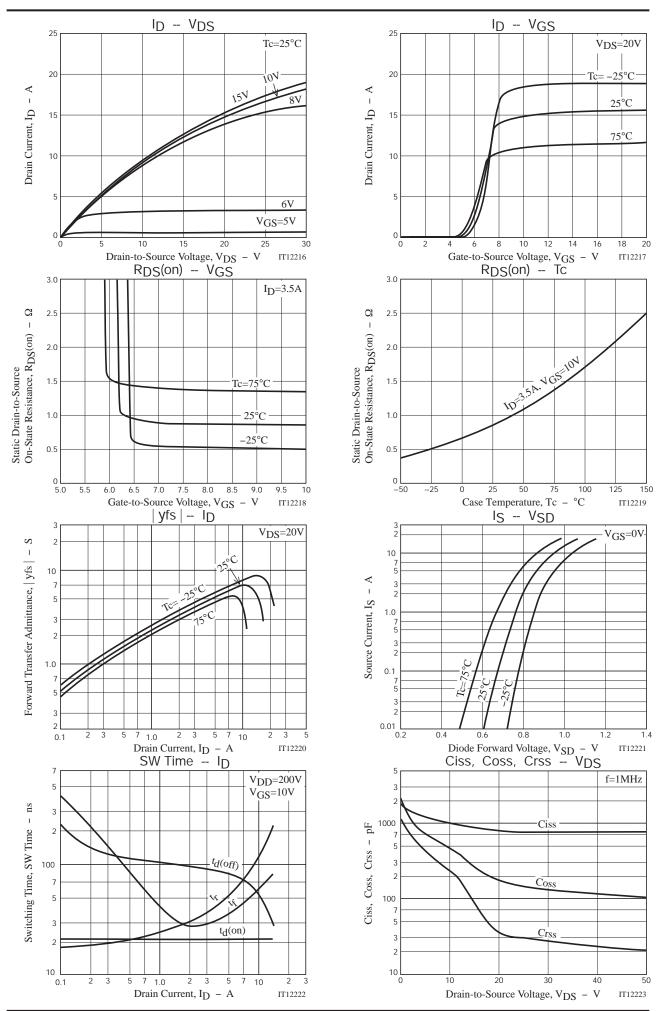


Switching Time Test Circuit

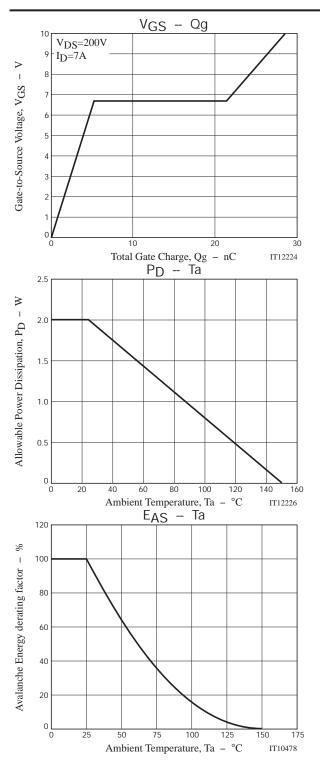


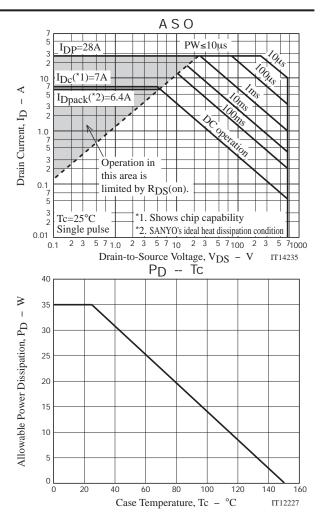
Avalanche Resistance Test Circuit





No. A1366-3/5





Note on usage : Since the 2SK4101FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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