TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOS V)

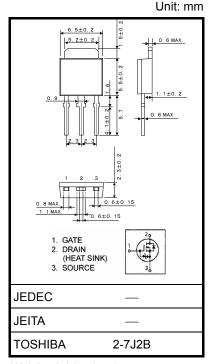
2SK4022

Switching Regulator, DC/DC Converter and Motor Drive Applications

- 4 V gate drive
- Low drain-source ON-resistance: $R_{DS (ON)} = 1.2 \Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 2.2 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (V_{DS} = 250 V)
- Enhancement mode: $V_{th} = 1.5 \sim 3.5 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	250	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	250	V	
Gate-source voltage		V_{GSS}	±20	V	
	DC (Note 1)	ΙD	3		
Drain current	Pulse (t = 1 ms) (Note 1)	I _{DP}	6	А	
Drain power dissipati	on (Tc = 25°C)	P _D	20	W	
Single-pulse avalanc	he energy (Note 2)	E _{AS}	36.2	mJ	
Avalanche current		I _{AR}	3	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	2	mJ	
Channel temperature	•	T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 50 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$, L = 6.7 mH, $I_{AR} = 3 \text{ A}$, $R_G = 25\Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

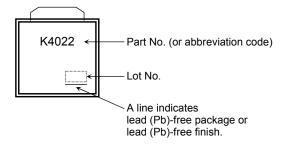
Electrical Characteristics (Ta = 25°C)

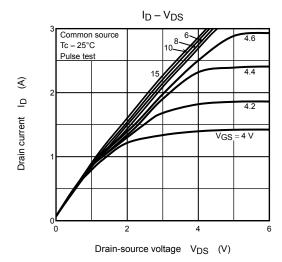
Chara	acteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	250	_	_	V
Gate threshold vo	ltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source ON-	resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 1.5 A	_	1.2	1.7	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1.5 A	0.5	2.2	_	S
Input capacitance		C _{iss}		_	267	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	32	_	pF
Output capacitance		Coss		_	98	_	
Switching time	Rise time	t _r	$I_D = 1.5$ A VOUT $I_D = 1.5$ A VOUT $I_D = 67$ Ω $I_D = 67$ Ω $I_D = 1.5$ A VOUT $I_D = 67$ Ω $I_D = 67$	_	5	_	- ns
	Turn-on time	t _{on}		_	20	_	
	Fall time	t _f		_	5	_	
	Turn-off time	t _{off}		_	30	_	
Total gate charge		Qg		_	12	_	
Gate-source charge		Q _{gs}	$V_{DD} = 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$	_	6	_	nC
Gate-drain charge		Q _{gd}		_	6	_	

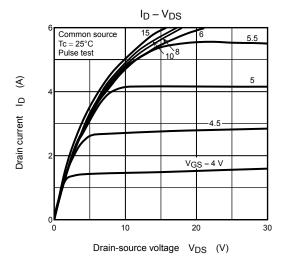
Source-Drain Ratings and Characteristics (Ta = 25°C)

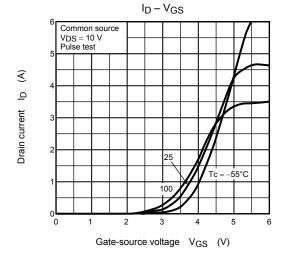
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	3	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	6	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 3 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 3 A, V _{GS} = 0 V,	_	125	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	470	_	nC

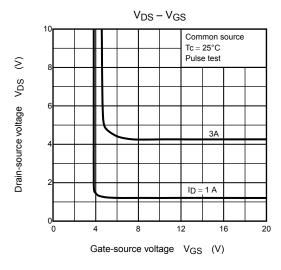
Marking

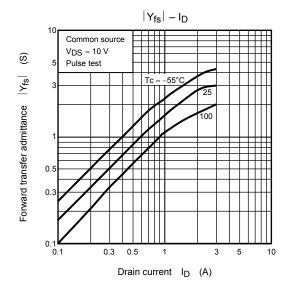


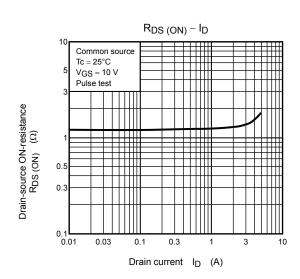


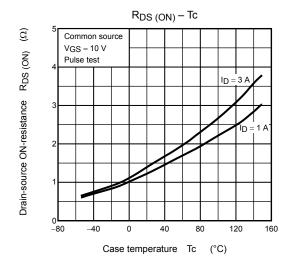


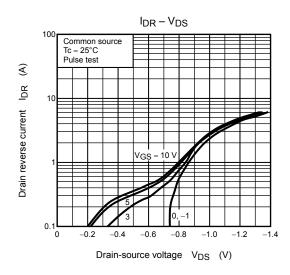


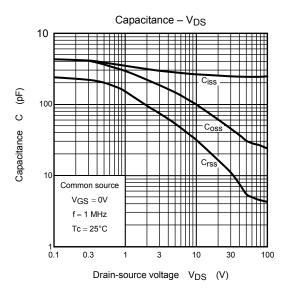


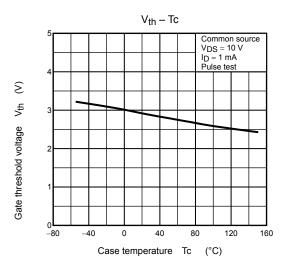


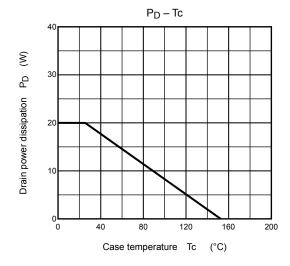


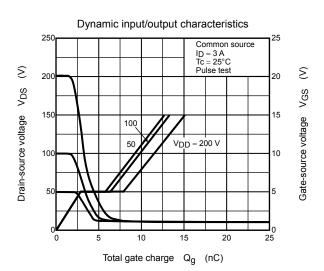


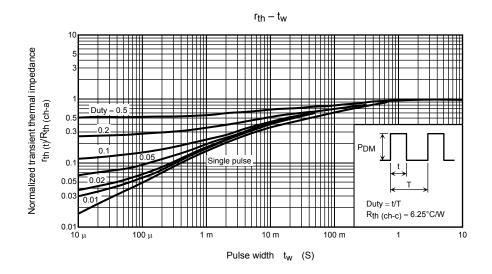


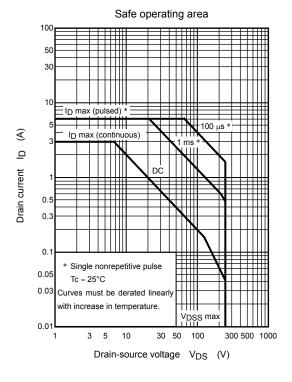


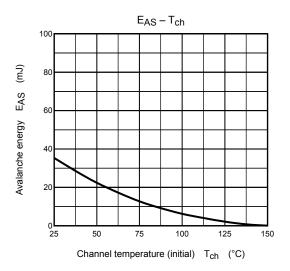


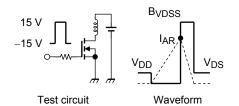












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 50~V,~L = 6.7~mH \end{aligned} \qquad E :_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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