TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2-\pi$ -MOSV)

# 2SK3658

# DC-DC Converter, Relay Drive and Motor Drive **Applications**

• Low drain-source ON resistance : RDS (ON) =  $0.23 \Omega$  (typ.) High forward transfer admittance  $|Y_{fs}| = 2.0 \text{ S (typ.)}$ Low leakage current :  $I_{DSS} = 100 \,\mu A \,(max) \,(V_{DS} = 60 \,V)$ 

Enhancement-mode :  $V_{th} = 0.8 \text{ to } 2.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{mA})$ 

# **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	60	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	60	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	ΙD	2	Α	
	Pulse (Note 1)	$I_{DP}$	6		
Drain power dissipation	n (Tc = 25°C)	$P_{D}$	0.5	W	
Drain power dissipation (Note 2)		$P_{D}$	1.5	W	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note 1: Please use devices on condition that the channel temperature is

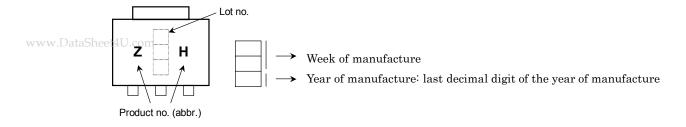
**TOSHIBA** 2-5K1B below 150°C. Weight: 0.05 g (typ.) Note 2: Mounted on ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm) Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

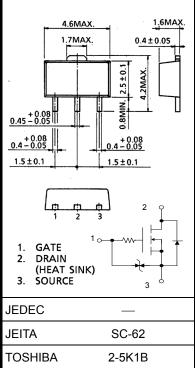
### Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	250	°C/W	

This transistor is an electrostatic sensitive device. Please handle with caution.

#### Marking







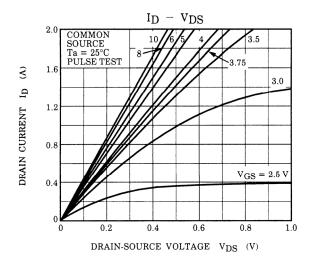
# Electrical Characteristics (Ta = 25°C)

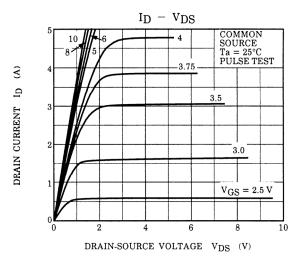
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_	_	V
Gate threshold v	oltage/	$V_{th}$	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	VGS = 4 V, ID = 1 A	_	0.33	0.44	Ω
			VGS = 10 V, ID = 1 A	_	0.23	0.30	12
Forward transfer	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 A	1.0	2.0	_	S
Input capacitano	e	C <sub>iss</sub>		_	140	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	20	_	pF
Output capacitance		Coss		_	65	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $V_{OV}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$ $V_{OUT}$	_	140	_	ns
	Turn-on time	t <sub>on</sub>		_	210	_	
	Fall time	t <sub>f</sub>		_	470	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\rm w} = 10 \mu \rm s$	_	1600	_	
Total gate charge (gate-source plus gate-drain)		Qg			5.0		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$		3.6		nC
Gate-drain ("miller") Charge		$Q_{gd}$		_	1.4	_	

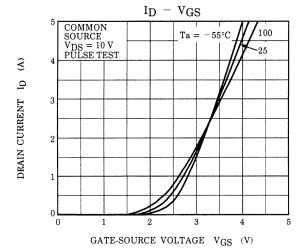
# Source-Drain Ratings and Characteristics (Ta = 25°C)

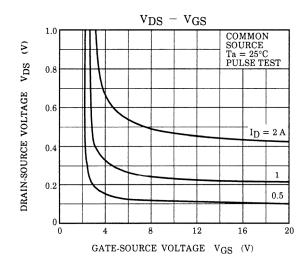
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	6	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 2 A, V <sub>GS</sub> = 0 V		100	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 50 A / μs		40	_	nC

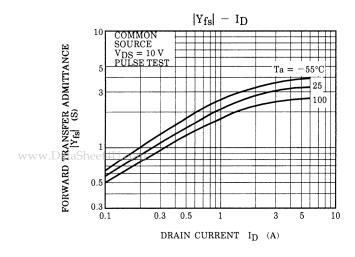
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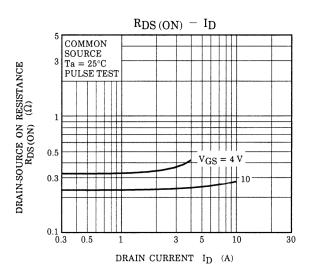


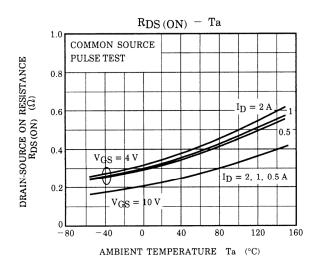


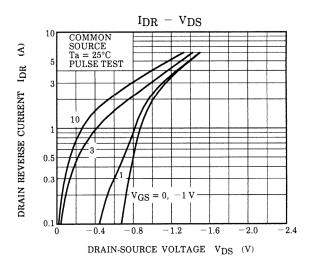


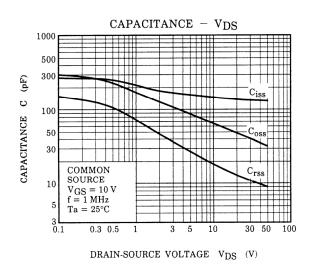


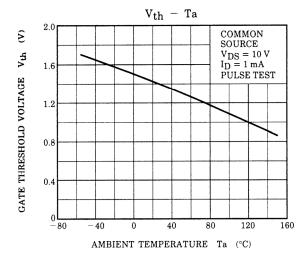


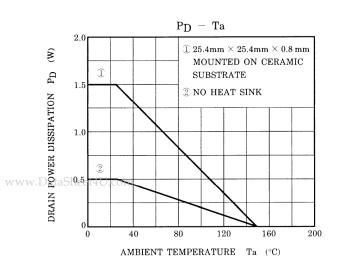


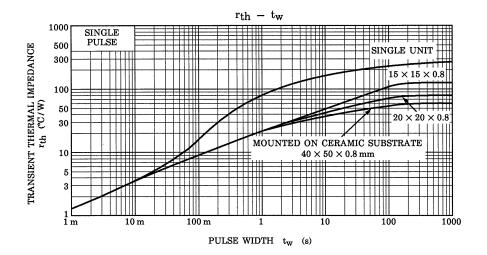




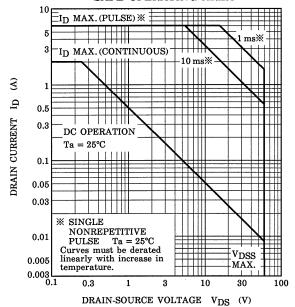








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