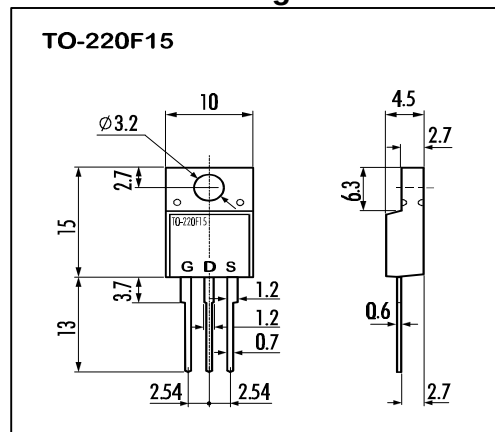


> Features

- High Current
- Low On-Resistance
- No Secondary Breakdown
- Low Driving Power
- Avalanche Rated

> Applications

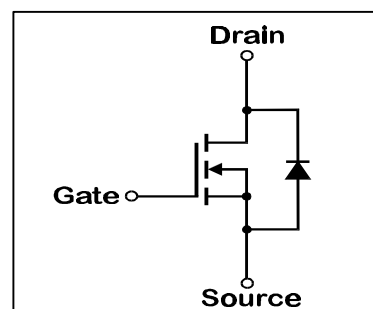
- Motor Control
- General Purpose Power Amplifier
- DC-DC converters

> Outline Drawing

> Maximum Ratings and Characteristics

- Absolute Maximum Ratings ($T_C=25^\circ\text{C}$), unless otherwise specified

Item	Symbol	Rating	Unit
Drain-Source-Voltage	V_{DS}	60	V
Continuous Drain Current	I_D	±70	A
Pulsed Drain Current	$I_{D(puls)}$	±280	A
Gate-Source-Voltage	V_{GS}	+30 / -20	V
Maximum Avalanche Energy	E_{AV}	700.6	mJ*
Max. Power Dissipation	P_D	70	W
Operating and Storage Temperature Range	T_{ch}	150	°C
	T_{stg}	-55 ~ +150	°C

* $L=0.19\text{mH}$, $V_{CC}=24\text{V}$

> Equivalent Circuit


- Electrical Characteristics ($T_C=25^\circ\text{C}$), unless otherwise specified

Item	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown-Voltage	BV_{DSS}	$I_D=1\text{mA}$ $V_{GS}=0\text{V}$	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$I_D=10\text{mA}$ $V_{DS}=V_{GS}$	2,5	3,0	3,5	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60\text{V}$ $T_{ch}=25^\circ\text{C}$		1,0	100,0	μA
		$V_{GS}=0\text{V}$ $T_{ch}=125^\circ\text{C}$		10,0	500,0	μA
Gate Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}$ $V_{DS}=0\text{V}$		10	100	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$I_D=40\text{A}$ $V_{GS}=40\text{V}$		5,0	6,5	mΩ
Forward Transconductance	g_{fs}	$I_D=40\text{A}$ $V_{DS}=10\text{V}$	25	50		S
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}$		9000		pF
Output Capacitance	C_{oss}	$V_{GS}=0\text{V}$		1250		pF
Reverse Transfer Capacitance	C_{rss}	$f=1\text{MHz}$		700		pF
Turn-On-Time t_{on} ($t_{on}=t_{d(on)}+t_r$)	$t_{d(on)}$	$V_{CC}=30\text{V}$		50		ns
	t_r	$V_{GS}=10\text{V}$		200		ns
Turn-Off-Time t_{off} ($t_{on}=t_{d(off)}+t_f$)	$t_{d(off)}$	$ID=80\text{A}$		150		ns
	t_f	$R_{GS}=10\Omega$		135		ns
Avalanche Capability	I_{AV}	$L=100\mu\text{H}$ $T_{ch}=25^\circ\text{C}$	70			A
Diode Forward On-Voltage	V_{SD}	$I_F=80\text{A}$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$		1,0	1,5	V
Reverse Recovery Time	t_{rr}	$I_F=50\text{A}$ $V_{GS}=0\text{V}$		85		ns
Reverse Recovery Charge	Q_{rr}	$-dI_F/dt=100\text{A}/\mu\text{s}$ $T_{ch}=25^\circ\text{C}$		0,25		μC

- Thermal Characteristics

Item	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Thermal Resistance	$R_{th(ch-a)}$	channel to ambient			62,5	°C/W
	$R_{th(ch-c)}$	channel to case			1,79	°C/W

N-channel MOS-FET

60V | 6,5mΩ | ±70A | 70W

2SK3273-01MR

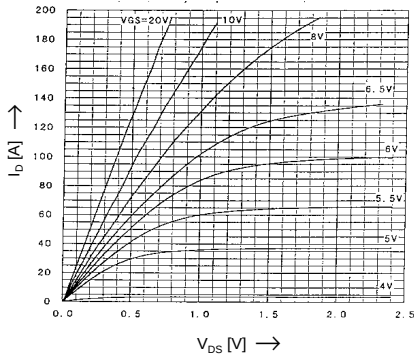
Trench Gate MOSFET

FUJI
ELECTRIC

> Characteristics

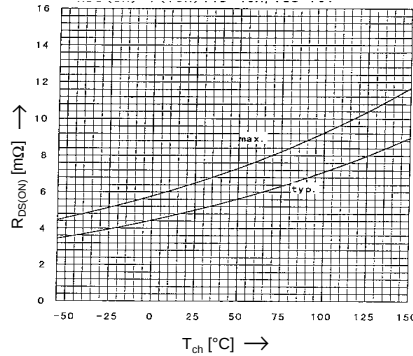
Typical Output Characteristics

$I_D = f(V_{DS})$; 80μs pulse test; $T_C = 25^\circ\text{C}$



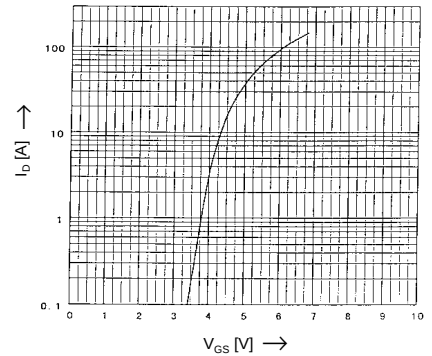
Drain-Source On-State Resistance vs. T_{ch}

$R_{DS(on)} = f(T_{ch})$; $I_D = 25A$; $V_{GS} = 10V$



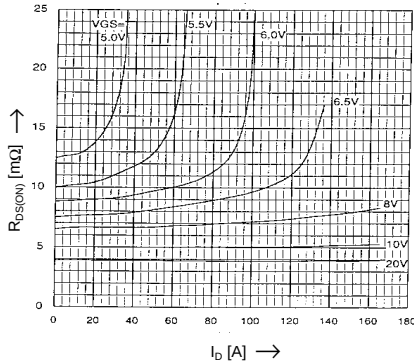
Typical Transfer Characteristics

$I_D = f(V_{GS})$; 80μs pulse test; $V_{DS} = 25V$; $T_{ch} = 25^\circ\text{C}$



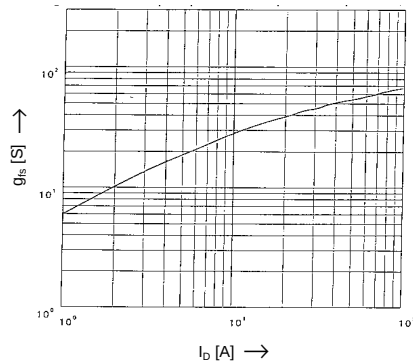
Typical Drain-Source On-State-Resistance vs. I_D

$R_{DS(on)} = f(I_D)$; 80μs pulse test; $T_C = 25^\circ\text{C}$



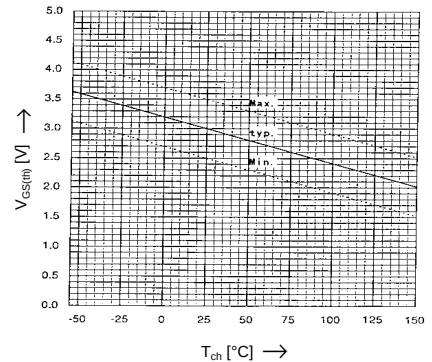
Typical Forward Transconductance vs. I_D

$g_{fs} = f(I_D)$; 80μs pulse test; $V_{DS} = 25V$; $T_{ch} = 25^\circ\text{C}$



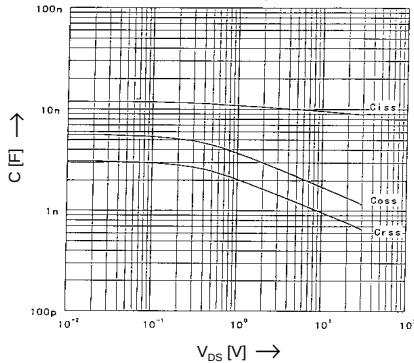
Gate Threshold Voltage vs. T_{ch}

$V_{GS(th)} = f(T_{ch})$; $I_D = 1mA$; $V_{DS} = V_{GS}$



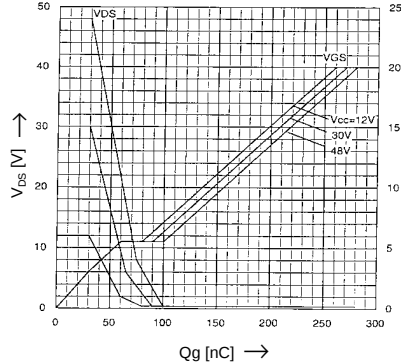
Typical Capacitances vs. V_{DS}

$C = f(V_{DS})$; $V_{GS} = 0V$; $f = 1MHz$



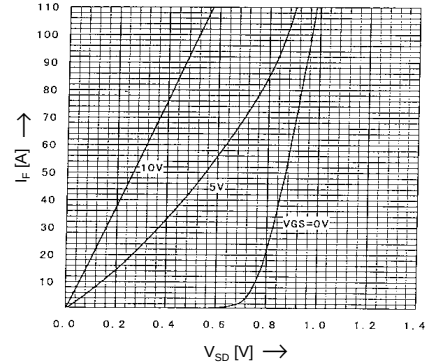
Typical Gate Charge Characteristic

$V_{GS} = f(Q_g)$; $I_D = 80A$; $T_{ch} = 25^\circ\text{C}$



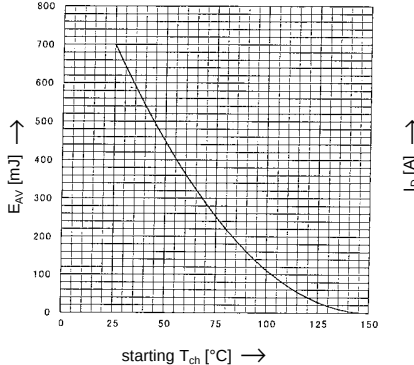
Forward Characteristics of Reverse Diode

$I_F = f(V_{SD})$; 80μs pulse test; $T_{ch} = 25^\circ\text{C}$



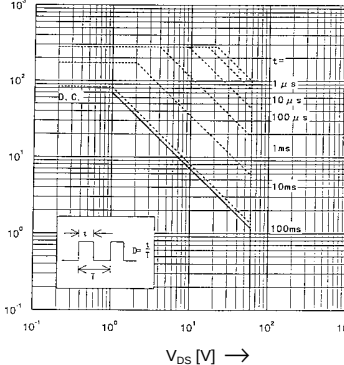
Maximum Avalanche Energy vs. starting T_{ch}

$E_{AV} = f(\text{starting } T_{ch})$; $V_{CC} = 24V$; $I_{AV} \leq 80A$



Safe Operation Area

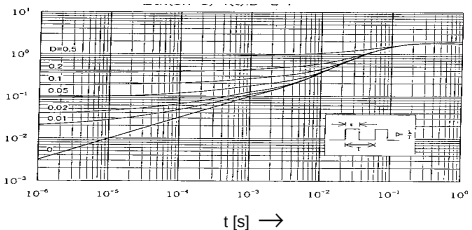
$I_D = f(V_{DS})$; $D = 0.01$; $T_C = 25^\circ\text{C}$



$Z_{th(ch-c)} [K/W]$

Transient Thermal impedance

$Z_{th(ch-c)} = f(t)$ parameter: $D = t/T$



This specification is subject to change without notice!