TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

2SK2417

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance : RDS (ON) = 0.42 Ω (typ.)

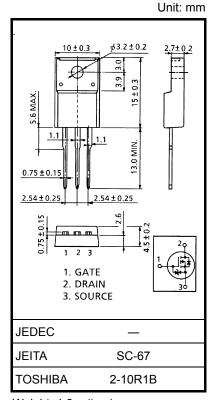
• High forward transfer admittance $: |Y_{fs}| = 7.5 \text{ S (typ.)}$

• Low leakage current $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 250 \,\text{V})$

• Enhancement mode : $V_{th} = 1.5 \text{ to } 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics S		ymbol	Rating	Unit
Drain-source voltage		V_{DSS}	250	V
Drain-gate voltage (R _{GS} = 20 kΩ) V		DGR 250		V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	I _D	7.5	Α
	Pulse (Note 1)	I_{DP}	30	Α
Drain power dissipation (Tc = 25°C)		P _D	30	W
Single pulse avalanche energy (Note 2)		E _{AS}	110	mJ
Avalanche current		I _{AR}	7.5	Α
Repetitive avalanche energy (Note 3)		E _{AR}	3	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics S	ymbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)} 4.1	6	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 3.3 mH, R_G = 25 Ω , I_{AR} = 7.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

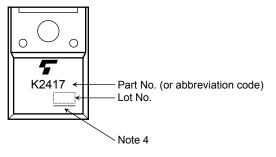
Electrical Characteristics (Ta = 25°C)

Charac	teristics S	ymbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V		_	±10	μΑ
Drain cut-off cu	rent	I _{DSS}	V _{DS} = 250 V, V _{GS} = 0 V			100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V			_	V
Gate threshold v	roltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5		3.5	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.5 A	_	0.42	0.5	Ω
Forward transfer	admittance	Y _{fs} V	_{DS} = 10 V, I _D = 3.5 A		7.5	_	S
Input capacitano	е	C _{iss} —	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		700	_	pF
Reverse transfe	capacitance	C _{rss} —			80	_	
Output capacitance		Coss			0	_	
Switching time	Rise time	t _r —	V_{GS} V_{OUT} V_{OU		10	_	ns
	Turn-on time	t _{on} —			20	_	
	Fall time	t _f —			10	_	
	Turn-off time	t _{off}	$V_{DD} = 100V$ Duty $\leq 1\%$, $t_w = 10\mu s$	— 70		_	
Total gate charge (Gate-source plus gate-drain)		Q _g —			20	_	nC
Gate-source charge		Q _{gs} —	$V_{DD} \approx 200 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$		13	_	
Gate-drain ("miller") charge		Q_{gd}					

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics S	ymbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR} —		_	_	7.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_			30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 7.5 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr} —	I _{DR} = 7.5 A, V _{GS} = 0 V		180	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 100 A / μs	— 1.1	_		μC

Marking

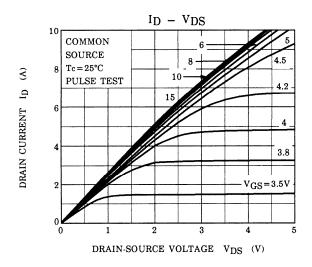


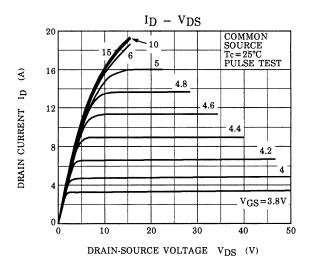
Note 4: A line under a Lot No. identifies the indication of product Labels.

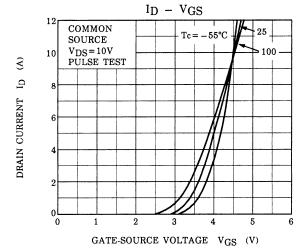
Not underlined: [[Pb]]/INCLUDES > MCV

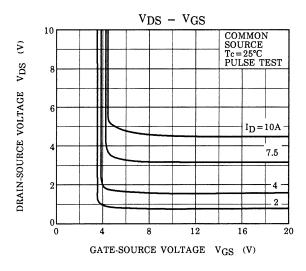
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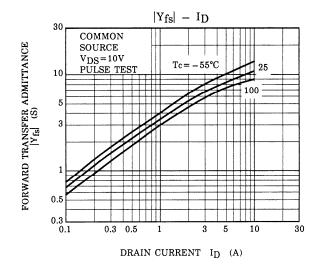
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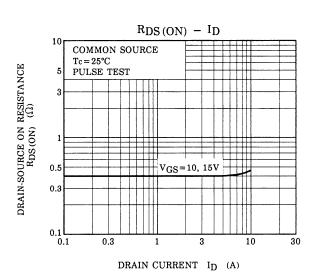


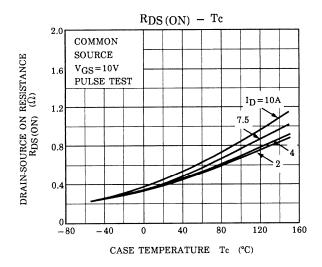


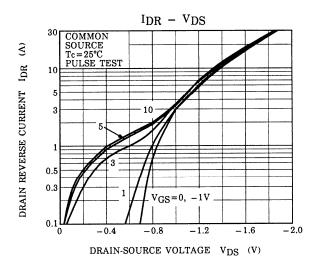


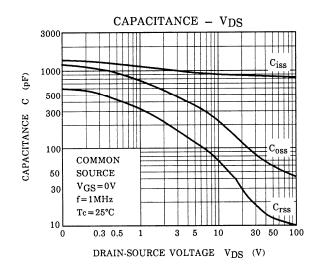


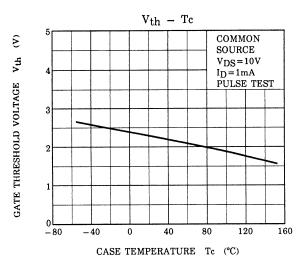


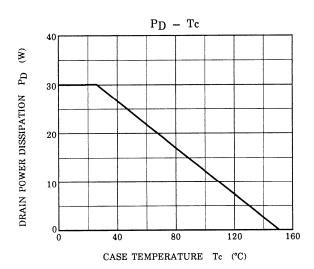


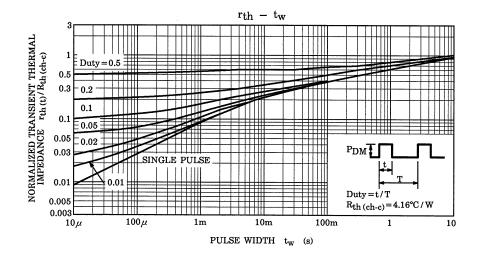


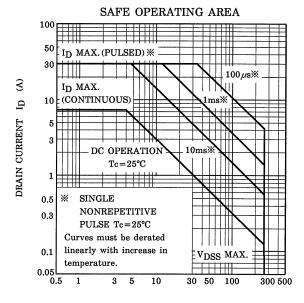












EAS — T_{ch}

200

160

120

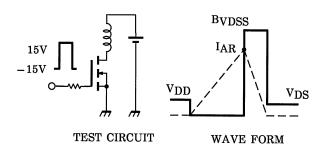
120

40

25 50 75 100 125 150

CHANNEL TEMPERATURE (INITIAL) T_{ch} (°C)

DRAIN-SOURCE VOLTAGE V_{DS} (V)



$$\begin{split} R_G &= 25~\Omega \\ V_{DD} &= 50~V,\, L = 3.3~mH \end{split}$$

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$$EAS = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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