Ordering number:EN3509A

P-Channel Silicon MOSFET

2SJ187

Ultrahigh-Speed Switching Applications

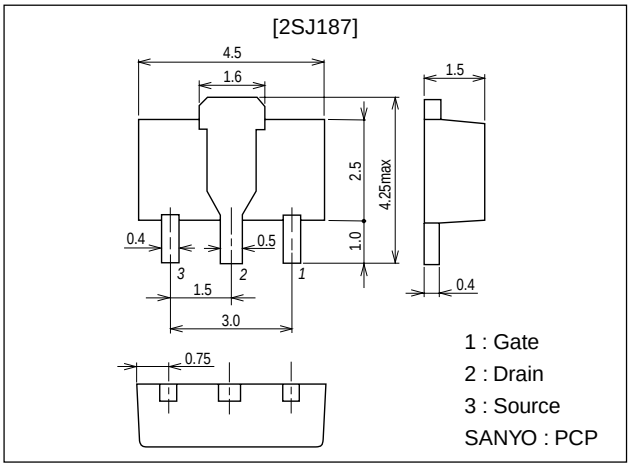
Features

- Low ON resistance.
- Ultrahigh-speed switching.
- Low-voltage drive.

Package Dimensions

unit:mm

2062A



Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		–30	V
Gate-to-Source Voltage	V_{GSS}		±15	V
Drain Current (DC)	I_D		–1	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	–4	A
Allowable Power Dissipation	P_D	Tc=25˚C	3.5	W
		Mounted on ceramic board (250mm²×0.8mm)	1.5	W
Channel Temperature	Tch		150	˚C
Storage Temperature	Tstg		–55 to +150	˚C

Electrical Characteristics at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=–1mA, V_{GS}=0	–30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=–30V, V_{GS}=0			–100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±12V, V_{DS}=0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=–10V, I_D=–1mA	–1.0		–2.0	V
Forward Transfer Admittance	| yfs |	V_{DS}=–10V, I_D=–500mA	0.6	1.0		S
Static Drain-to-Source ON-State Resistance	$R_{DS(on)}$	I_D=–500mA, V_{GS}=–10V		0.5	0.75	Ω
	$R_{DS(on)}$	I_D=–500mA, V_{GS}=–4V		0.75	1.1	Ω

Marking : JA

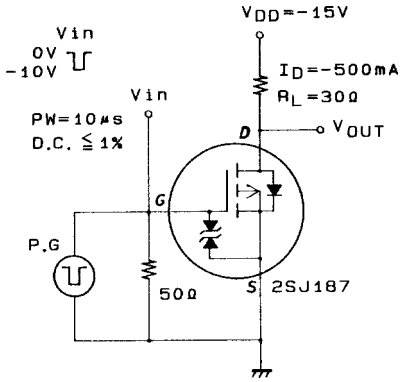
Continued on next page.

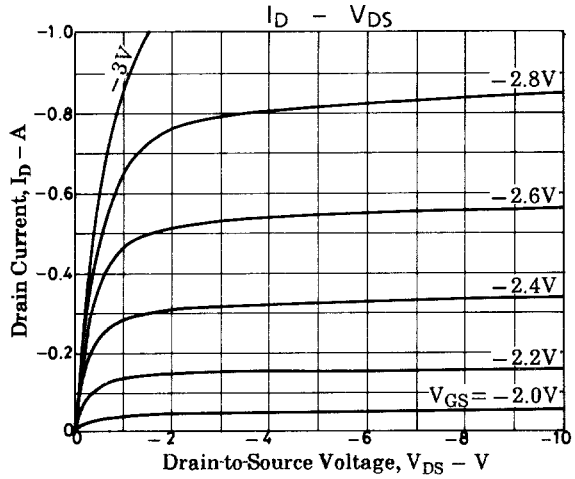
SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquaters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

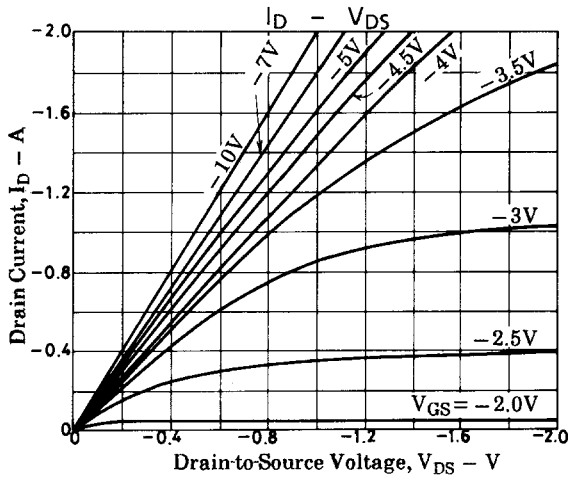
Continued from preceding page.

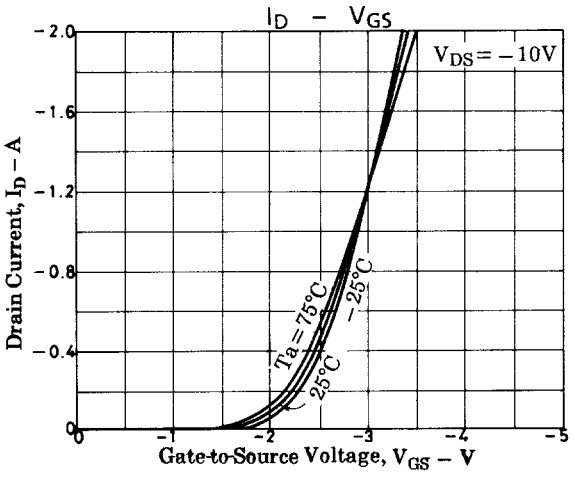
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=–10V, f=1MHz		170		pF
Output Capacitance	Coss	V_{DS}=–10V, f=1MHz		110		pF
Reverse Transfer Capacitance	Crss	V_{DS}=–10V, f=1MHz		20		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		10		ns
Rise Time	t_r	See specified Test Circuit		13		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		70		ns
Fall Time	t_f	See specified Test Circuit		30		ns
Diode Forward Voltage	V_{SD}	I_S=–1A, V_{GS}=0		–0.9		V

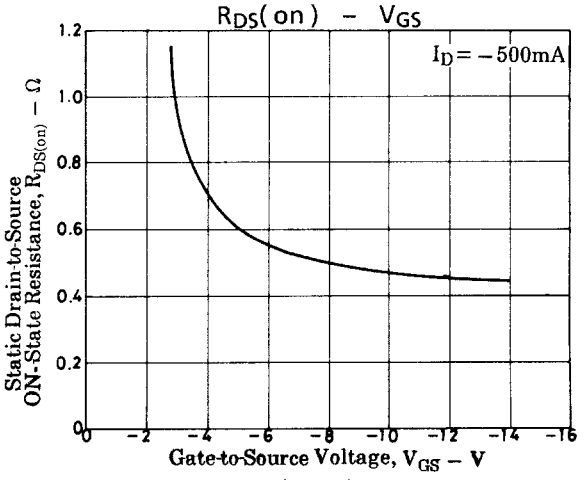
Switching Time Test Circuit

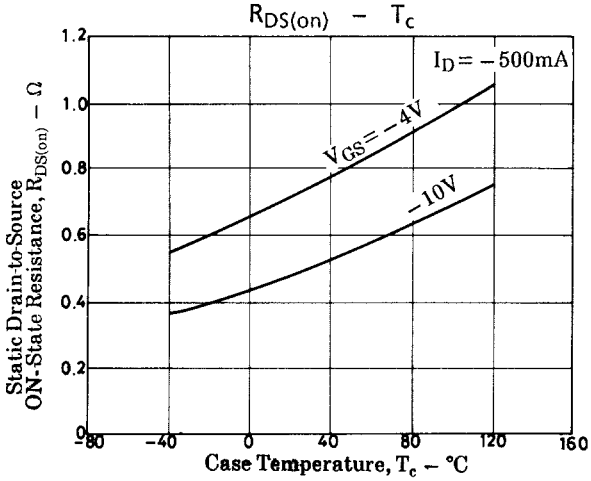


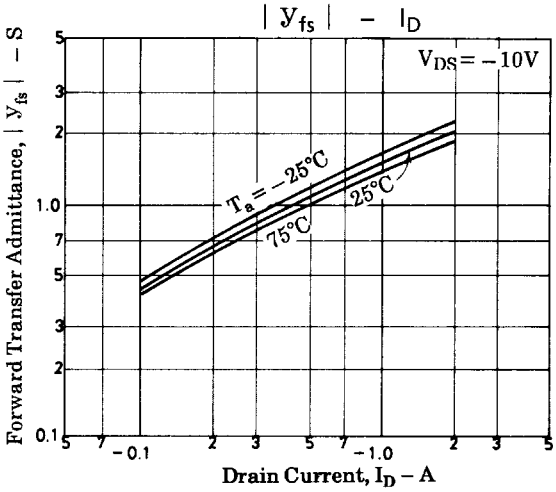


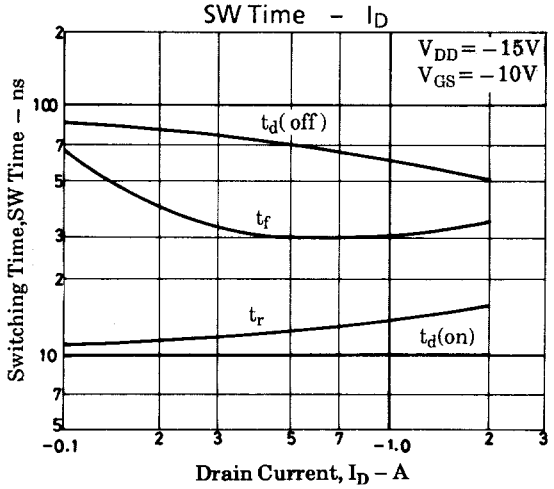


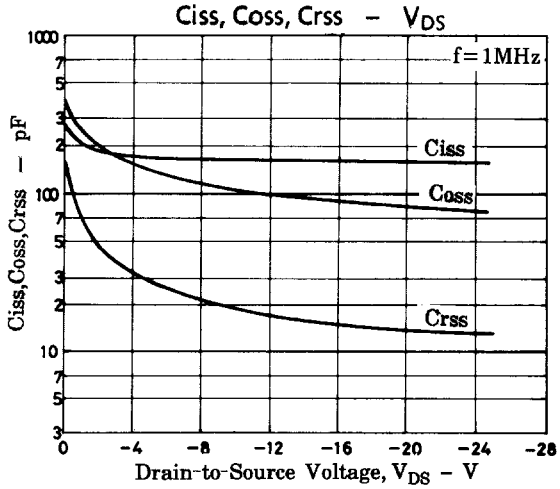


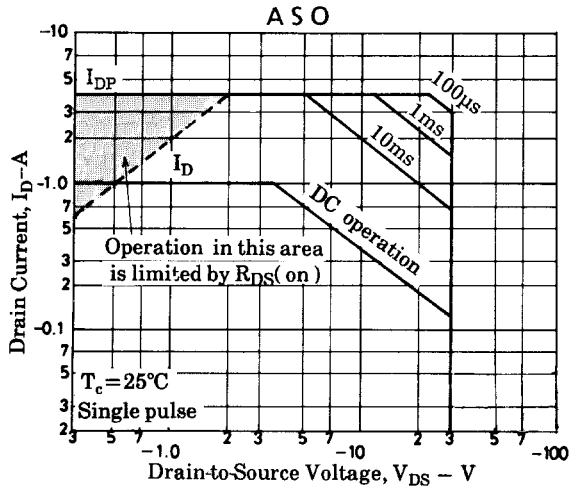


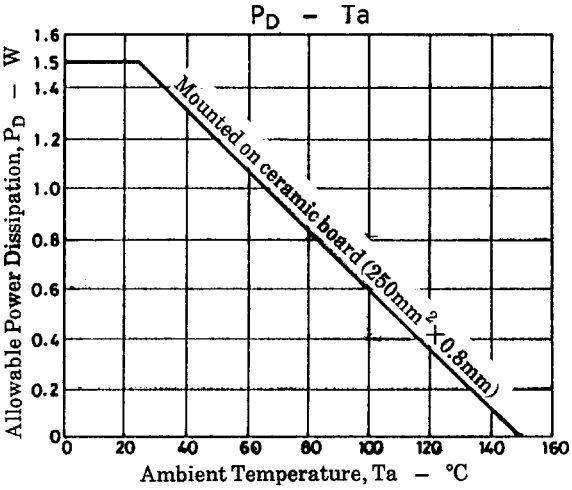

RDS(on) – Tc
ID = –500mA
VGS = –4V
–10V
Static Drain-to-Source ON-State Resistance, RDS(on) – Ω
Case Temperature, Tc – °C

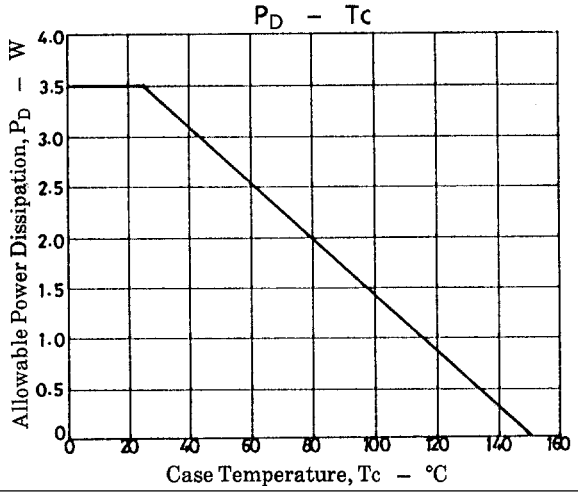

| yfs | – ID
VDS = –10V
Ta = –25°C
25°C
75°C
Forward Transfer Admittance, | yfs | – S
Drain Current, ID – A


SW Time – ID
VDD = –15V
VGS = –10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A


Ciss, Coss, Crss – VDS
f = 1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V


A S O
IDP
ID
100μs
1ms
10ms
DC operation
Operation in this area is limited by RDS(on)
Tc = 25°C
Single pulse
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V


PD – Ta
Mounted on ceramic board (250mm² × 0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C


PD – Tc
Allowable Power Dissipation, PD – W
Case Temperature, Tc – °C