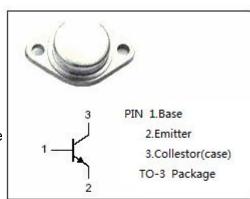


isc Silicon NPN Power Transistor

DESCRIPTION

- · High Breakdown Voltage-
- : V_{CBO}= 1500V (Min)
- · High Switching Speed
- · Low collector saturation voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

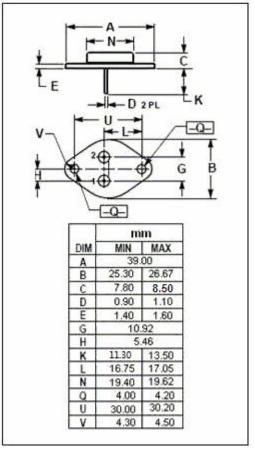


APPLICATIONS

• Designed for TV horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	600	V
V_{EBO}	Emitter-Base Voltage	/oltage 6	
Ic	Collector Current-Continuous	4	Α
I _{CM}	Collector Current-Peak 6		Α
Pc	Collector Power Dissipation @ T _C =25 °C	50	W
T_J	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-45-150	°C





isc Silicon NPN Power Transistor

2SD783

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	6		V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	600		V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3.5A; I _B = 1A		5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3.5A; I _B = 1A		1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} =1500V;I _B = 0		0.5	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C =0		10	uA
h _{FE-1}	DC Current Gain	I _C = 0.3A ; V _{CE} = 5V	10	36	
h _{FE-2}	DC Current Gain	I _C = 3A; V _{CE} = 5V	7		
t _f	Fall Time	I _C = 3.0A; I _{B1} =0.6 A; I _{B2} = 1.5A		2.0	μ S

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