

**isc Silicon NPN Power Transistor****2SD74****DESCRIPTION**

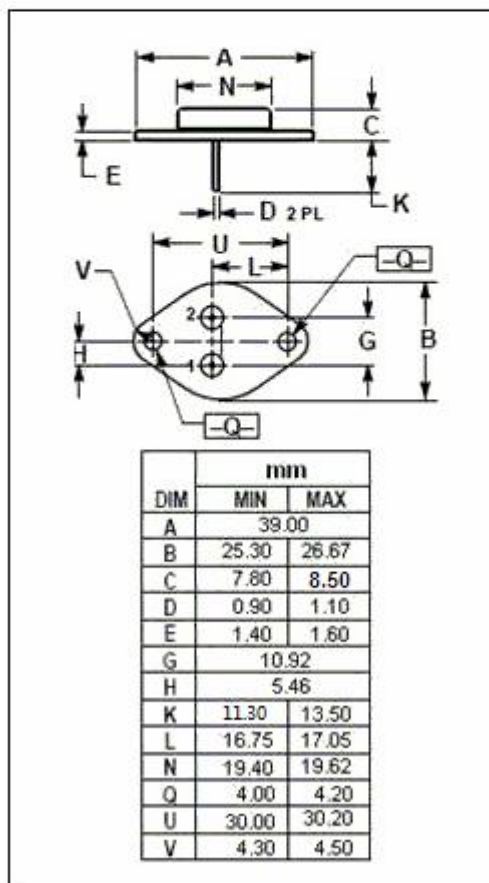
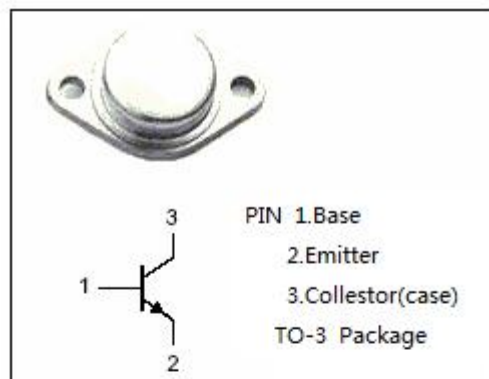
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = 90V(\text{Min})$
- Good Linearity of  $h_{FE}$
- Wide Area of Safe Operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

- Designed for use in general purpose power amplifier and switching applications

**ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	150	V
$V_{CEO}$	Collector-Emitter Voltage	90	V
$V_{EBO}$	Emitter-Base Voltage	5	V
$I_C$	Collector Current-Continuous	5	A
$I_{CM}$	Collector Current-Peak	8	A
$P_C$	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	60	W
$T_J$	Junction Temperature	175	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-65~175	$^\circ\text{C}$



**isc Silicon NPN Power Transistor****2SD74****ELECTRICAL CHARACTERISTICS****T<sub>C</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 30mA; I <sub>B</sub> = 0	90			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA; I <sub>E</sub> = 0	150			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA; I <sub>C</sub> = 0	5			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			2.0	V
V <sub>BE(on)</sub>	Base -Emitter On Voltage	I <sub>C</sub> = 1A; V <sub>CE</sub> = 5V			1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 150V; I <sub>E</sub> = 0			100	uA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 5V; I <sub>C</sub> = 0			100	uA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = 1A; V <sub>CE</sub> = 5V	60		200	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = 4A; V <sub>CE</sub> = 5V	20			
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 1A; V <sub>CE</sub> = 5V		20		MHz

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