

# isc Silicon NPN Power Transistor

## 2SD362

### DESCRIPTION

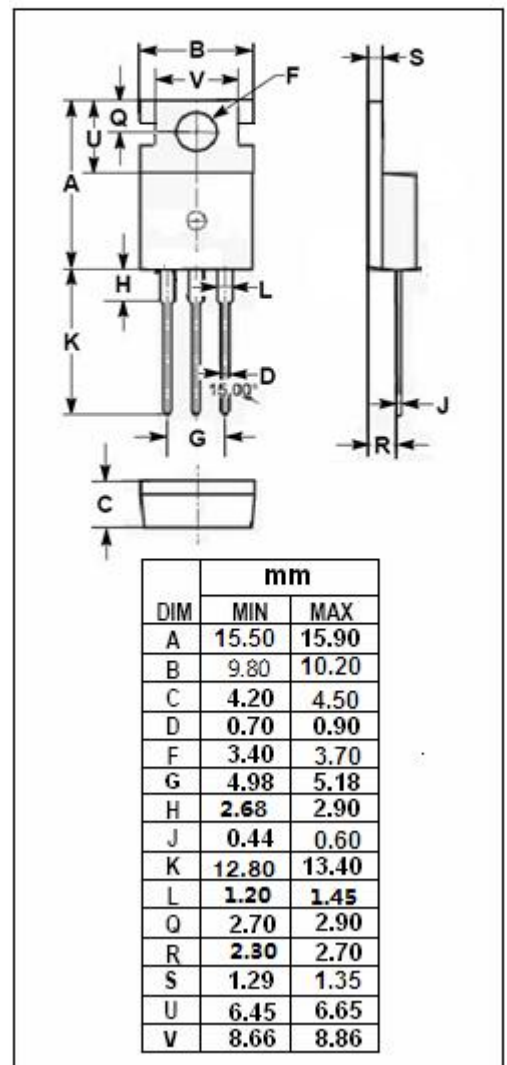
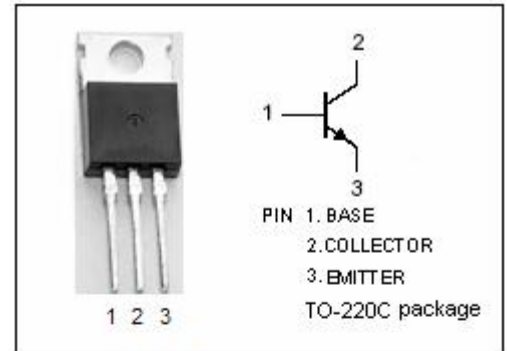
- Collector-Emitter Breakdown Voltage-  
:  $V_{(BR)CEO} = 70V(\text{Min})$
- Collector Power Dissipation-  
:  $P_C = 40W(\text{Max}) @ T_C = 25^\circ\text{C}$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

- Designed for B/W TV horizontal deflection output applications.

### ABSOLUTE MAXIMUM RATINGS( $T_a = 25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	150	V
$V_{CEO}$	Collector-Emitter Voltage	70	V
$V_{EBO}$	Emitter-Base Voltage	8	V
$I_C$	Collector Current-Continuous	5	A
$P_C$	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	40	W
$T_J$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ\text{C}$



**isc Silicon NPN Power Transistor****2SD362****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 2mA; R <sub>BE</sub> = ∞	70			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA; I <sub>E</sub> = 0	150			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 1mA; I <sub>C</sub> = 0	8			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			1.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			1.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 100V; I <sub>E</sub> = 0			20	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 5V; I <sub>C</sub> = 0			20	μA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 5A; V <sub>CE</sub> = 5V	20		140	
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 0.5A; V <sub>CE</sub> = 5V		10		MHz

**◆ h<sub>FE</sub> Classifications**

N	R	O
20-50	40-80	70-140

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