

isc Silicon NPN Power Transistor

2SD334

DESCRIPTION

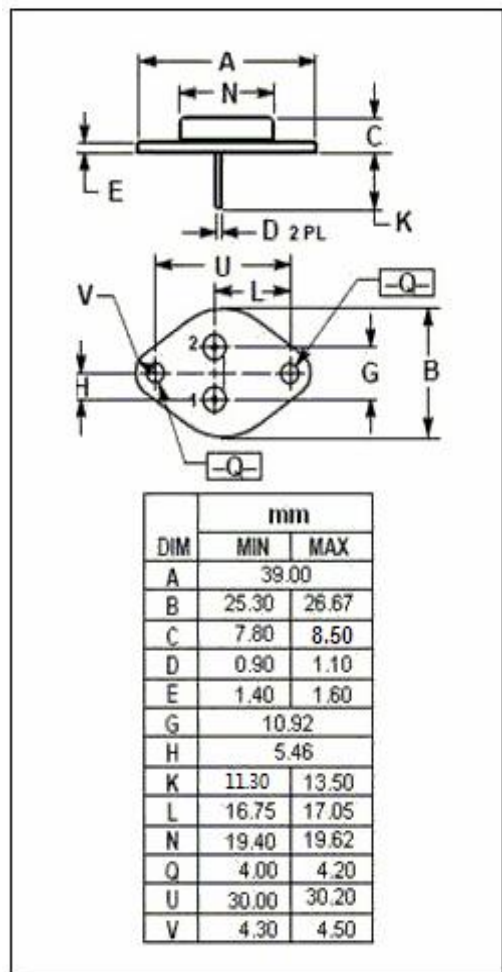
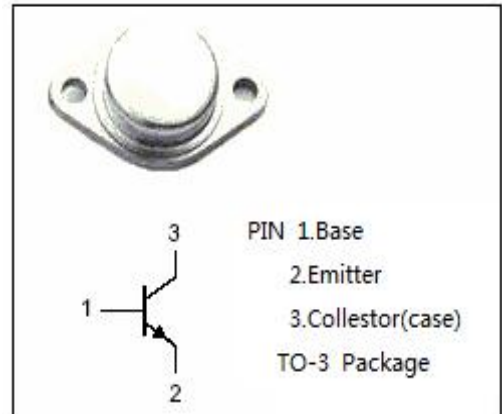
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V$ (Min)
- Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	MAX	UNIT
V_{CBO}	Collector-Base Voltage	110	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	6	A
I_B	Base Current-Continuous	3	A
P_C	Collector Power Dissipation @ $T_c=25^{\circ}C$	75	W
T_j	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-65~150	$^{\circ}C$



isc Silicon NPN Power Transistor**2SD334****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA ; I _B = 0	80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA ; I _C = 0	7			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A ; I _B = 0.5A			2	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 1A ; V _{CE} = 4V			2.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 40V ; I _E = 0			50	μ A
		V _{CB} = 110V ; I _E = 0			1	mA
h _{FE}	DC Current Gain	I _C = 1A ; V _{CE} = 4V	40		260	

◆ **h_{FE} Classifications**

R	O	Y
40-80	70-150	130-260

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