

isc Silicon NPN Power Transistor

2SD2062

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
- : V_{(BR)CEO}= 80V(Min)
- · Good Linearity of hFE
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

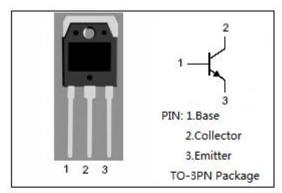


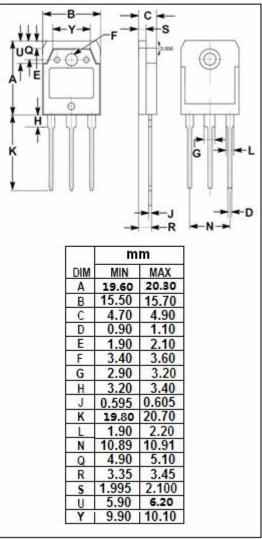
APPLICATIONS

• Designed for audio and general purpose applications



SYMBOL	PARAMETER	VALUE	UNIT
V _{СВО}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	80	V
V _{EBO}	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	7	Α
l _Β	Base Current-Continuous	3	Α
Pc	Collector Power Dissipation @ T _C =25 ℃	80	W
Тл	Junction Temperature	150	℃
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$







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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA; I _B = 0	80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			10	μА
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	60		320	
h _{FE-2}	DC Current Gain	I _C = 4A; V _{CE} = 5V	35			
f⊤	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 10V		20		MHz

♦ h_{FE-1} Classifications

D	E	F
60-120	100-200	160-320

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