

isc Silicon NPN Power Transistor

2SD2058

DESCRIPTION

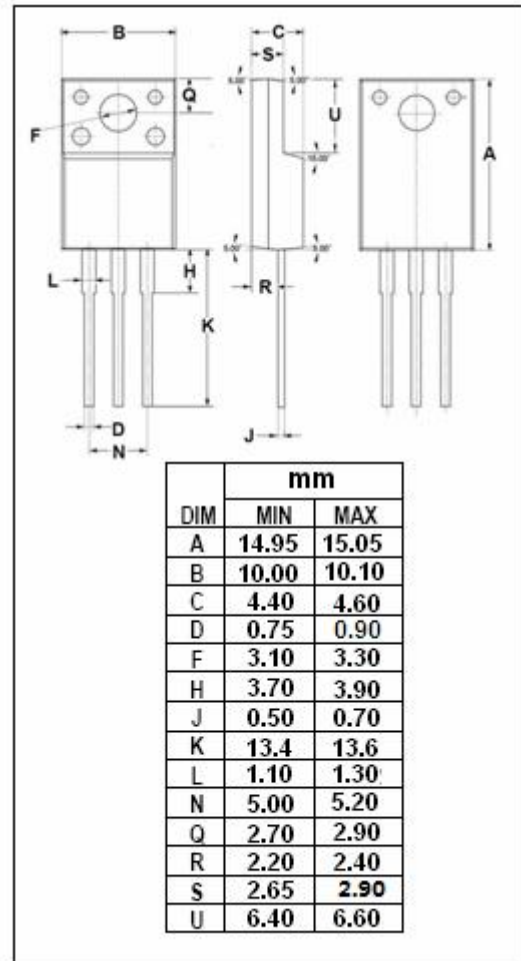
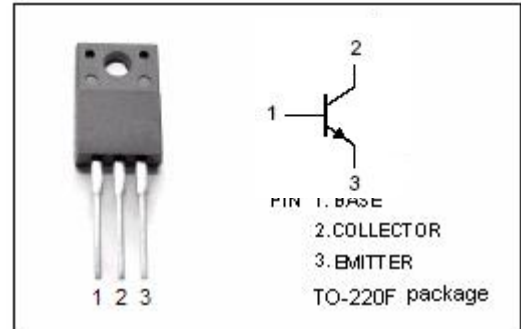
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 60V$ (Min)
- Collector Power Dissipation
: $P_C = 25 W$ (Max)
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	60	V
V_{CEO}	Collector-Emitter Voltage	60	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	3	A
I_B	Base Current-Continuous	0.5	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	25	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature	-55~150	$^\circ C$



isc Silicon NPN Power Transistor**2SD2058****ELECTRICAL CHARACTERISTICS****T_j=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA; I _B = 0	60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 2A; I _B = 0.2A			1.5	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 0.5A; V _{CE} = 5V			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 60V; I _E = 0			10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			1	mA
h _{FE}	DC Current Gain	I _C = 0.5A; V _{CE} = 5V	60		300	
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1MHz		35		pF
f _T	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 5V		3		MHz

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