

isc Silicon NPN Power Transistor

2SD1897

DESCRIPTION

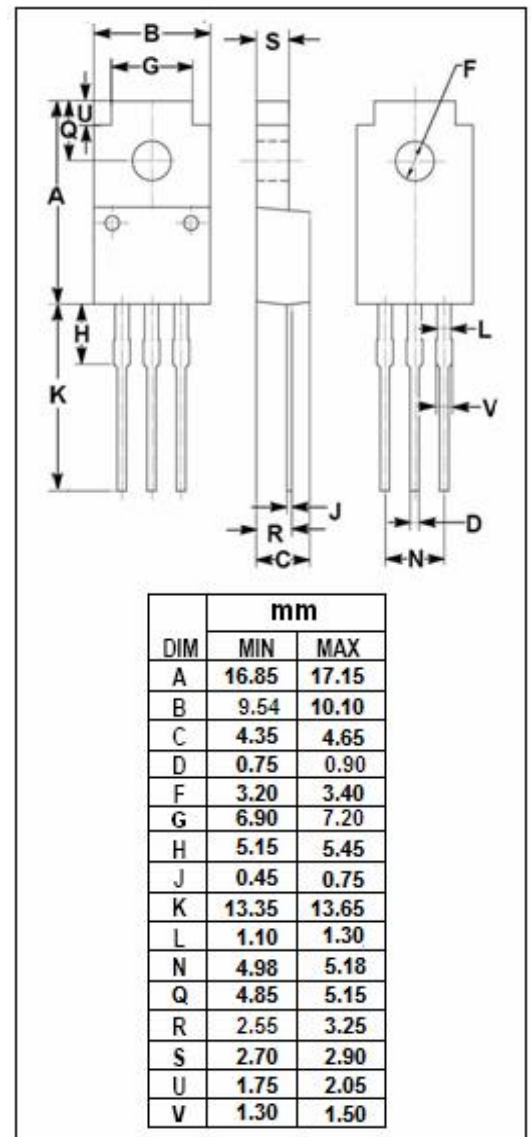
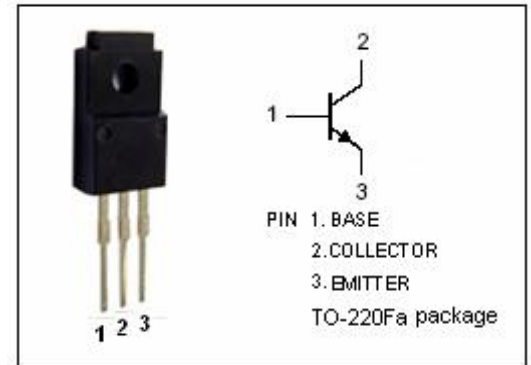
- Low Collector Saturation Voltage
: $V_{CE(sat)} = 1.0V(\text{Max.}) @ I_C = 3A$
- High Collector Power Dissipation
- Good Linearity of h_{FE}
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	100	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	5	A
I_{CM}	Collector Current-Pulse	10	A
P_C	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	2	W
	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	30	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD1897****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	100			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μA; I _E = 0	100			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 50 μA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			10	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			10	μA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	100		200	
f _T	Current-Gain—Bandwidth Product	I _E = 0.5A; V _{CE} = 5V		8		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1.0MHz		100		pF

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