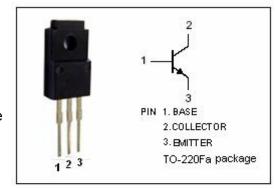


isc Silicon NPN Power Transistor

DESCRIPTION

- High Collector Current:: I_C= 4A
- · Low Collector Saturation Voltage
 - : V_{CE(sat)}= 1.0V(Max)@I_C= 3A
- · Wide Area of Safe Operation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



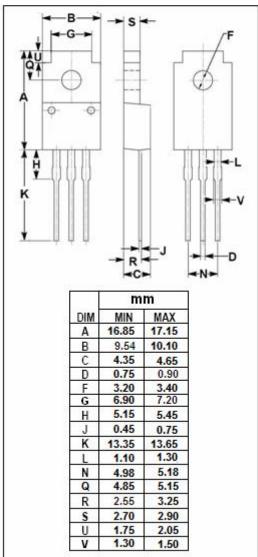


APPLICATIONS

• Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	80	V	
Vceo	Collector-Emitter Voltage	60	V	
V _{EBO}	Emitter-Base Voltage	5	V	
Ic	Collector Current-Continuous	4	А	
Ісм	Collector Current-Peak	6	Α	
Pc	Total Power Dissipation @ Tc=25°C	30	W	
TJ	Junction Temperature	150	$^{\circ}\!$	
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$	





isc Silicon NPN Power Transistor

2SD1855

ELECTRICAL CHARACTERISTICS

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 1mA; I _B = 0	60			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μ A; I _E = 0	80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 50 μ A; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.3A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 60V; I _E = 0			10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			10	μА
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	60		320	
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1MHz		90		pF
f⊤	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 5V		8		MHz

♦ h_{FE} Classifications

D	E	F		
60-120	100-200	160-320		

NOTICE:

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