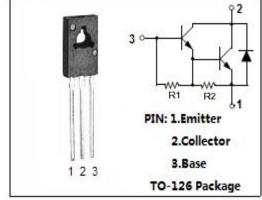


isc Silicon NPN Darlington Power Transistor

2SD1638

DESCRIPTION

- · High DC Current Gain-
- : h_{FE} = 1000(Min)@ I_C= 1A
- · Collector-Emitter Breakdown Voltage-
- : $V_{(BR)CEO} = 100V(Min)$
- · Low Collector-Emitter Saturation Voltage-
 - : V_{CE(sat)} = 1.5V(Max)@ I_C= 1A
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

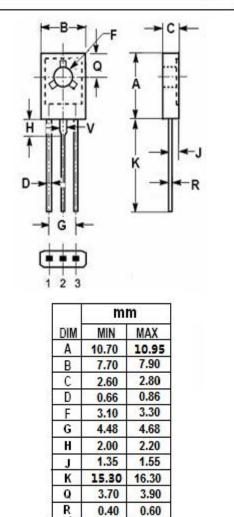


APPLICATIONS

• Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	100	V
V _{EBO}	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	2	А
Ісм	Collector Current-Peak	3	А
P _C	Collector Power Dissipation T _C =25℃	15	10/
	Collector Power Dissipation T _a =25℃	1.2	W
T _j	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$



1.17

1.37



isc Silicon NPN Darlington Power Transistor

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _(BR) CEO	Collector-Emitter Breakdown Voltage	I _C = 5mA, I _B = 0	100			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 50 μ A, I _E = 0	100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A ,I _B = 1mA			1.5	V
Ісво	Collector Cutoff Current	V _{CB} = 100V, I _E = 0			10	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			3	mA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 2V	1000		10000	
Сов	Output Capacitance	I _E = 0 ; V _{CB} = 10V,f= 1MHz		25		pF



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