

isc Silicon NPN Power Transistor

2SD1456

DESCRIPTION

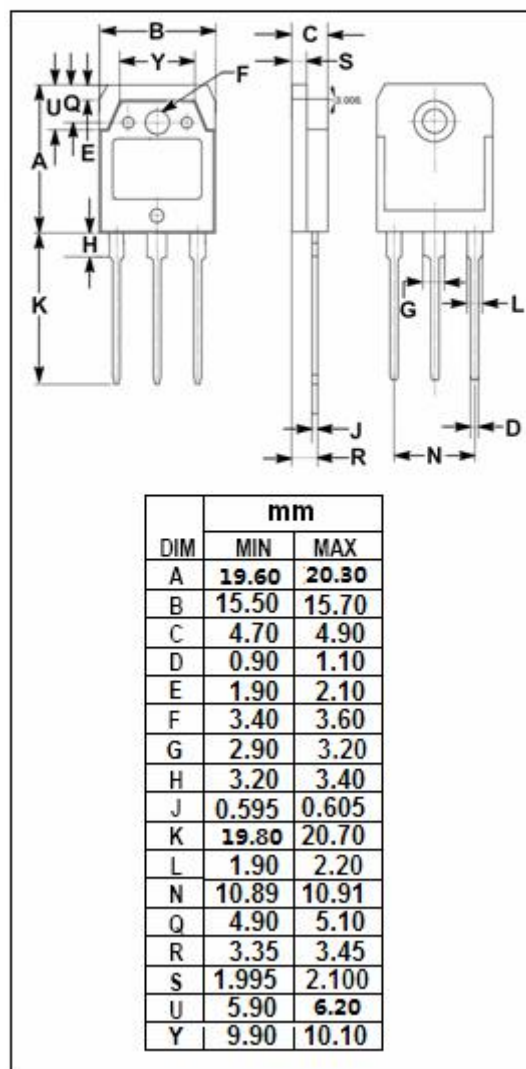
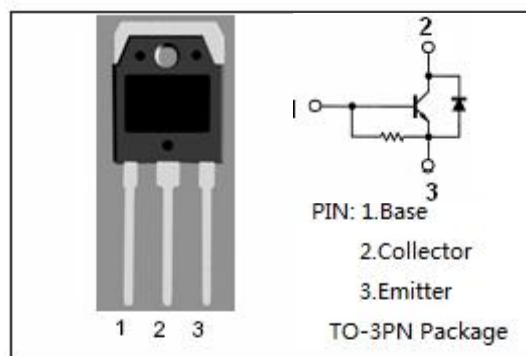
- High Breakdown Voltage-
: $V_{CBO} = 1500V$ (Min)
- High Switching Speed
- Built-in Damper Diode
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for TV horizontal deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CES}	Collector-Emitter Voltage	1500	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	6	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	50	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-45~150	$^\circ C$



isc Silicon NPN Power Transistor**2SD1456****ELECTRICAL CHARACTERISTICS**T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 200mA; I _C = 0	6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 1A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 1A			1.5	V
I _{CES}	Collector Cutoff Current	V _{CE} = 1500V; R _{BE} = ∞			0.5	mA
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	6			
V _{ECF}	C-E Diode Forward Voltage	I _F = 6A			2.2	V

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