

isc Silicon NPN Power Transistor

2SC5855

DESCRIPTION

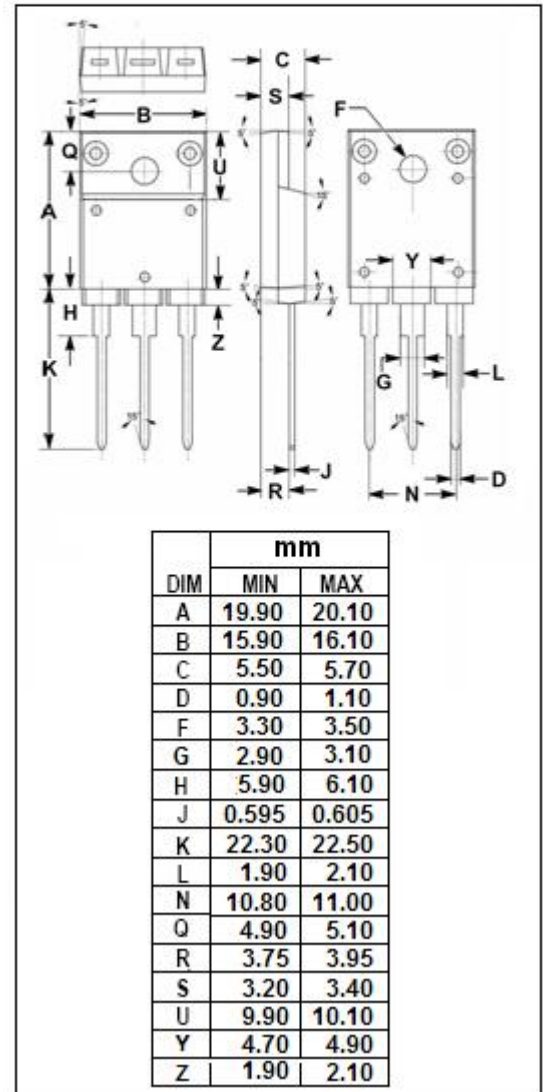
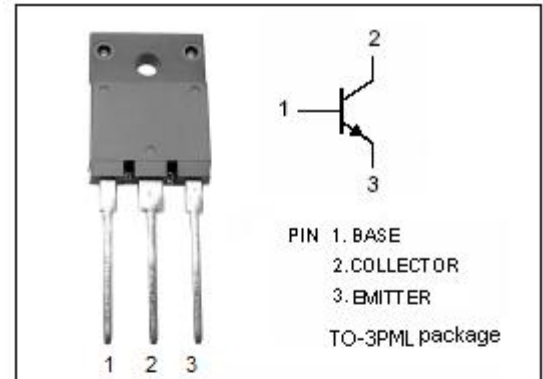
- High speed switching
- High voltage
- Low saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Horizontal deflection output for super high resolution
- Display color TV digital TV

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	700	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	10	A
I_B	Base Current-Continuous	5	A
P_C	Collector Power Dissipation @ $T_C=25^{\circ}\text{C}$	50	W
T_J	Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



isc Silicon NPN Power Transistor**2SC5855****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C =10mA; I _B = 0	700			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E =1mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =8A; I _B = 2A			3	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C =8A; I _B = 2A			1.4	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 1500V; I _E = 0			100	μ A
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	28		60	
h _{FE-2}	DC Current Gain	I _C =6A; V _{CE} = 5V	6.2		10	
h _{FE-3}	DC Current Gain	I _C =8A; V _{CE} = 5V	4.3		6.7	

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