

isc Silicon NPN Power Transistor

2SC4589

DESCRIPTION

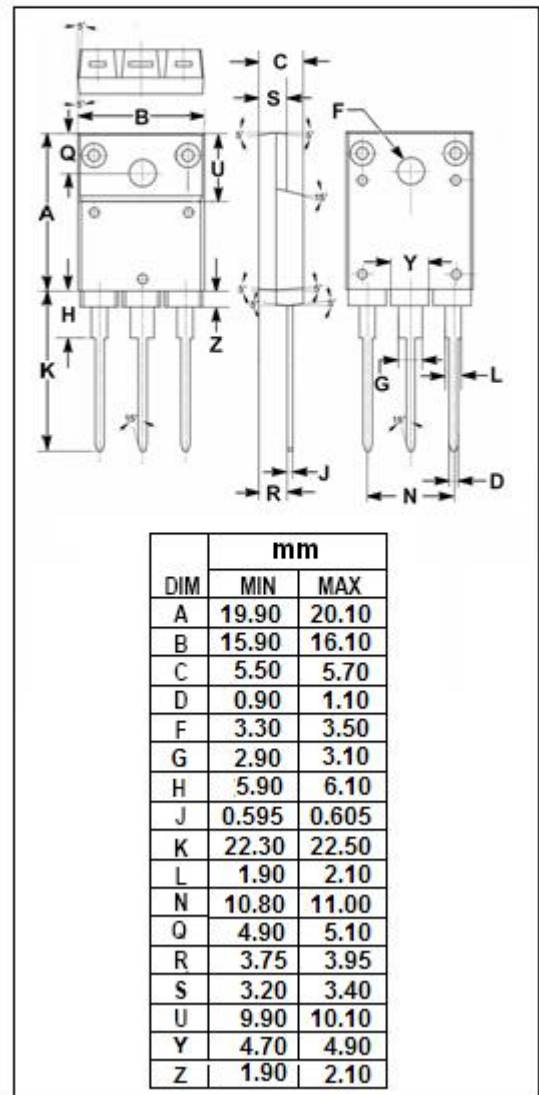
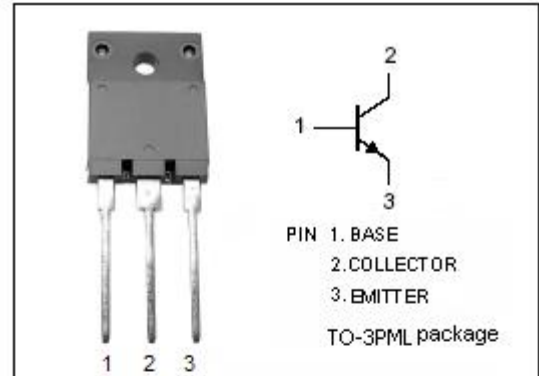
- High Breakdown Voltage-
: $V_{CBO} = 1500V$ (Min)
- High Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for CTV/character display horizontal deflection output stage applications

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	800	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current- Continuous	10	A
$I_{C(surge)}$	Collector Current-Surge	20	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	50	W
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



isc Silicon NPN Power Transistor**2SC4589****ELECTRICAL CHARACTERISTICS****T_C=25℃ unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; R _{BE} = ∞	800			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 10mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 8A; I _B = 1.6A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 8A; I _B = 1.6A			1.5	V
I _{CES}	Collector Cutoff Current	V _{CE} = 1500V; R _{BE} = 0			500	μ A
h _{FE}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8		38	
t _f	Fall Time	I _{CP} = 7A , I _{B1} = 1.4A			0.5	μ s

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