## DATA SHEET

# SILICON POWER TRANSISTOR 2SC4331,4331-Z

## NPN SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SC4331 and 2SC4331-Z are mold power transistors developed for high-speed switching and features a very low collector-to-emitter saturation voltage.

This transistor is ideal for use in switching regulators, DC/DC converters, motor drivers, solenoid drivers, and other low-voltage power supply devices, as well as for high-current switching.

#### **FEATURES**

NEC

- · Available for high-current control in small dimension
- · Z type is a lead-processed product and is deal for mounting a hybrid IC.
- · Low collector saturation voltage VCE(sat) = 0.3 V MAX. (Ic = 3.0 A)
- · Fast switching speed:  $t_f \le 0.4 \ \mu s$  MAX. (Ic = 3.0 A)
- · High DC current gain and excellent linearity

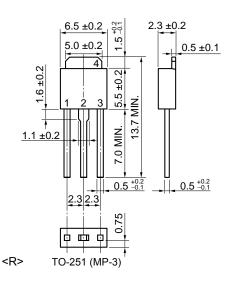
#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ )

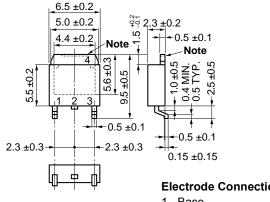
Collector to Base Voltage	Vсво	150	V
Collector to Emitter Voltage	VCEO	100	V
Emitter to Base Voltage	VEBO	7.0	V
Collector Current (DC)	C(DC)	5.0	А
Collector Current (pulse) Note 1	C(pulse)	10	А
Base Current (DC)	B(DC)	2.5	А
Total Power Dissipation (Tc = $25^{\circ}$ C)	<b>P</b> T1	15	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	<b>P</b> <sub>T2</sub>	1.0 <sup>Note 2</sup> , 2.0 <sup>Note 3</sup>	W
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	-55 to +150	°C

**Notes 1.**  $PW \le 10 \text{ ms}$ , duty cycle  $\le 50\%$ 

- 2. Printing board mounted
- **3.**  $7.5 \text{ cm}^2 \times 0.7 \text{ mm}$ , ceramic board mounted

#### PACKAGE DRAWING (Unit: mm)





TO-252 (MP-3Z)

#### 1. Base

- 2. Collector
- 3. Emitter
- 4. Collector Fin

Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)

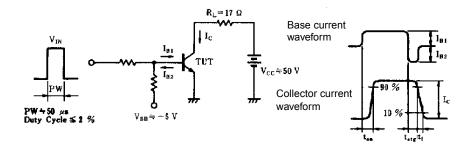
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Collector to emitter voltage	VCEO(SUS)	Ic = 2.5 A, I <sub>B</sub> = 0.25 A, L = 1 mH	100			V
	Collector to emitter voltage	VCEX(SUS)	Ic = 2.5 A, I <sub>B1</sub> = $-I_{B2}$ = 0.25 A, V <sub>BE(OFF)</sub> = $-1.5$ V, L = 180 $\mu$ H, clamped	100			V
	Collector cutoff current	Ісво	Vce = 100 V, Ie = 0			10	μA
	Collector cutoff current	ICER	Vce = 100 V, Rbe = 50 Ω, Ta = 125°C			1.0	mA
	Collector cutoff current	ICEX1	VCE = 100 V, VBE(OFF) = -1.5 V			10	μA
et4U.con	Collector cutoff current	ICEX2	$V_{CE} = 100 \text{ V}, \text{ V}_{BE(OFF)} = -1.5 \text{ V},$ TA = 125°C			1.0	mA
	Emitter cutoff current	Іево	V <sub>EB</sub> = 5.0 V, I <sub>C</sub> = 0			10	μA
	DC current gain <sup>Note</sup>	hfe1	V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 0.5 A	100			
	DC current gain <sup>Note</sup>	hfe2	V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 1.0 A	100	200	400	
	DC current gain <sup>Note</sup>	hfe3	V <sub>CE</sub> = 2.0 V, I <sub>C</sub> = 3.0 A	60			
	Collector saturation voltage Note	V <sub>CE(sat)1</sub>	Ic = 3.0 A, I <sub>B</sub> = 0.15 A			0.3	V
	Collector saturation voltage Note	V <sub>CE(sat)2</sub>	Ic = 4.0 A, I <sub>B</sub> = 0.2 A			0.5	V
	Base saturation voltage Note	V <sub>BE(sat)1</sub>	Ic = 3.0 A, I <sub>B</sub> = 0.15 A			1.2	V
	Base saturation voltage Note	V <sub>BE(sat)2</sub>	Ic = 4.0 A, I <sub>B</sub> = 0.2 A			1.5	V
	Collector capacitance	Cob	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1.0 MHz		60		pF
	Gain bandwidth product	f⊤	V <sub>CE</sub> = 10 V, I <sub>E</sub> = -0.5 A		150		MHz
	Turn-on time	ton	Ic = 3.0 A, R <sub>L</sub> = 17 Ω, I <sub>B1</sub> = $-I_{B2}$ = 0.15 A, Vcc $\cong$ 50 V Refer to the test circuit.			0.3	μs
	Storage time	tstg				1.5	μs
	Fall time	tr				0.4	μs

**Note** Pulse test PW  $\leq$  350  $\mu$ s, duty cycle  $\leq$  2%

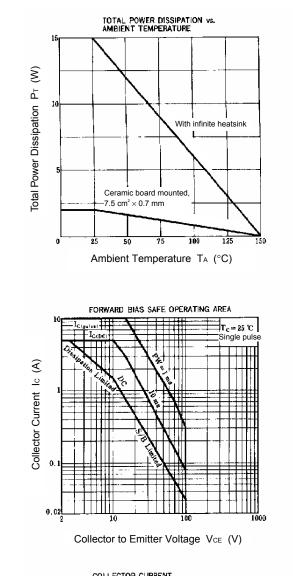
#### **hfe CLASSIFICATION**

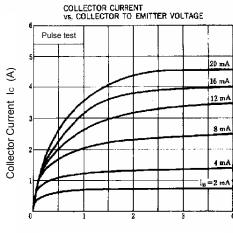
Marking	М	L	к
hFE2	100 to 200	150 to 300	200 to 400

### SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT

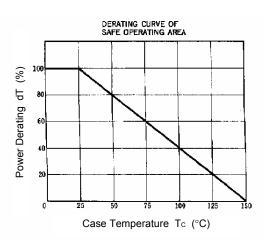


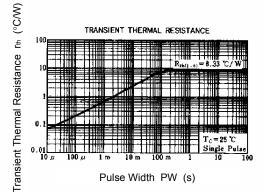
#### TYPICAL CHARACTERISTICS (TA = 25°C)

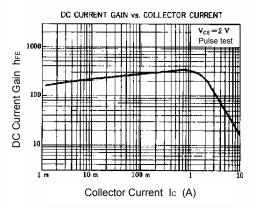




Collector to Emitter Voltage VCE (V)

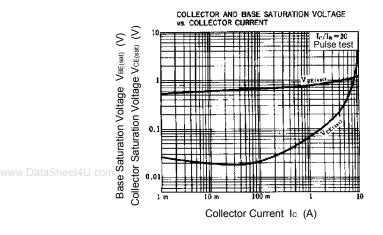






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