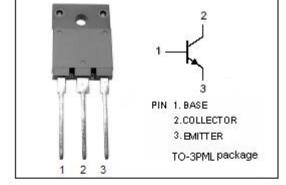


isc Silicon NPN Power Transistor

DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= 800V(Min)
- · High Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

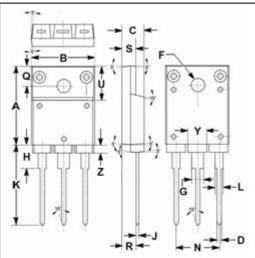


APPLICATIONS

 Designed for switching regulator and general purpose applications.



SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	900	V
Vceo	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	7	V
Ic	Collector Current-Continuous	7	А
Ісм	Collector Current-Peak	14	А
l _Β	Base Current-Continuous	3.5	А
Pc	Collector Power Dissipation @T _C =25℃	80	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature	-55~150	$^{\circ}$



	mm		
DIM	MIN	MAX	
Α	19.90	20.10	
В	15.90	16.10	
С	5.50	5.70	
D	0.90	1.10	
F	3.30	3.50	
G	2.90	3.10	
Н	5.90	6.10	
J	0.595	0.605	
K	22.30	22.50	
L	1.90	2.10	
N	10.80	11.00	
0	4.90	5.10	
R	3.75	3.95	
S	3.20	3.40	
U	9.90	10.10	
Y	4.70	4.90	
Z	1.90	2.10	



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ELECTRICAL CHARACTERISTICS

Ti=25℃ unless otherwise specified

Tj=25°C unless otherwise specified									
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT			
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	800			V			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.6A			0.5	V			
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.6A			1.2	V			
I _{CBO}	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			100	μА			
ІЕВО	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			100	μА			
h _{FE}	DC Current Gain	I _C = 3A; V _{CE} = 4V	10		30				
Сов	Output Capacitance	I _E = 0; V _{CB} = 10V; f= 1MHz		105		pF			
f _T	Current-Gain—Bandwidth Product	I _E = -1A; V _{CE} = 12V		6		MHz			
Switching Times									
t _{on}	Turn-On Time				1.0	μS			
t _{stg}	Storage Time	I _C = 3A; I _{B1} = 0.45A; I _{B2} = -1.5A; V _{CC} = 250V; R _L = 83 Ω			5.0	μS			
tf	Fall Time				1.0	μS			

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