

Silicon NPN Power Transistors

2SC4140

DESCRIPTION

- With TO-3PN package
- High voltage
- High speed switching

APPLICATIONS

- For switching regulator and general purpose applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

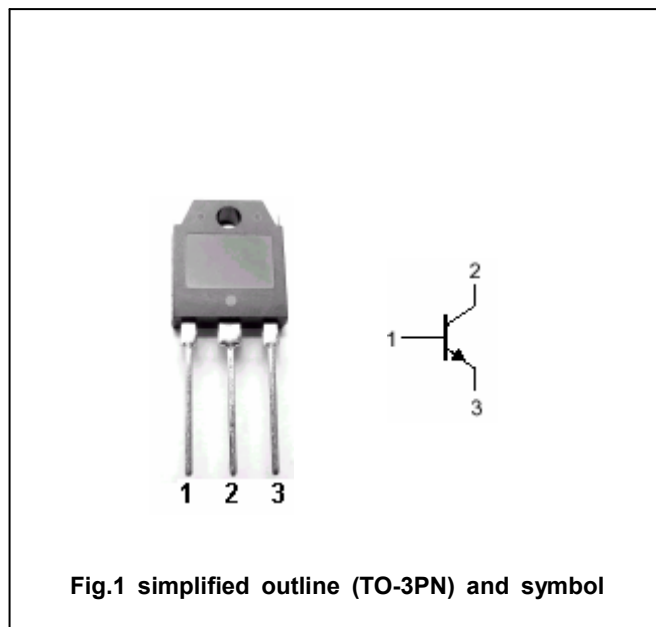


Fig.1 simplified outline (TO-3PN) and symbol

Absolute maximum ratings($T_a = \square$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	500	V
V_{CEO}	Collector-emitter voltage	Open base	400	V
V_{EBO}	Emitter-base voltage	Open collector	10	V
I_C	Collector current		18	A
I_{CP}	Collector current-pulse		36	A
I_B	Base current		6	A
P_C	Collector power dissipation	$T_C = 25\square$	130	W
T_j	Junction temperature		150	$\square$
T_{stg}	Storage temperature		-55~150	$\square$

Silicon NPN Power Transistors**2SC4140****CHARACTERISTICS****T_j=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =25mA ; I _B =0	400			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =10A; I _B =2A			0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =10A; I _B =2A			1.3	V
I _{CBO}	Collector cut-off current	V _{CB} =500V ; I _E =0			100	μA
I _{EBO}	Emitter cut-off current	V _{EB} =10V; I _C =0			100	μA
h _{FE}	DC current gain	I _C =10A ; V _{CE} =4V	10		30	
f _T	Transition frequency	I _E =-2A ; V _{CE} =12V		10		MHz
C _{OB}	Collector output capacitance	f=1MHz ; V _{CB} =10V		165		pF

Switching times

t _{on}	Turn-on time	I _C =10A; I _{B1} =1A; I _{B2} =-2A; R _L =20Ω V _{CC} =200V			1.0	μs
t _{stg}	Storage time				3.0	μs
t _f	Fall time				0.5	μs

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PACKAGE OUTLINE

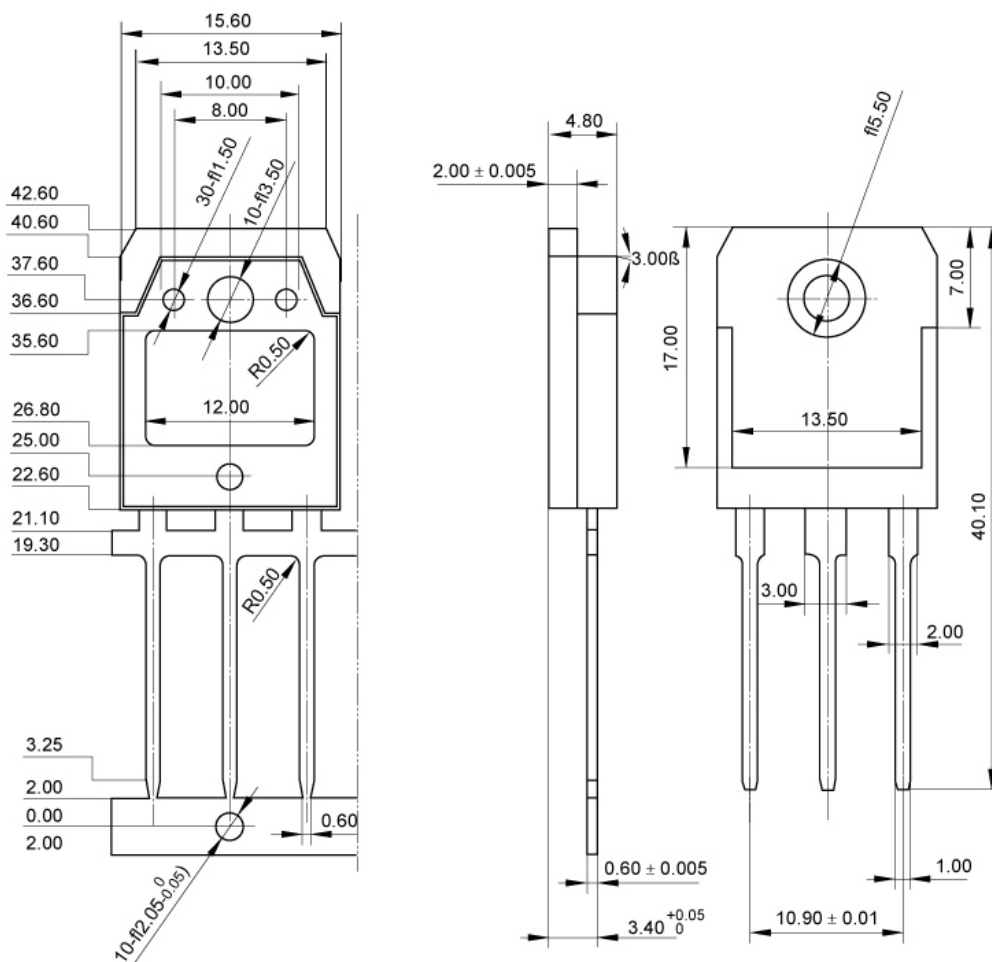


Fig.2 outline dimensions (unindicated tolerance: ±0.1mm)

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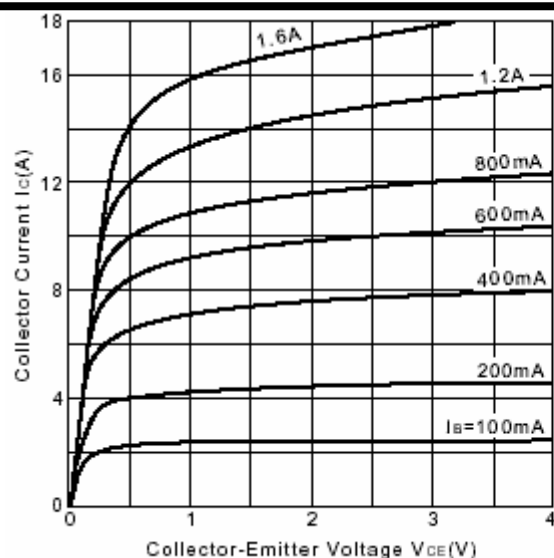


Fig.3 Static Characteristic

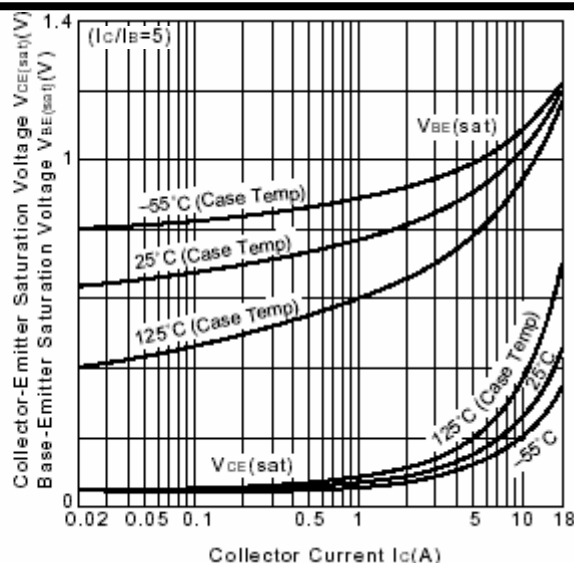
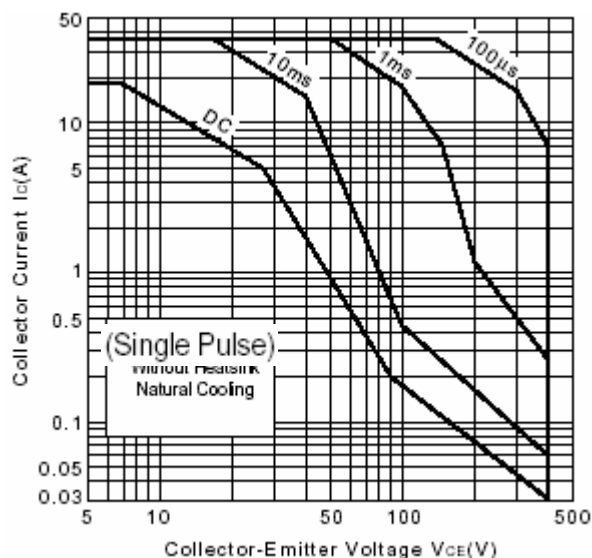
Fig.4 Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

Fig.5 Safe Operating Area

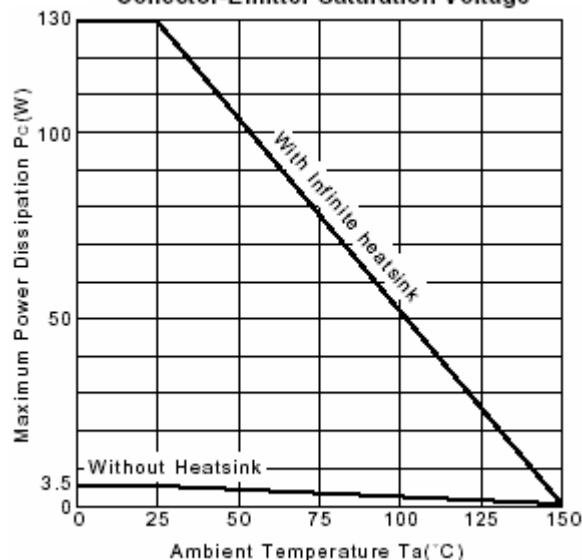
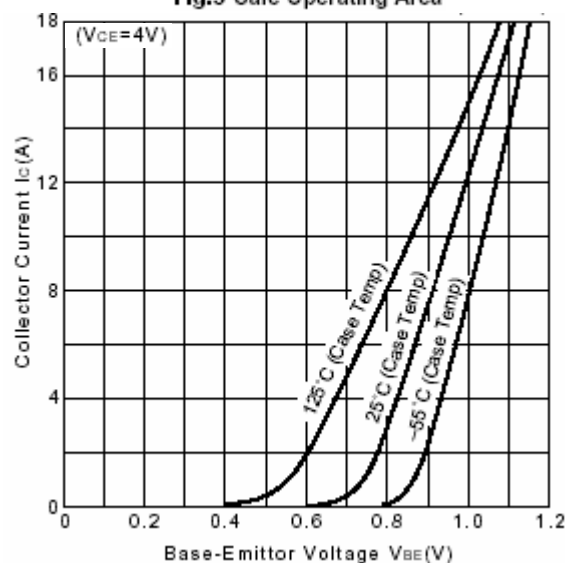
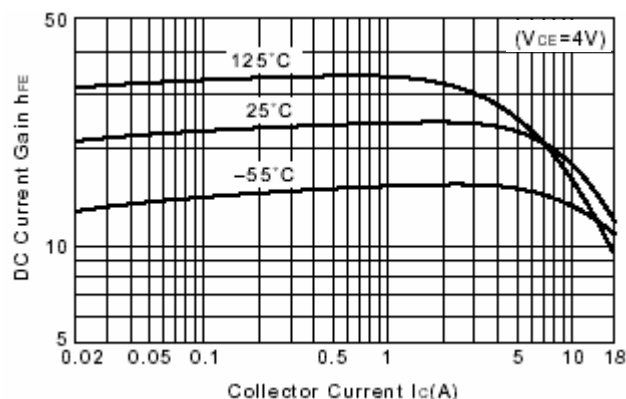
Fig.6 P_C - T_a DeratingFig.7 I_C - V_{BE} 

Fig.8 DC current Gain