

isc Silicon NPN Power Transistor

DESCRIPTION

- With MT-200 package
- · High power dissipation
- High current capability
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

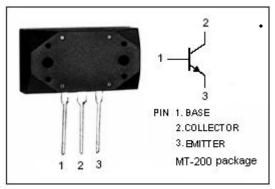


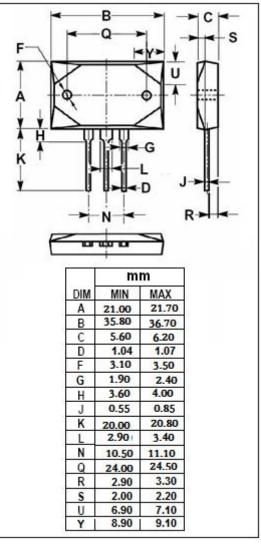
APPLICATIONS

Design for audio power amplifier and general purpose applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	200	V	
V _{CEO}	Collector-Emitter Voltage	200	V	
V _{EBO}	Emitter-Base Voltage		V	
lc	Collector Current-Continuous	ent-Continuous 15		
l _Β	Base Current-Continuous	5	Α	
Pc	Collector Power Dissipation @ T _C =25°C		W	
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	${\mathbb C}$	







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2SC2773

CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =30mA ; I _B =0	200			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =10A; I _B =1.0A			3.0	V
I _{CBO}	Collector cut-off current	V _{CB} =200V; I _E =0			100	μА
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			100	μА
h _{FE}	DC current gain	I _C =5A; V _{CE} =4V	50		180	
C _{ob}	Output capacitance	I _E =0 ; V _{CB} =10V;f=1MHz		250		pF
f⊤	Transition frequency	I _C =0.5A; V _{CE} =12V		20		MHz

♦ h _{FE} classifications						
Ο	Р	Y				
50-100	70-140	90-180				

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