

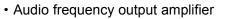
isc Silicon NPN Power Transistor

2SC2556

DESCRIPTION

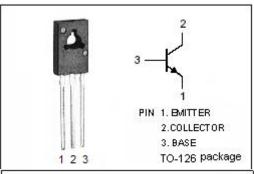
- · High transistor frequency
- Low Saturation Voltage
- High V_{CBO}
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

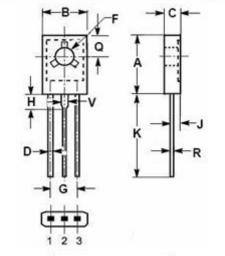
APPLICATIONS



ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	130	V
V _{CER}	Collector-Emitter Voltage R_{BE} =150 Ω	130	V
V _{CEO}	Collector-Emitter Voltage	40	V
V _{EBO}	Emitter-Base Voltage	5	V
lc	Collector Current-Continuous	1	Α
I _{CM}	Collector Current-Peak	1.5	Α
Pc	Collector Power Dissipation @ Tc=25℃	5	W
TJ	Junction Temperature 150		$^{\circ}$
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$





	mm		
DIM	MIN	MAX	
Α	10.70	10.95	
В	7.70	7.90	
C	2.60	2.80	
D	0.66	0.86	
F	3.10	3.30	
G	4.48	4.68	
н	2.00	2.20	
J	1.35	1.55	
K	15.30	16.30	
Q	3.70	3.90	
R	0.40	0.60	
V	1.17	1.37	



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ELECTRICAL CHARACTERISTICS

 T_c =25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =0.5A; I _B = 0.05A			0.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 130V ; I _E = 0			1	μА
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			1	μ А
h _{FE-1}	DC Current Gain	I _C = 500mA ; V _{CE} = 2V	150			
h _{FE-2}	DC Current Gain	I _C = 1A; V _{CE} = 0.5V	150		350	



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