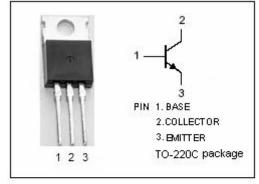


isc Silicon NPN Power Transistor

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DESCRIPTION

- Collector-Emitter Breakdown Voltage-
 - :V_{(BR)CEO}= 80(V)(Min.)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

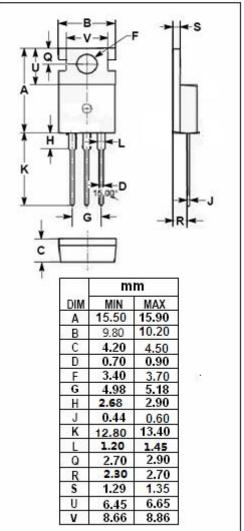


APPLICATIONS

· Series regulator, switch and general purpose applications

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	100	V
V _{CEO}	Collector-Emitter Voltage	80	V
V _{EBO}	Emitter-Base Voltage	6	V
Ic	Collector Current-Continuous	6	А
Pc	Total Power Dissipation @ T _C =25℃	50	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$ C





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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _(BR) CEO	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	80			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 60mA			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			0.1	mA
ІЕВО	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			0.1	mA
h _{FE}	DC Current Gain	I _C = 0.5A; V _{CE} = 4V	500			
f⊤	Current-Gain—Bandwidth Product	I _E = 100mA; V _{CE} = 12V		30		MHz



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