

isc Silicon NPN Power Transistor

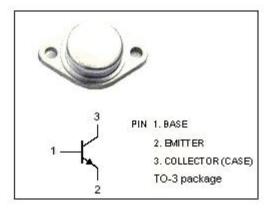
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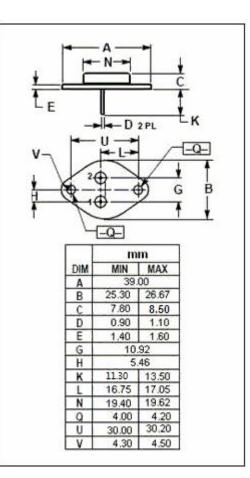
DESCRIPTION

- · Collector-Emitter Breakdown Voltage-
 - : V_{(BR)CEO}= 100V (Min)
- Wide Area of Safe Operation
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation



• Designed for high speed switching and power amplifier applications.





ABSOLUTE MAXIMUM RATINGS(Ta=25°C)							
SYMBOL	PARAMETER	МАХ	UNIT				
V _{CBO}	Collector-Base Voltage	150	V				
V _{CEO}	Collector-Emitter Voltage	100	V				
V_{EBO}	Emitter-Base Voltage	5	V				
lc	Collector Current-Continuous	10	A				
Pc	Collector Power Dissipation @T _c =25℃	80	W				
Tj	Junction Temperature	150	°C				
T _{stg}	Storage Temperature Range	-55~150	°C				

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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 50mA; I _B = 0	100			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 1mA; I _E = 0	150			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.8A			0.8	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.8A			1.5	V
h _{FE}	DC Current Gain	I _C = 4A; V _{CE} = 5V	40			
Ісво	Collector Cutoff Current	V _{CB} = 150V; I _E = 0			0.1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			0.1	mA

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