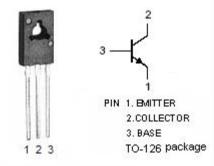


isc Silicon NPN Power Transistor

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DESCRIPTION

- High Collector-Emitter Breakdown Voltage-: V_{(BR)CEO}= 50V(Min)
- Good Linearity of h_{FE}
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

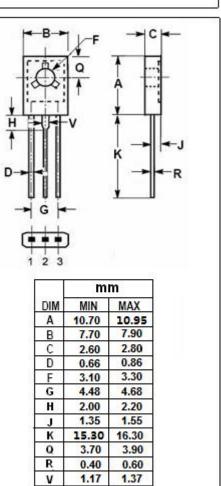


APPLICATIONS

Designed for low-frequency power amplification

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	80	V	
V _{CEO}	Collector-Emitter Voltage	itter Voltage 50		
V _{EBO}	Emitter-Base Voltage	6	V	
Ι _C	Collector Current-Continuous	1.5	A	
Pc	Collector Power Dissipation @ Ta=25℃	5	W	
TJ	Junction Temperature	150	°C	
T _{stg}	Storage Temperature Range	-55~150	°C	





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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	мах	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA ; I _B = 0	50			V
V _{(BR)EBO}	Emitter-Base Breakdown Vltage	I _E = 1mA ; I _C = 0	6			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 500mA; I _B = 50mA			0.5	V
Ісво	Collector Cutoff Current	V _{CB} = 80V; I _E = 0			1	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			1	μA
h _{FE}	DC Current Gain	Ic= 100mA ; V _{CE} = 5V	90		320	
f _T	Current-Gain—Bandwidth Product	I _C = 100mA;V _{CE} = 5V		250		MHz
Сов	Output Capacitance	I _E = 0 ; V _{CB} = 10V,f _{test} = 1MHz		18	40	pF

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