

isc Silicon NPN Power Transistor

2SC2023

DESCRIPTION

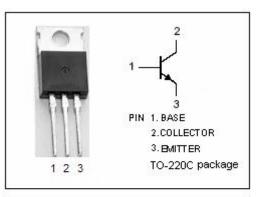
- Silicon NPN triple diffused planar transistor
- · Collector-Emitter Breakdown Voltage-:V_{(BR)CEO}= 300(V)(Min.)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

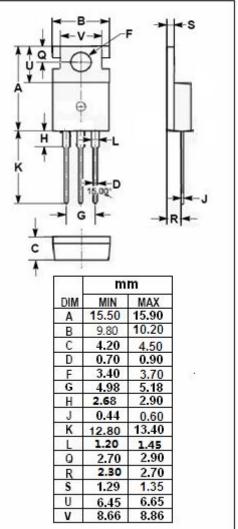
APPLICATIONS

· Series regulator, switch and general purpose applications

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)						
SYMBOL	PARAMETER	VALUE	UNIT			
V _{CBO}	Collector-Base Voltage	300	V			
V _{CEO}	Collector-Emitter Voltage	300	V			
V _{EBO}	Emitter-Base Voltage 6		V			
lc	Collector Current-Continuous	2	А			
Pc	Total Power Dissipation @ T _c =25℃	40	W			
TJ	Junction Temperature	150	°C			
T _{stg}	Storage Temperature Range	-55~150	Ĉ			

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ELECTRICAL CHARACTERISTICS

$T_c=25^{\circ}C$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	МАХ	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	300			V
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1.0A; I _B = 200mA			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 300V; I _E = 0			1	mA
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			1	mA
h _{FE}	DC Current Gain	I _C = 0.5A; V _{CE} = 4V	30			
fT	Current-Gain—Bandwidth Product	I _E = 200mA; V _{CE} = 12V		10		MHz

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