

isc Silicon NPN Power Transistor

2SC1904

DESCRIPTION

- · Low collector to emitter saturation voltage
- Output of 1W can be obtained by a complementary with 2SA899
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

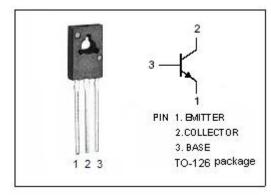


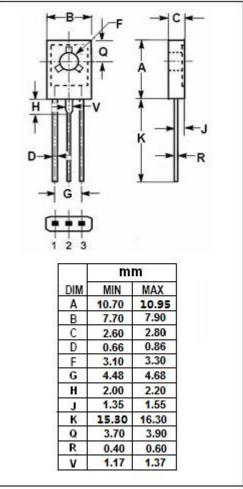
APPLICATIONS

High frequency power amplification

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	150	V	
V _{CEO}	Collector-Emitter Voltage 150		V	
V _{EBO}	Emitter-Base Voltage	5		
lc	Collector Current-Continuous	50	mA	
Pc	Collector Power Dissipation @ T _a =25℃	1	W	
	Collector Power Dissipation @ T_c =25 $^{\circ}$ C	4		
TJ	Junction Temperature	150	$^{\circ}$	
T _{stg}	Storage Temperature Range	-55~150	$^{\circ}$	







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 10mA; I _B = 1mA			0.5	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I _C = 10mA; I _B = 1mA			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 140V; I _E = 0			1.0	μА
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = 4V; I _C = 0			1.0	μА
h _{FE}	DC Current Gain	I _C = 10mA ; V _{CE} = 5V	35		500	
f⊤	Current-Gain—Bandwidth Product	I _C = 10mA; V _{CE} = 5V		70		MHz



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