

isc Silicon NPN Power Transistor

2SC1449

DESCRIPTION

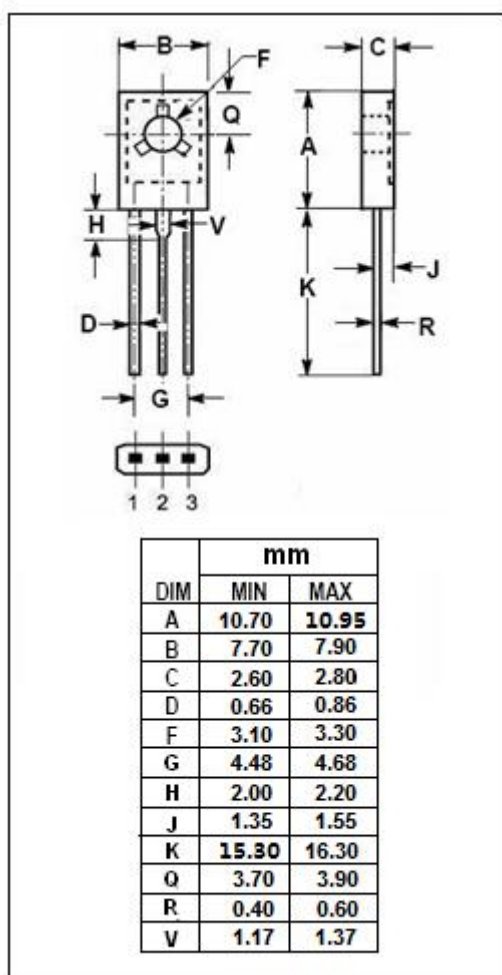
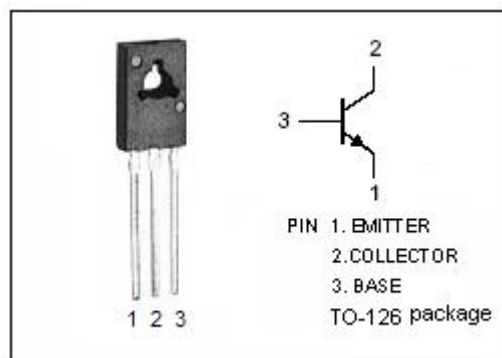
- High Collector Current $I_C = 2.0A$
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 35V(\text{Min})$
- Good Linearity of h_{FE}
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	40	V
V_{CEO}	Collector-Emitter Voltage	35	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	2.0	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ C$	1	W
	Collector Power Dissipation @ $T_a = 25^\circ C$	10	
T_J	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-55~150	$^\circ C$



isc Silicon NPN Power Transistor**2SC1449****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 1mA ; I _E = 0	40			V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA ; R _{BE} = ∞	35			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA ; I _C = 0	5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 0.5A ; I _B = 0.05A			0.7	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 0.5A ; I _B = 0.05A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 40V ; I _E = 0			100	μ A
h _{FE}	DC Current Gain	I _C = 300mA ; V _{CE} = 2V	40		250	
f _T	Current-Gain—Bandwidth Product	I _C = 100mA ; V _{CE} = 5V		55		MHz

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