

isc Silicon PNP Power Transistor

2SB954

DESCRIPTION

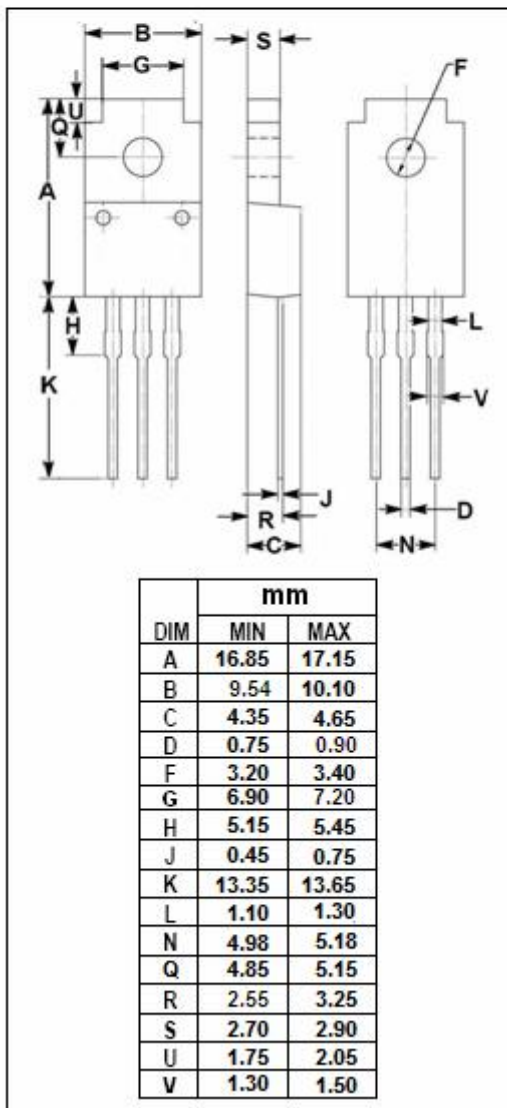
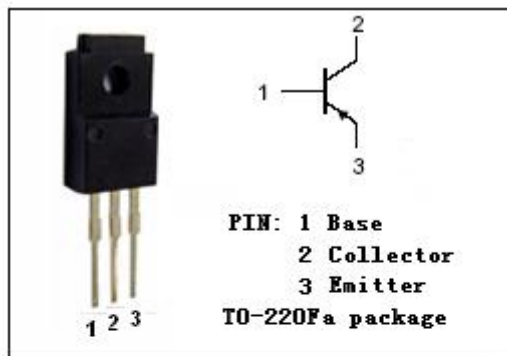
- Low Collector Saturation Voltage-
: $V_{CE(sat)} = -1.0V(Max) @ I_C = -1A$
- Good Linearity of h_{FE}
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for power amplifications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-1	A
I_{CM}	Collector Current-Peak	-2	A
P_C	Collector Power Dissipation @ $T_a=25^{\circ}C$	2	W
	Collector Power Dissipation @ $T_c=25^{\circ}C$	30	
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



isc Silicon PNP Power Transistor**2SB954****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -30mA; I _B = 0	-60			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1A; I _B = -0.125A			-1.0	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -1A; V _{CE} = -4V			-1.3	V
I _{CEO}	Collector Cutoff Current	V _{CE} = -30V; I _B = 0			-300	μ A
I _{CES}	Collector Cutoff Current	V _{CE} = -60V; V _{BE} = 0			-200	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-1	mA
h _{FE-1}	DC Current Gain	I _C = -0.2A; V _{CE} = -4V	70		250	
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -4V	15			

◆ h_{FE-1} Classifications

Q	P
70-150	120-250

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